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HEP software validation on ARM: status and next steps



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ICSC Italian Research Center on High-Performance Computing,
Big Data and Quantum Computing

Missione 4 • Istruzione e Ricerca

Current status

The plan in view of MS8 (end of phase I)

- First period (tentatively month 13-22 - aligned with MS8): procure and configure ARM machines in order to provide access to the experiment software and storage via a production infrastructure; select and document workflows to be benchmarked from the most representatives; **prepare a validation strategy agreed with the experiments.**



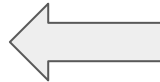
A specific document was provided with MS8 report

ARM validation strategy (TAR2.15)

The goal of the "Validation of event reconstruction code on ARM" flagship use case is to provide the hardware and the software infrastructure to enable a validation of the software used by experiments to process data and Monte Carlo (MC), at least for two major LHC experiments, using resources from the ICSC datalake and in particular hosted at the INFN CNAF Tier-1 facility.

Describing 3 use cases
for phase II:

- ALICE
- CMS
- ATLAS



Current status (II)

For all the experiments considered there are two main areas where validation can run:

- Full simulation (MC)
- Data reconstruction

Both cases are explored in all the 3 cases

Where we are now... $\frac{2}{3}$ experiments are already in an advance status:

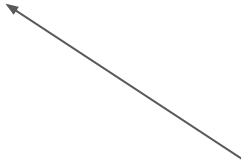
- ALICE (to be done)
- CMS (already advanced)
- ATLAS (ready to go)

Recent presentations at workshops and conferences

2024 reports:

- INFN-CCR 2024 workshop (Palau), L. Rinaldi et al., [“Utilizzo di risorse con architetture ARM negli esperimenti di LHC”](#)
- Congresso SIF 2024 (Simposio supercalcolo, Bologna), F. Noferini et al., [“Uso di nodi ARM nella fisica delle alte energie”](#)
- CHEP 2024 (Kracow), D. Lattanzio et al., [“Heterogeneous computing at INFN-T1”](#) →23/10/2024

Part of this talk will focus on activities at CNAF with ARM



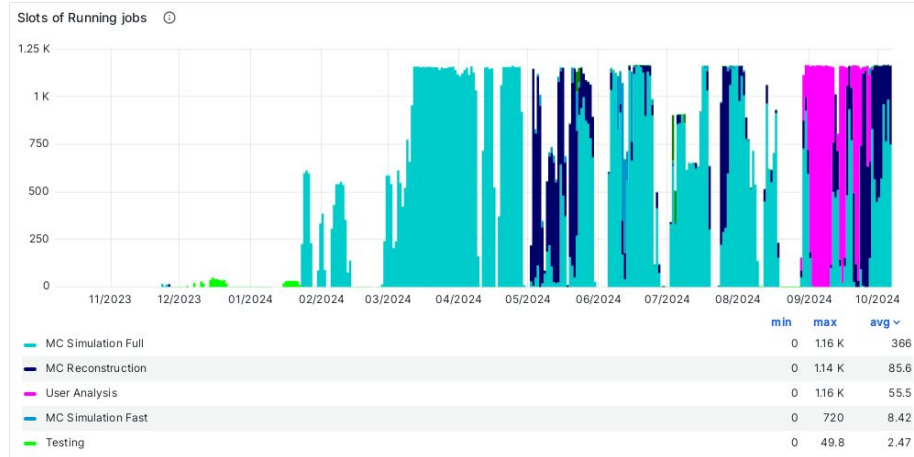
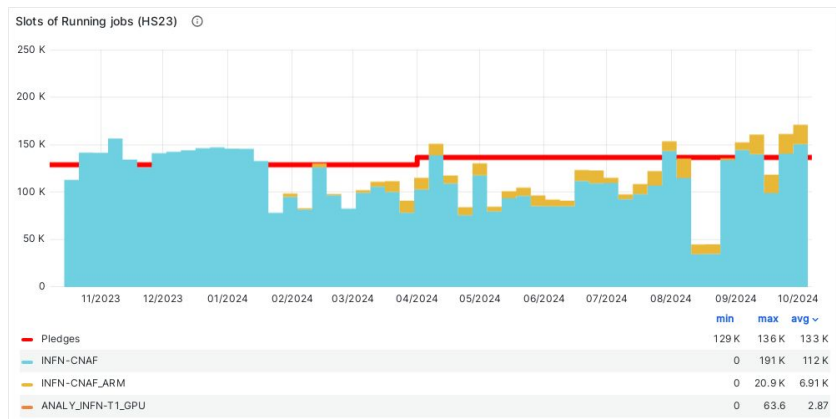
Milestones

- First period (ICSC month 13-22 - aligned with MS8): procure and configure ARM machines in order to provide access to the experiment software and storage via a production infrastructure; select and document workflows to be benchmarked from the most representatives; prepare a validation strategy agreed with the experiments.
- Second Period (ICSC month 23-36 - aligned with MS10): test and validate the selected workflows (most probably from data reconstruction and simulation); validate the submission infrastructure and perform $O(1 \text{ week})$ exclusive tests as needed by the validation strategy. Report the results to the experiments and in the ICSC documentation; disseminate the results at topical conferences.

We are now in phase II

ATLAS has been running jobs on ARM@CNAF for one year

- initially test jobs
 - ATLAS Software already Physics-validated on ARM
 - Technical validation performed at CNAF (HTCondorCE, pilot/PanDA, containers)
- workflows:
 - Full and Fast MC Simulation
 - MC reconstruction
 - Group production
 - User Analysis



Very good performance observed

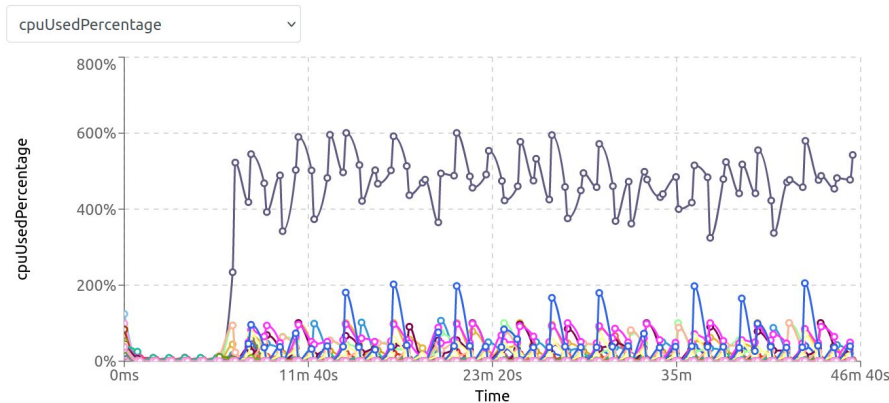
- steady use of available resources:
 - ~ on peak: 12% of ATLAS-dedicated resources at INFN-T1

Acknowledgment:
ARM Flagship (use-case) in CN-HPC - Spoke2

ALICE

Preliminary tests showed good balancing in using resources but still some instability in ARM builds didn't allow to run a validation so far.

- Data reconstruction



- GRID setting tuned for 8 cores per node
- CPU efficiency consistent with what observed in the GRID node
- Physics validation on the output not yet done

Performance test in 2023 @E4 showed good performance both in data and simulation (full MC)

Recent builds on Almalinux9 presented some issues (mainly fixed) → tests are going to be resumed

GRID submission @CNAF (GRID submission + aptainer container) → validated

Outlook and future plans (not requested in the validation strategy)

- An important piece is missing: measurement of **power consumption**
 - Define a strategy for the measurements and a good metric (e.i. evnt/sec/watt ?)
- The validation is on the way..
 - LHCb is performing test on Glasgow resources, CNAF will be used in next phase for DIRAC provisioning
- The use case would enable production workflows on ARM at the INFN CNAF center, a Tier-1 in the WLCG hierarchy and a node of the ICSC datalake
- On top of this, a successful validation would pave the way to more communities (and in particular smaller HEP and Astro experiments)
- In perspective, the path could enable the Italian Computing Infrastructure (INFN and ICSC, in this particular case) **to provision ARM machines in the near future**, with a sizable reduction of computing TCO.

backup

People involved in this use case

Participating Institutions

- Leader: INFN (Francesco Noferini, Daniele Spiga, Tommaso Boccali, Lucio Anderlini, Concezio Bozzi)
- Participants: INFN, UNIBO
- Experiments: ALICE (F. Noferini), CMS (D. Spiga, T. Boccali), ATLAS (L. Rinaldi, L. Carminati), LHCb (L. Anderlini, M. Veltri)

KPI

KPIs

| KPI ID | Description | Acceptance threshold |
|------------|---|---------------------------|
| KPI2.2.5.1 | Software validation on ARM in the full GRID chain | 50% (2/4 LHC experiments) |
| KPI2.2.5.2 | Presentation at conferences | ≥ 2 |
| KPI2.2.5.3 | Technical notes (in experiments and ICSC) | ≥ 2 |

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Available resources @ CNAF



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- Resource procurement started in the second half of 2023 when CNAF acquired two ARM nodes allowing the experiments to logging on those machines and running preliminary tests. By the end of 2023 CNAF acquired two additional nodes and setup a GRID-HTCondor queue.
- 4x 2U Dual socket ARM – Ampere Altra Max
 - 2x Ampere AltraMax M128-30 128Core 2.8Ghz 250W
 - 1TB RAM
 - 2x NVMe U.3 3.84TB discs
 - 1x Dual port SFP28 ethernet

Current setting (still work in progress)

- Cvmfs available
- Network: access to external network
- Gpfs client -> now available on ARM but only with a very recent version, not yet in production at INFN-T1
- Condor/GRID -> in production

