

Introductory course to VHDL

Report of Contributions

Contribution ID: 1

Type: **not specified**

Introduction to FPGA

Monday, 4 March 2024 09:30 (3 hours)

Recording:

<https://unipd.zoom.us/rec/play/cJ3fVW8hwi00R-TMptDlIBAAJSGds2zfSXCpQ5Xmqz1MgpXG8Th0JetEV2YGu4fq2j2gSxn4>

Passcode: 8wk+d8@C

Presenter: TRIOSI, Andrea (Universita' degli Studi di Padova)

Contribution ID: 2

Type: **not specified**

Introduction to VHDL

Monday, 4 March 2024 14:30 (3 hours)

Recording:

https://unipd.zoom.us/rec/play/QJKP7o6JYIIWxNcFdD83JVfFlkfEAbnPxi2amnU13cd-jykA_YorSuLMwLnDE3N0rJ1droBsz

Passcode: .8P%Xtsu

Presenter: TRIOSI, Andrea (Universita' degli Studi di Padova)

Contribution ID: 3

Type: **not specified**

Introduction to VHDL

Tuesday, 5 March 2024 09:30 (3 hours)

Recording:

https://unipd.zoom.us/rec/play/8ac-Uyvy13H3Xjv5xPLQlfHW88ZRNPq6qgtSz-59Zz6Vyo2WodRv_Roudu2GKvqZ0fe-EAwm7UAcUhm9.Mzza-F2uWQNNZf9N

Passcode: t45fHF#P

Presenter: TRIOSI, Andrea (Universita' degli Studi di Padova)

Contribution ID: 4

Type: **not specified**

Exercises

Tuesday, 5 March 2024 14:30 (3 hours)

Recording:

<https://unipd.zoom.us/rec/play/IyKRmFMjrKR9NTHKPgNlmlOVksQlnqKymE8CKgvpAs-YtHQOutNt9ReD28voC5J2tWkx.gDpDHnIx0VXDLjNE>

Passcode: RiMH\$S6V

Contribution ID: 5

Type: **not specified**

Exercises

Wednesday, 6 March 2024 09:30 (3 hours)

Recording:

https://unipd.zoom.us/rec/play/2A9YMjxIOdL19_vZTkjnl72u4EWmv7FWCP1muGH39Ypu_sYV4bgHogA1lu1l5GIUdzG2

Passcode: rh6*X@g

Presenter: TRIOSI, Andrea (Universita' degli Studi di Padova)

Contribution ID: 6

Type: **not specified**

Exercises

Wednesday, 6 March 2024 14:30 (3 hours)

Recording:

https://unipd.zoom.us/rec/play/m-cdeAw0p0pvpr6EBcTs-I_bIXlA327gZI1kiWZTjZNVdfQmIY9FZZciUfEVMKd9tjJ9nK_EJ

Passcode: jY7zg!Ls