

# Giessen proposed WP within Jennifer3

- Machine learning on FPGA
- DAQ (not trigger)  
anomaly detection, “smart filter”, data mining of large amounts of data and “find the needle in the haystack” or “blind discovery”
- PXD related (so we keep PXD from Jennifer1 and Jennifer2)
- VERSAL, recent Xilinx FPGA platform specific for ML  
I asked Yamada-san if we could define a common project (KEK also has a VERSAL board)  
Board purchased (16.200,- Euro),  
arrived 2 days ago at Giessen

