The RPC-based proposal for the ATLAS forward muon trigger upgrade

Junjie Zhu University of Michigan

On the behalf of the ATLAS Muon Collaboration

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ATLAS muon spectrometer

OPrecision tracking chambers:

 Muon Drift Tube (|η| < 2) , Cathode Strip Chamber (2 < |η| < 2.7) \Box Trigger chambers:

Resistive Plate Chamber (|η|<1.05) and Thin-Gap Chamber (1.05<|η|<2.4)

ATLAS trigger at L1

The RPC: Low-p_T trigger (RPC1 + RPC2) High-p_T trigger (+RPC3) \Box TGC: Low-p_T trigger (TGC2 + TGC3) High-p_T trigger (+TGC1) \Box A road represents an envelope containing the trajectories, from the origin, of muons of either charge with a p_T above a given threshold ■Geometrical acceptance: 80% for RPC and 95% for TGC 3

Problems with high p_T **muon triggers in endcaps**

Current Endcap Trigger

- \Box Only a vector BC at BW is measured
- Momentum defined by implicit
- assumption that track originated at IP
- \Box Random background tracks can easily fake this condition
- ~30% resolution at L1 for 20 GeV muons □ Fake tracks and worse momentum
- resolution \rightarrow large L1 trigger rate that will be difficult to handle

Proposed Trigger

- \Box Provide a vector A at new small wheel (NSW)
- \Box Use the deflection angle between A and BC
- to determine muon p_{τ}
- \Box Powerful constraint for real tracks
- ~95% of events triggered by MU20 endcap triggers do not have associated inner tracks \Box With pointing resolution of 1 mrad, NSW will also improve p_T resolution (15~20% for 20 GeV muons) and sharpen the trigger turn-on curve

Expected rates at sLHC w/ and w/o NSW

~90% of events triggered by TGC triggers for L1_MU20 Current L1 muon trigger bandwidth is 15-20 kHz at ATLAS **Goal:** to keep L1_MU20 unprescaled under the sLHC conditions

Proposed multi-gap RPC + small-tube MDT detector

 \rightarrow Dedicated tracking detector (sMDT) and trigger detector (mRPC) \rightarrow Deal with 14 kHz/cm² uncorrelated hits (at $R = 1m$, 0.3 mm spatial resolution at 1st trigger level, 3000 fb⁻¹ and \sim 1 C/cm² \rightarrow spatial position resolution ~0.3 mm \rightarrow 1 mrad angular resolution \rightarrow Mounted on MDT chambers and make it easier for relative alignment \rightarrow Combine SW segments with TGC segments from BW to determine muon p_T at L1

Improvements needed w.r.t. the present ATLAS RPC

Q Resistive (bakelite) plates: thickness (2 mm \rightarrow 1 mm), volume resistivity (2 - 3×10^{10} Ω cm \rightarrow 0.5 - 1×10¹⁰ Ω cm), surface quality etc

- **Q** Gas gap: 2 mm \rightarrow < 1 mm
- **Q** Detector structure: single-gap \rightarrow multi-gap (bi-gap considered so far)
- \Box Readout strips: $1.5 2$ mm pitch
- \Box Front-end readout electronics: higher sensitivity and better signal to noise ratio

Excellent timing capability is crucial

Number of hits as a function of the arrival time within one LHC BX (25 ns) with TOF subtracted

Proposed detector layout

□ Fully exploit the excellent RPC timing resolution to reject backgrounds with high efficiency (see details from G. Chiodini's talk for current ATLAS RPC timing) Main principle of the trigger scheme (quote R. Santonico's words):

 \rightarrow Remove as soon as possible and as much as possible fake hits

 \Box Each RPC layer: fast timing cut to remove uncorrelated hits (f_{μ} : 8% \rightarrow 50%) □ Each RPC station: 2 out of 3 coincidence to remove uncorrelated hits and retain high efficiency ($f_{uncorrelated}$: 30% \rightarrow 0%) \Box Two RPC stations: Track pointing to the IP within certain angles to remove backgrounds from other charged particles (f_{μ} : 50% \rightarrow 100%)

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Designed parameters for ATLAS NSW

Advantages of the proposed trigger strategy

 \Box Unambiguous identification of Bunch Crossing ID

 \Box Simple on-chamber pattern recognition

■ Small amount of information sent to combine with track segments found by the BW TGCs and the coincidence can be done using FPGAs in the counting room avoiding high radiation

 \Box Significant safety margin as excellent timing will be useful to prevent unexpected things that may happen at high luminosity

Mean timer Circuit
Shown for simplicity as 16 steps but 32 is proposed (390ps steps)

 The coincidence of two contiguous detector layers with parallel strips and front end electronics at the same end is time walk free \rightarrow mean timer with 250 ps timing resolution equipped on both ends of the readout strips

 \Box Allows the coincidence with Bunch Crossing with sub-nanosecond resolution Allows to localize the coincidence between contiguous layers to a very small area (\sim 4 cm²) to give an extremely strong rejection power for uncorrelated hits \Box \sim 15 cm resolution achieved using ATLAS MDT electronics (0.78 ns resolution), investigating new electronics with 150 ps resolution (expect \sim 3cm)

 \Box Can also be used to determine the hit position by exploring the fact that strips 12

Sensitive front-end readout electronics

- The new sensitive front-end electronics allows a new working mode with a factor 10 less of charge per count
- \Box Used to find the maximum of the RPC charge distribution

See details from R. Cardarelli's talk

- Selector \Box \Box \Box \Box N strips are processed at the same time
	- \Box The circuit amplifies the inputs and provides the output only for the strip above a settable fractional threshold, normalized to the average charge provided
	- \Box The threshold is chosen to have one or two strips firing (cluster size 1 or 2)
	- \Box The decoder transforms the simple digital pattern in to a number representing the hit coordinate on the chamber

Beam test setup at CERN

See details from L. Han's talk

Test beam results

Rate capability test at GIF (CERN)

Rate capability test at GIF (CERN)

Efficiency as a function of HV for the bi-gap RPC

Rate test is limited by the available source flux in GIF $\frac{17}{17}$

Conclusions

 \Box Propose to upgrade the ATLAS SW detector with a combined tracking detector (using sMDT) and trigger detector (using mRPC) at the sLHC \Box Need to deal with 14 kHz/cm², 0.3 mm spatial resolution at 1st trigger level, 3000 fb⁻¹ and \sim 1 C/cm²

■ Main principle of the trigger scheme: Remove as soon as possible and as much as possible fake hits, and then send all information to the trigger processor

 \Box Thin bi-gap RPCs with high efficiency per detector unit

 \Box 1[~] 2 mm pitch strips to achieve 0.3 mm resolution

 Mean-timer circuit on both ends to remove uncorrelated background hits

 \Box Sensitive front-end electronics to deal with smaller charge and improve the signal to noise ratio

□ Ongoing studies with RPC prototypes, front-end electronics, trigger algorithm and ageing (see details from G. Aielli's talk)

Backup

Energy distributions

Current parameters for RPC in the ATLAS barrel region

