



# TREDI 2024

## Tuesday, 20 February 2024

### Electronics - Aula Magna (16:30 - 18:00)

| time  | [id] title  | presenter         |
|-------|---|-------------------|
| 16:30 | [18] Test results of the monolithic ASIC for the upgraded preshower detector of the FASER experiment at the LHC | MAGLIOCCA, Chiara |
| 16:50 | [8] The ASIC FAST3, a front-end integrated circuit optimized to read out 50 $\mu\text{m}$ LGAD sensors          | FERRERO, Marco    |
| 17:10 | [27] ToASt : a 64-channel ASIC for the readout of the Silicon Strip Detectors of the PANDA experiment           | MAZZA, Giovanni   |
| 17:30 | [29] Tests and Characterization of ASTRA-64 ASIC for Silicon Micro-Strip Detectors Read-Out                     | BARBANERA, Mattia |