



ALICE



# APTS OpAmp Activities

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# Experimental Setup

**Chip:** w22AO10AP44

**Power supply:** Rohde&Schwarz HAMEG HMP4040

**Inner px readout:** Rohde&Schwarz RTO1044

(BW: 4GHz Sampling Rate: 20GSa/s)

**Outer pixels:** ADC

**Script for testing:**

*apts/opamp\_gain.py*

*apts/opamp\_pulsing.py*

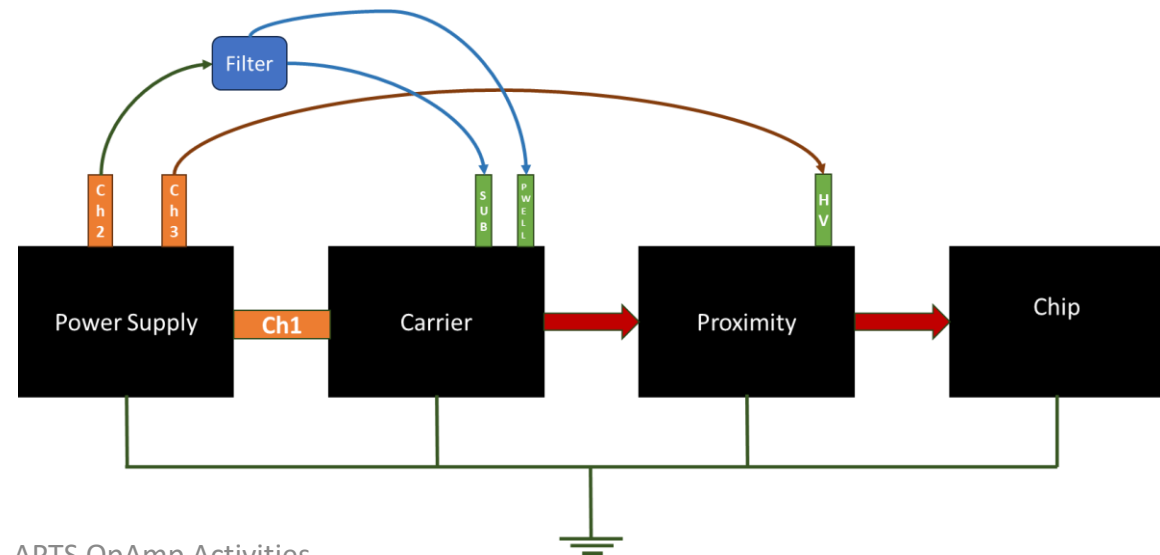
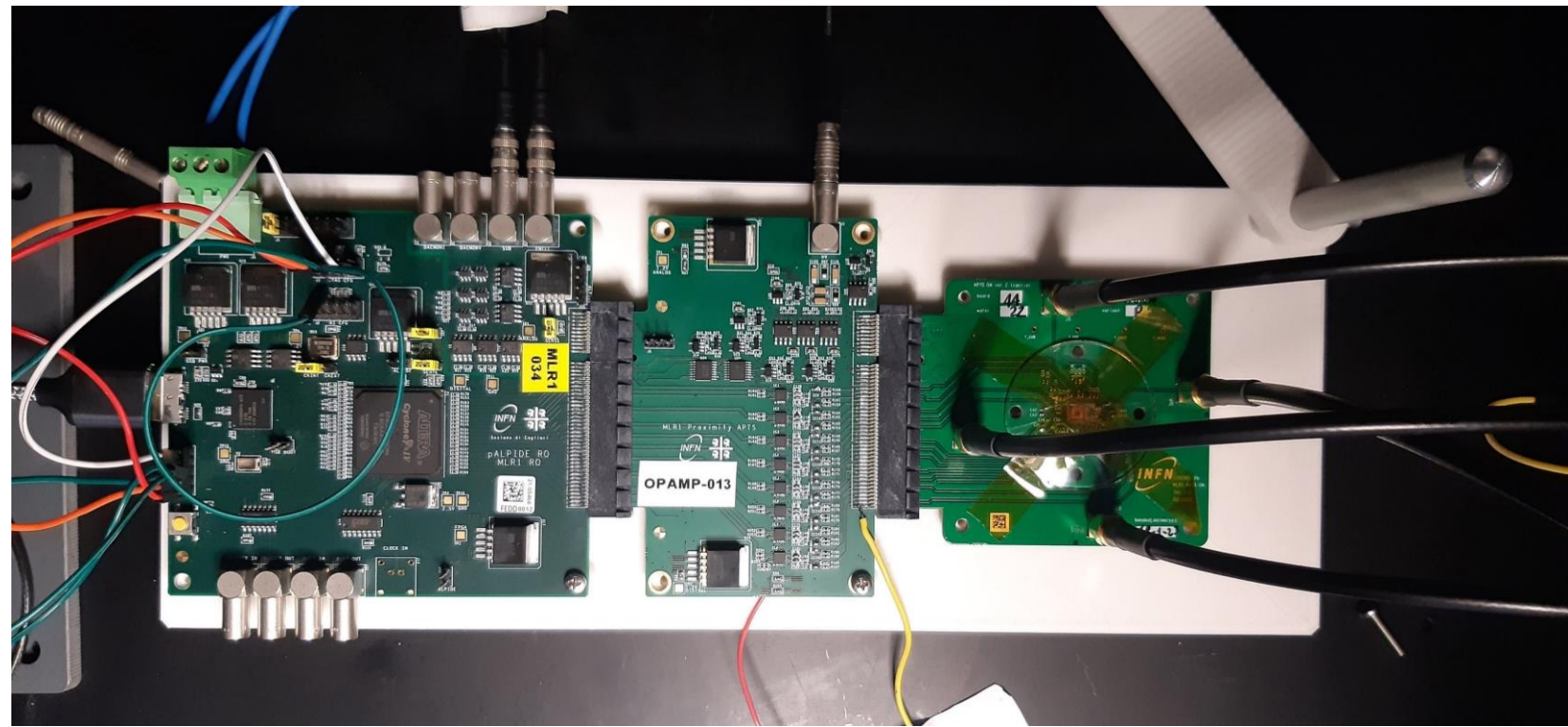
*apts/opamp\_vhscan.py*

**Bias:**

HV : 0÷20V

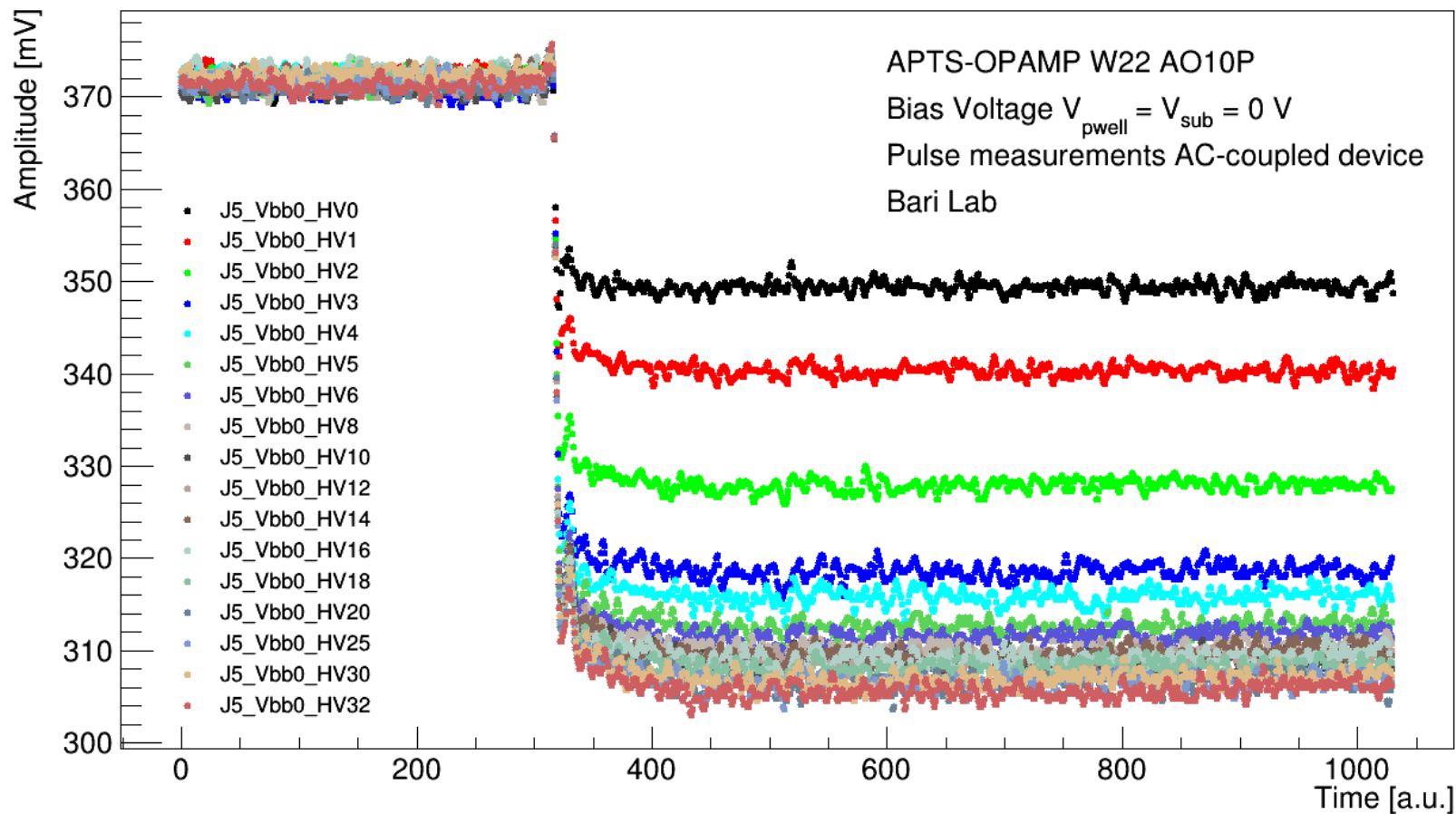
$V_{sub}$  : 0 , -0.6V

$V_{pwell}$  : 0V

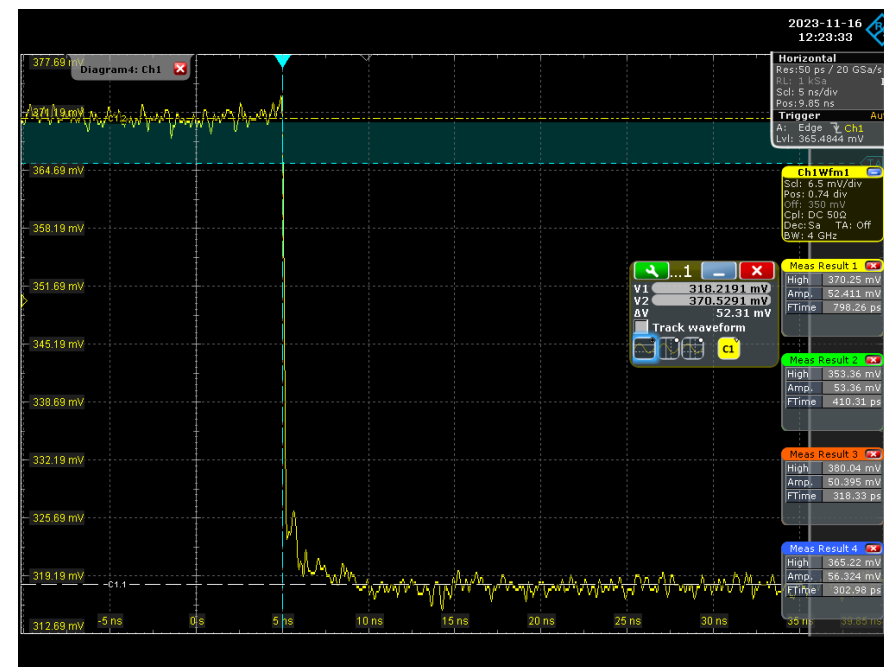


# W22AO10APb44 AC Coupled Chip Inner pixel signal output

Wfa overlapped Pixel J5

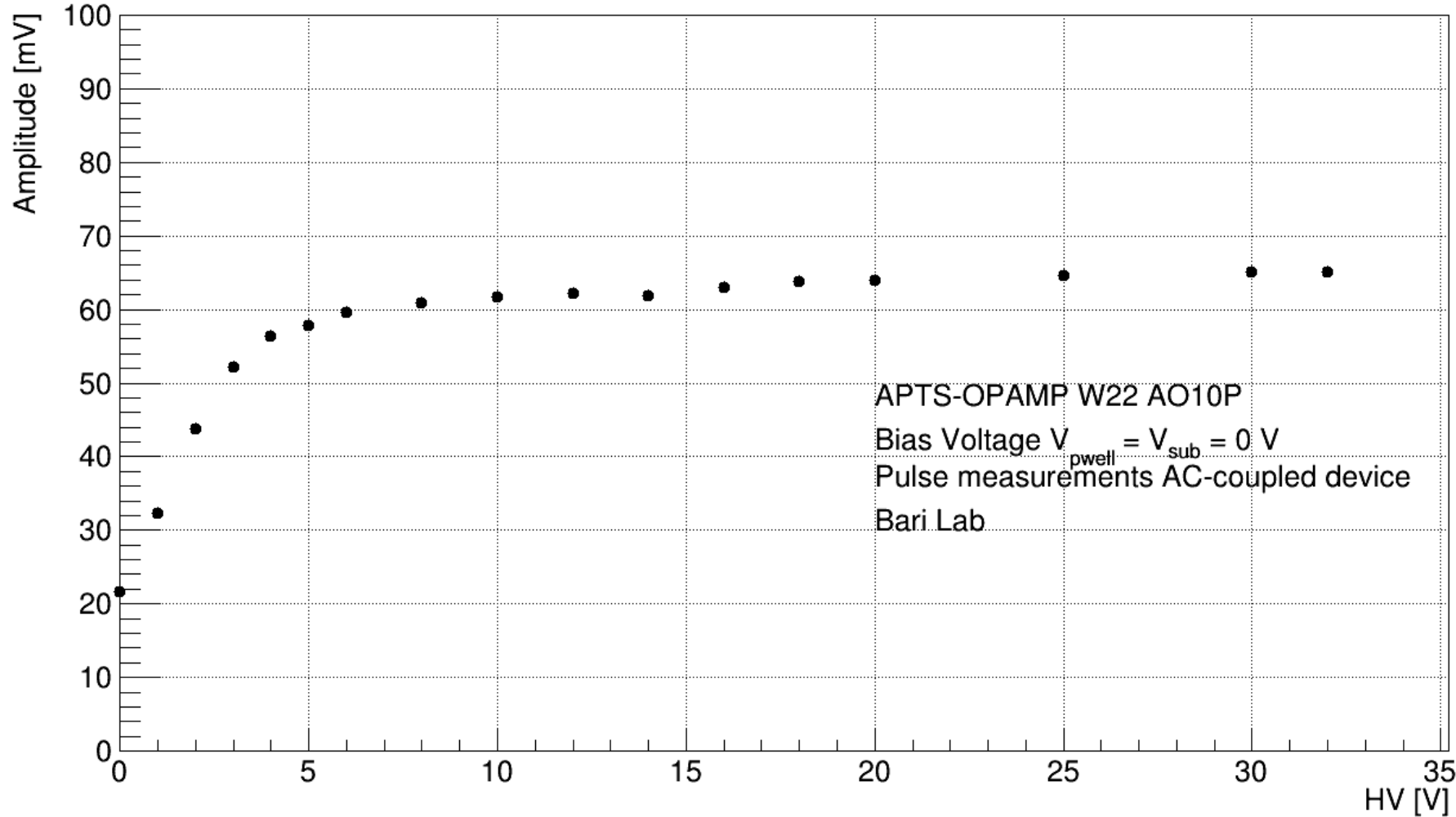


single signal acquired on the scope



# W22AO10APb44 AC Coupled Chip Amplitude vs HV

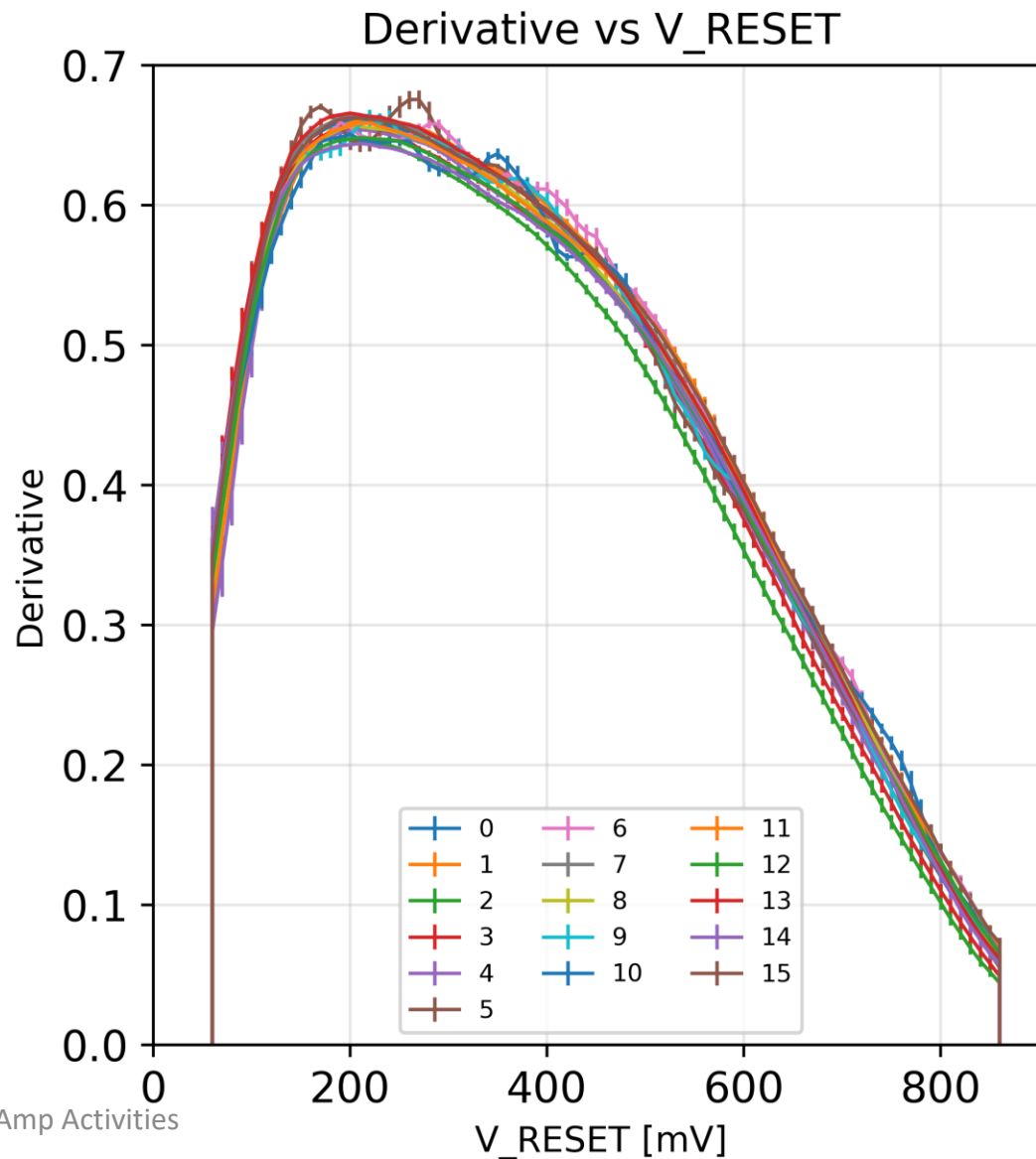
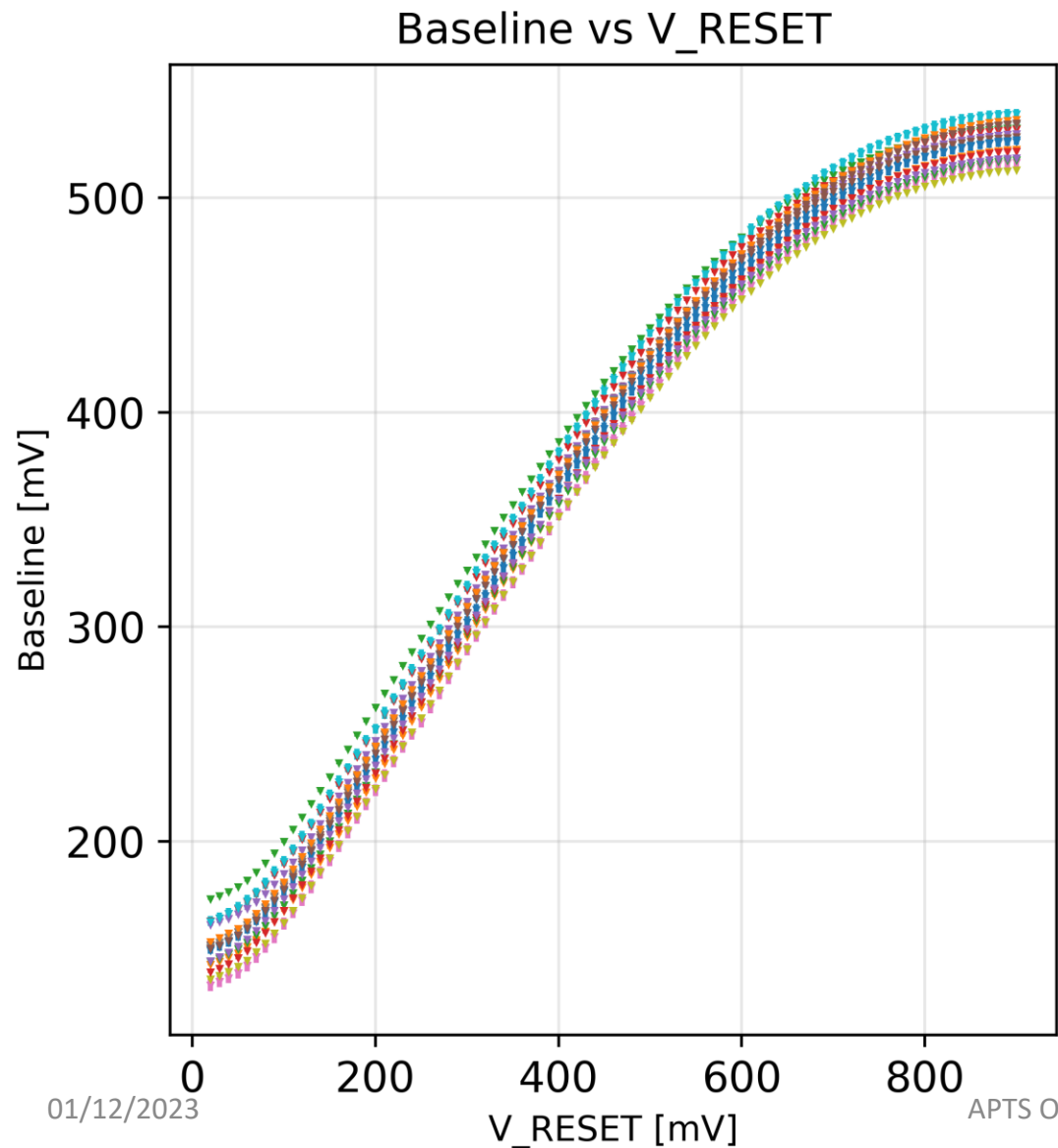
## Amplitude Vs HV Pixel J5



Saturation at increasing HV clearly visible

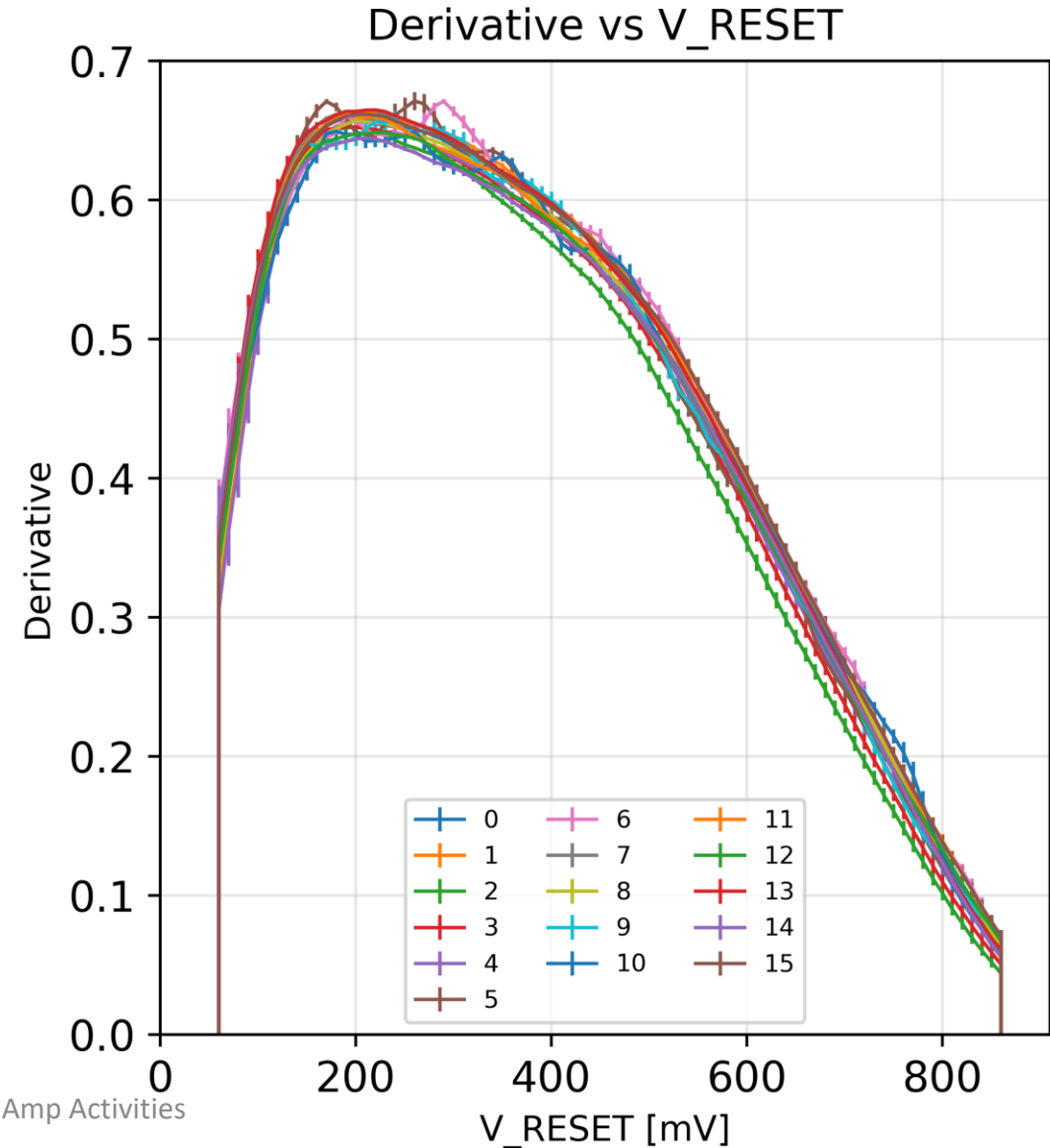
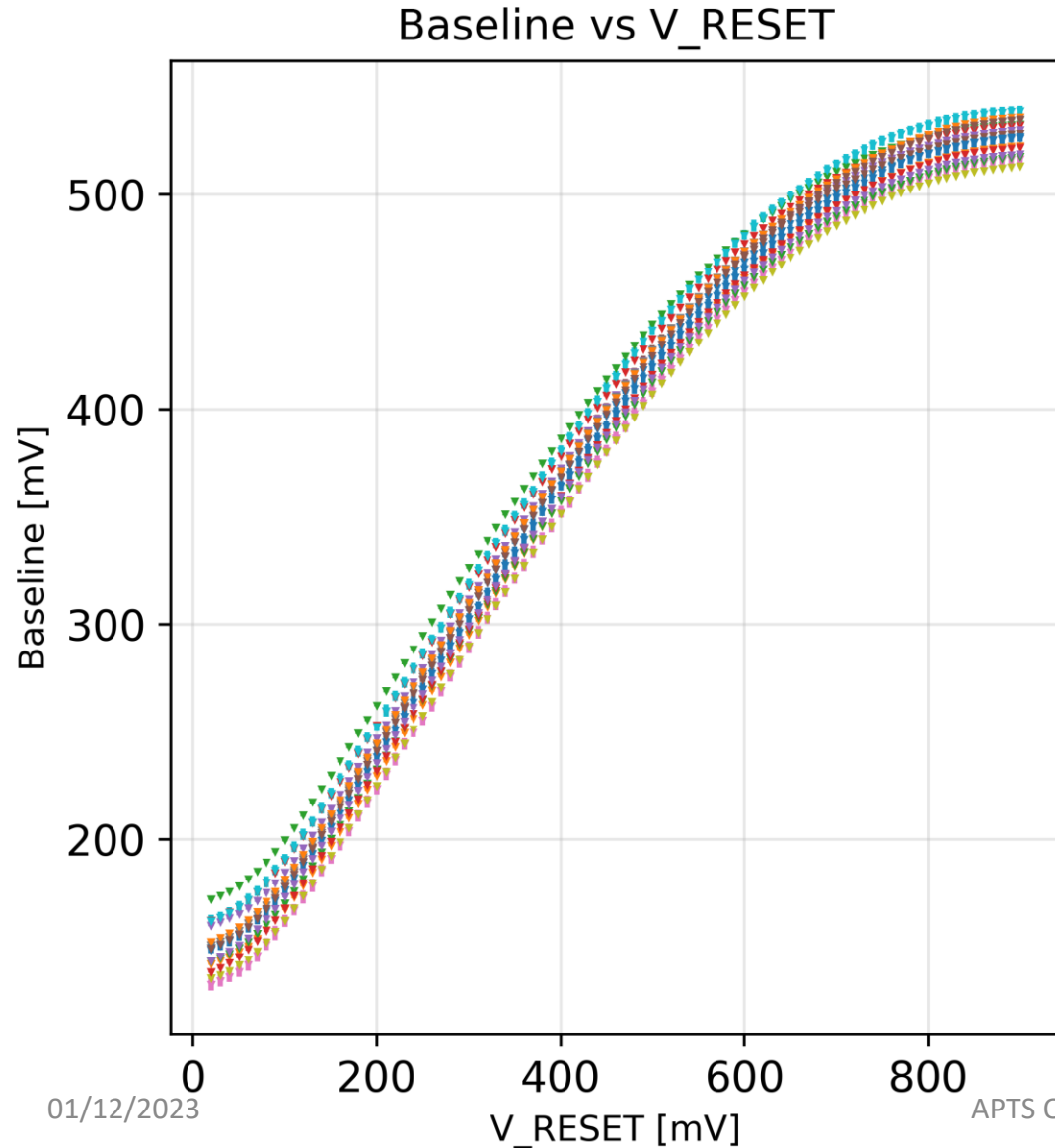
# W22AO10APb44 AC Coupled Chip $V_{\text{sub}} = 0\text{V}$ $V_{\text{pwell}} = 0\text{V}$ HV = 20V

## Gain Calibration



# W22AO10APb44 AC Coupled Chip $V_{\text{sub}} = -0.6\text{V}$ $V_{\text{pwell}} = 0\text{V}$ $\text{HV} = 20\text{V}$

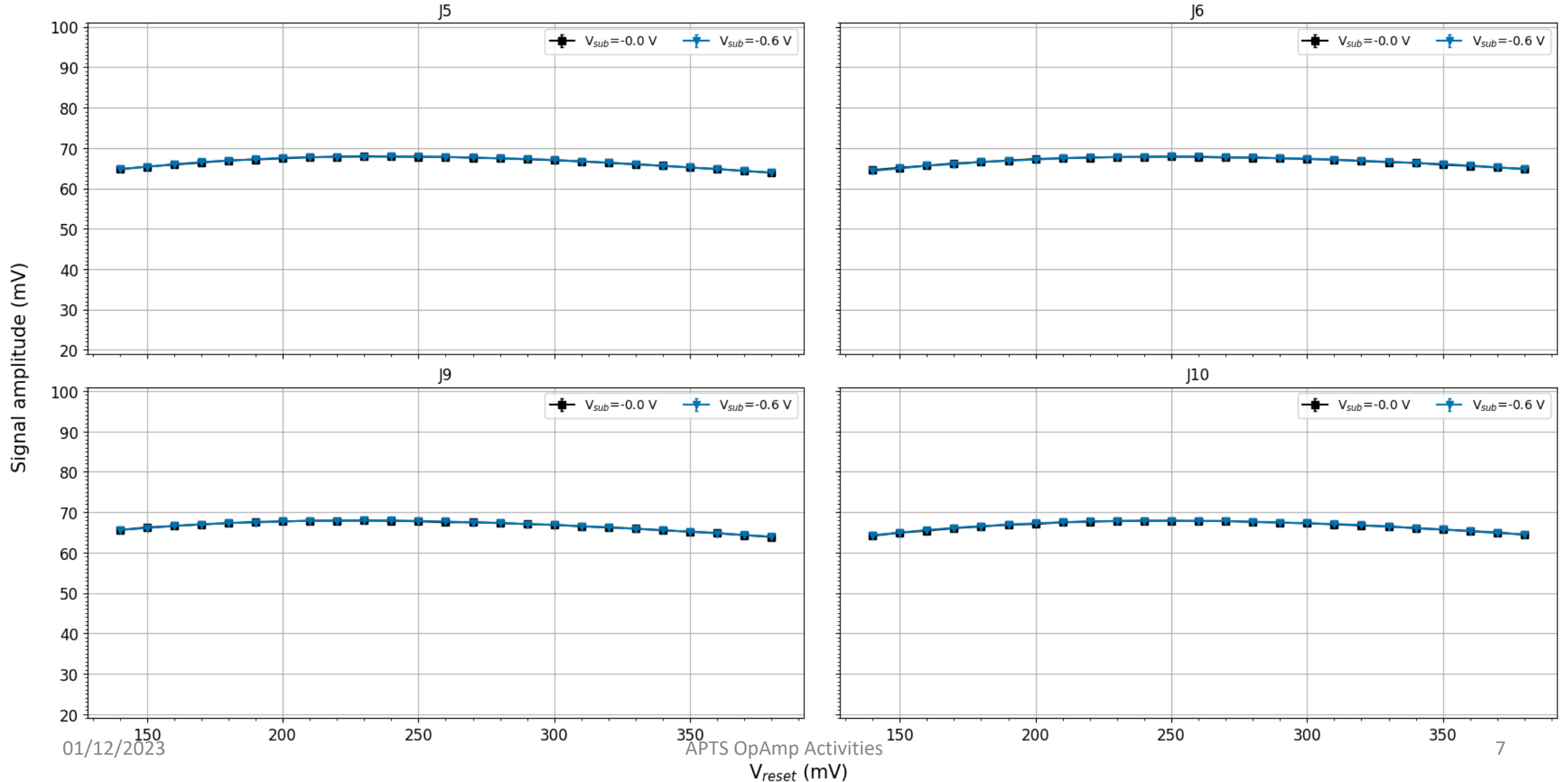
## Gain Calibration



# W22AO10APb44 AC Coupled Chip $V_{sub} = 0; -0.6V$ $V_{pwell} = 0V$ $HV = 20V$

Signal amplitude

## Pulsing: Signal Amplitude



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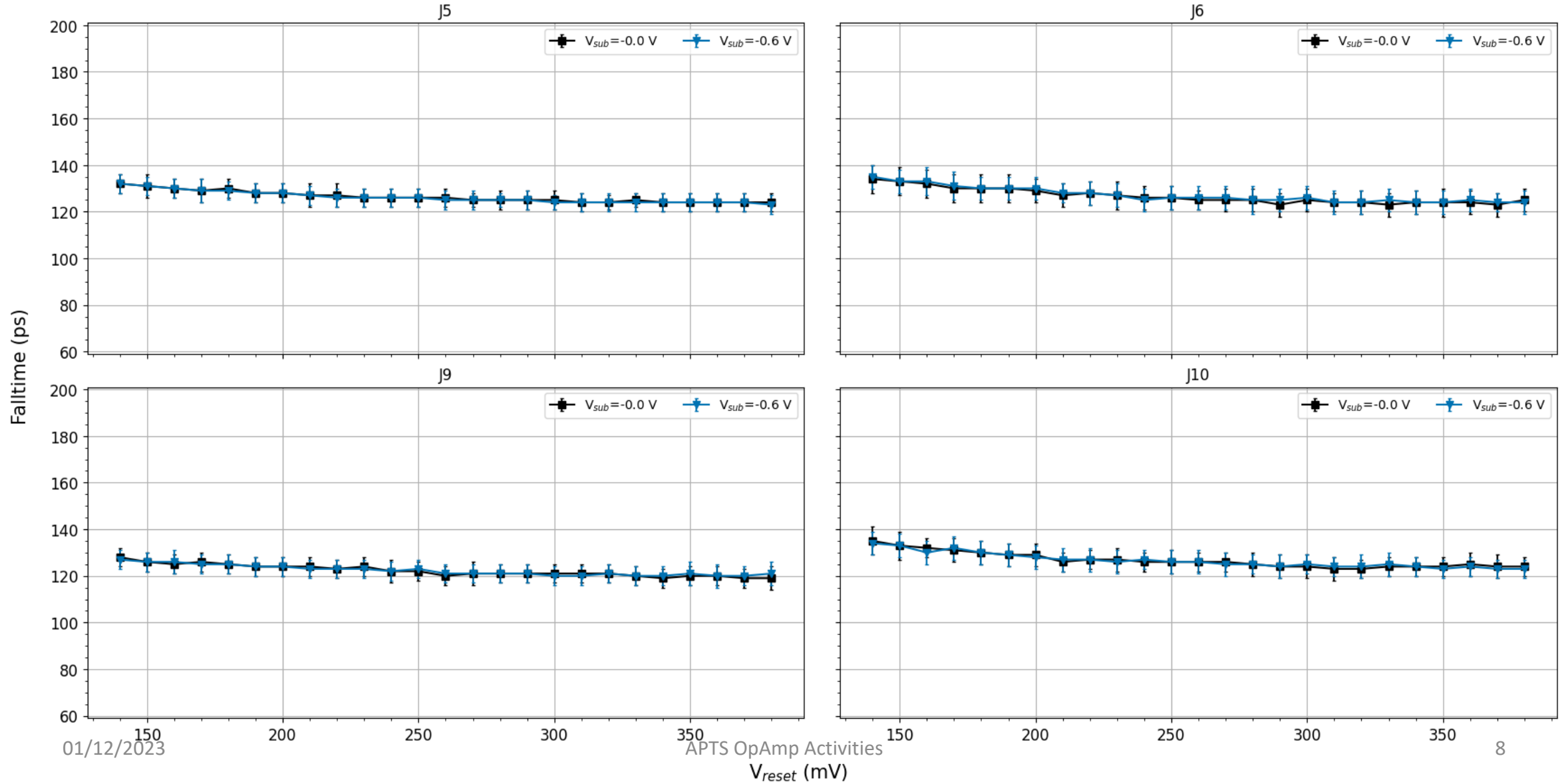
$V_{reset}$  (mV)

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# W22AO10APb44 AC Coupled Chip $V_{sub} = 0; -0.6V$ $V_{pwell} = 0V$ $HV = 20V$

Falltime 10%-50%

Pulsing: Falltime 10 50



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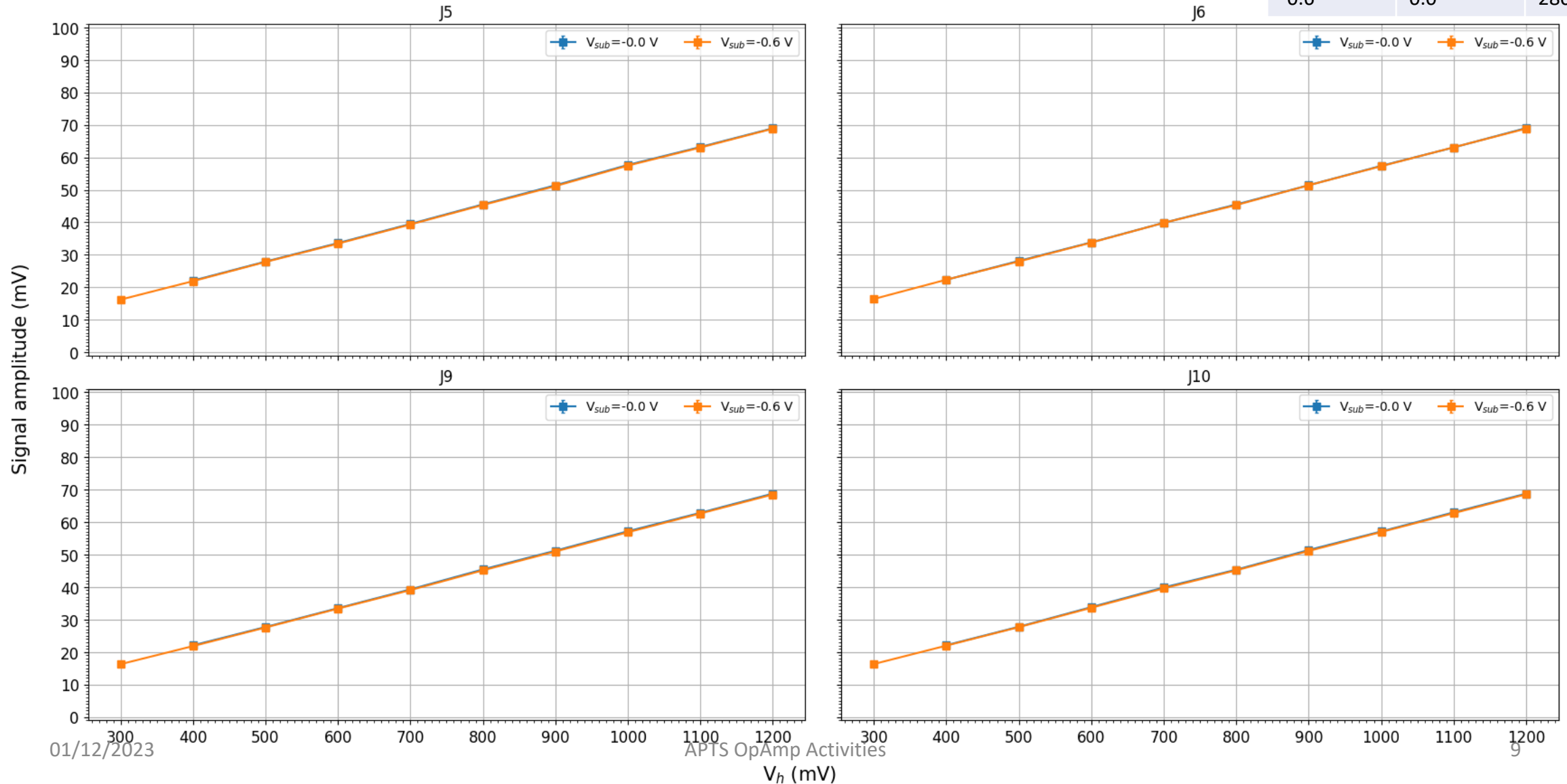
# W22AO10APb44 AC Coupled Chip $V_{sub} = 0; -0.6V$ $V_{pwell} = 0V$ $HV = 20V$

Working Point

| $V_{sub}$ (V) | $V_{pwell}$ (V) | $V_{reset}$ (mV) |
|---------------|-----------------|------------------|
| 0.0           | 0.0             | 270              |
| -0.6          | 0.0             | 280              |

## Vh scan: Signal Amplitude

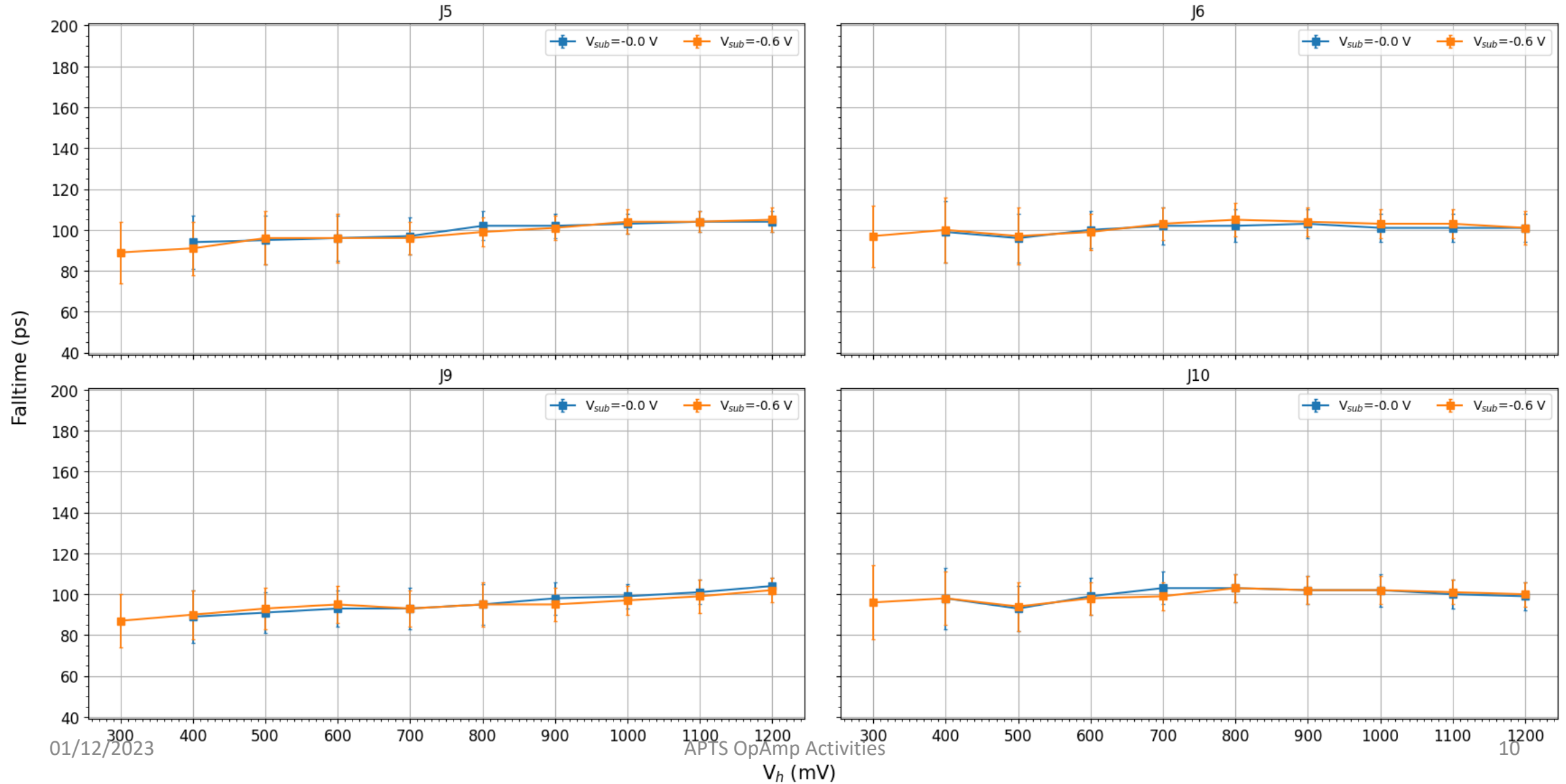
Signal amplitude



# W22AO10APb44 AC Coupled Chip $V_{sub} = 0; -0.6V$ $V_{pwell} = 0V$ $HV = 20V$

Vh scan: Falltime 10 50

Falltime 10%-50%



01/12/2023

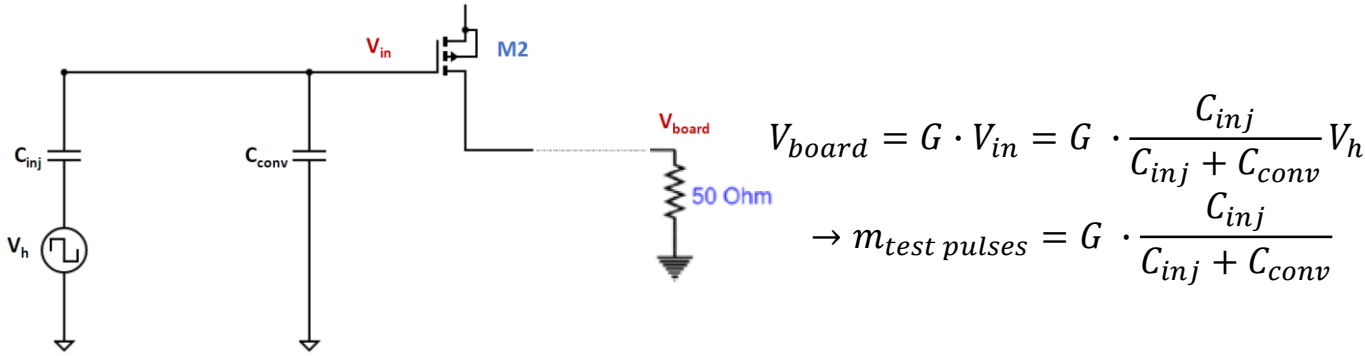
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# Equivalent OpAmp circuit: Injection and Conversion Capacitance

- Test pulses: W22AO10Pb18 DC Coupled Chip**

- $V_h$  charges the capacitance that pulses the collection electrode
- $V_h$  scan at fixed chip operation condition has been done

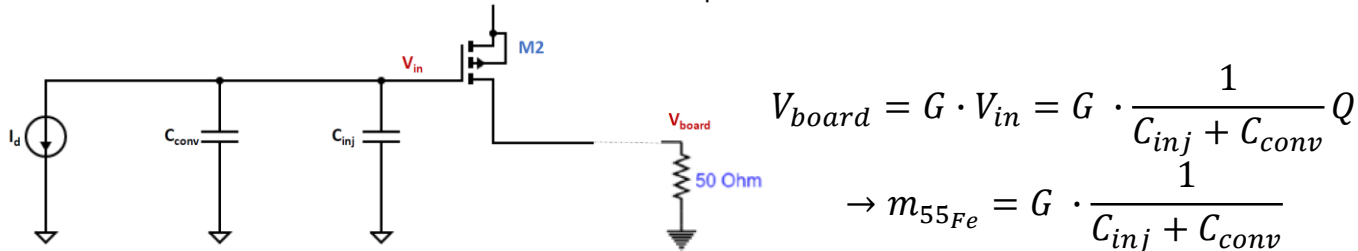


$$V_{board} = G \cdot V_{in} = G \cdot \frac{C_{inj}}{C_{inj} + C_{conv}} V_h$$

$$\rightarrow m_{test\ pulses} = G \cdot \frac{C_{inj}}{C_{inj} + C_{conv}}$$

- $^{55}\text{Fe}$  Radioactive source:**

- A particle/photon producing a charge Q that induces a current  $I_d$
- Mn- $k_\alpha$  @ 5.9keV  $\rightarrow$  1638 $e^-$  and Mn- $k_\beta$  @ 6.5keV  $\rightarrow$  1804 $e^-$  peaks as calibration points

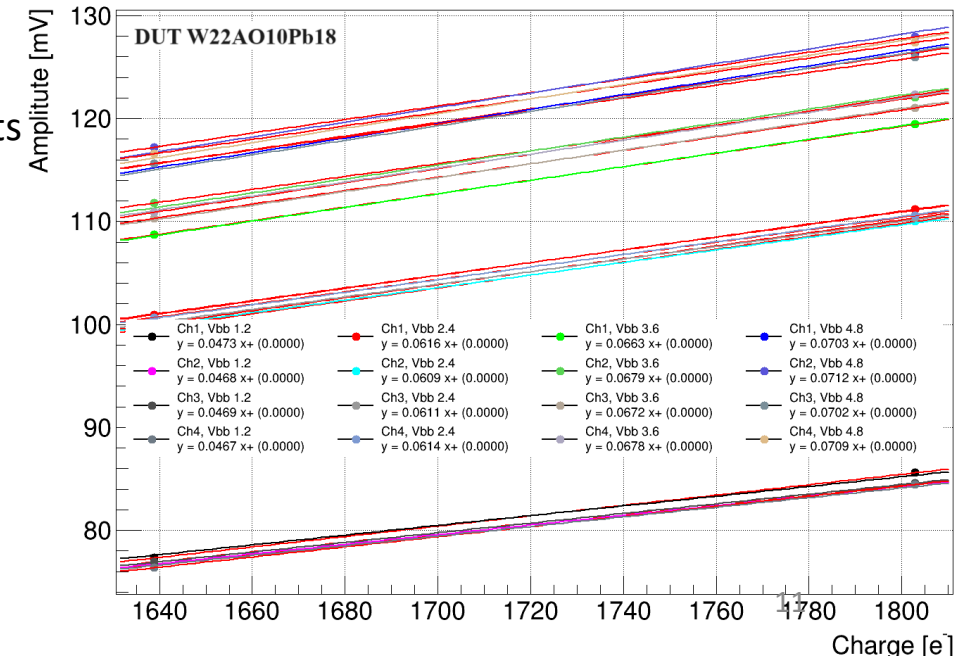
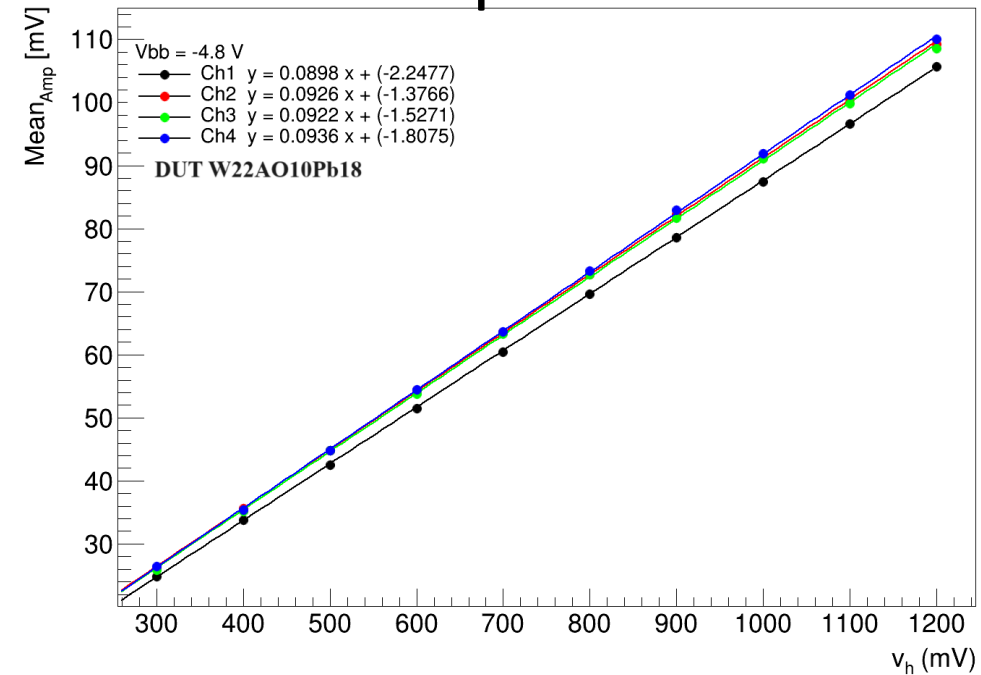


$$V_{board} = G \cdot V_{in} = G \cdot \frac{1}{C_{inj} + C_{conv}} Q$$

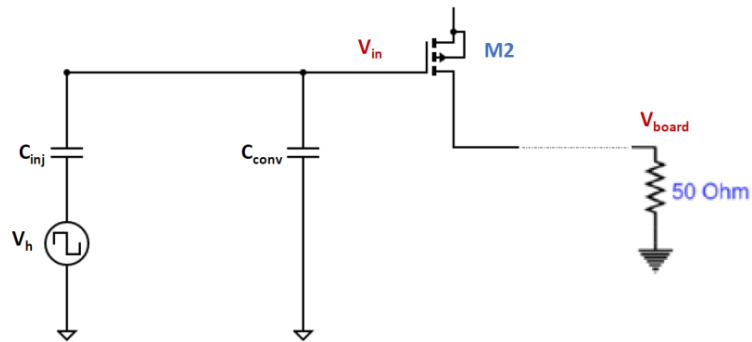
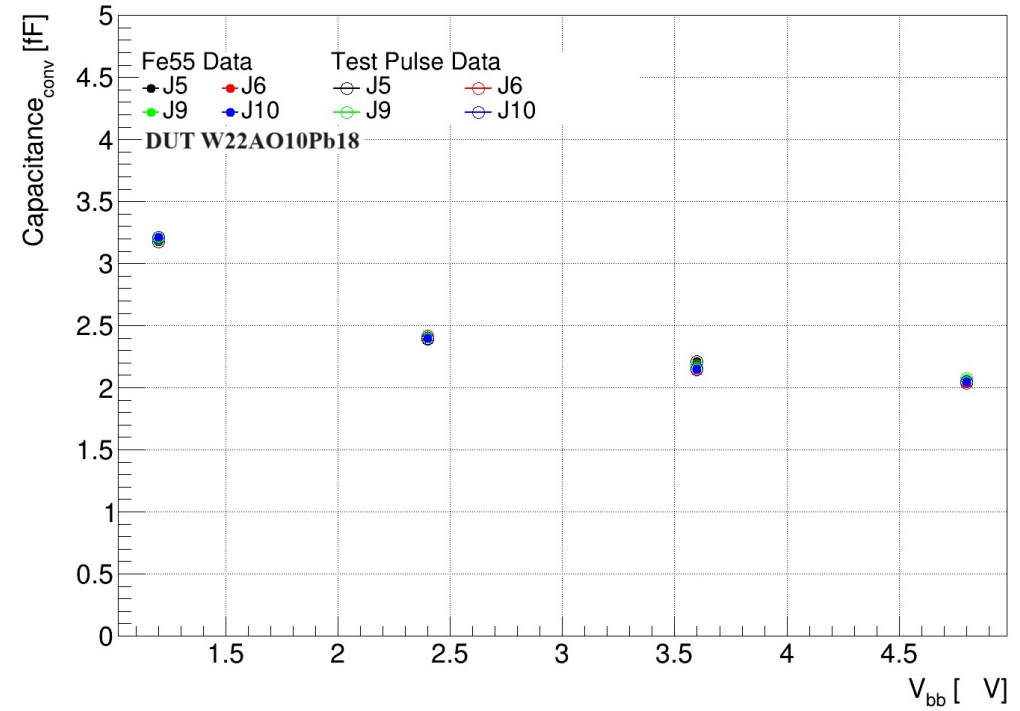
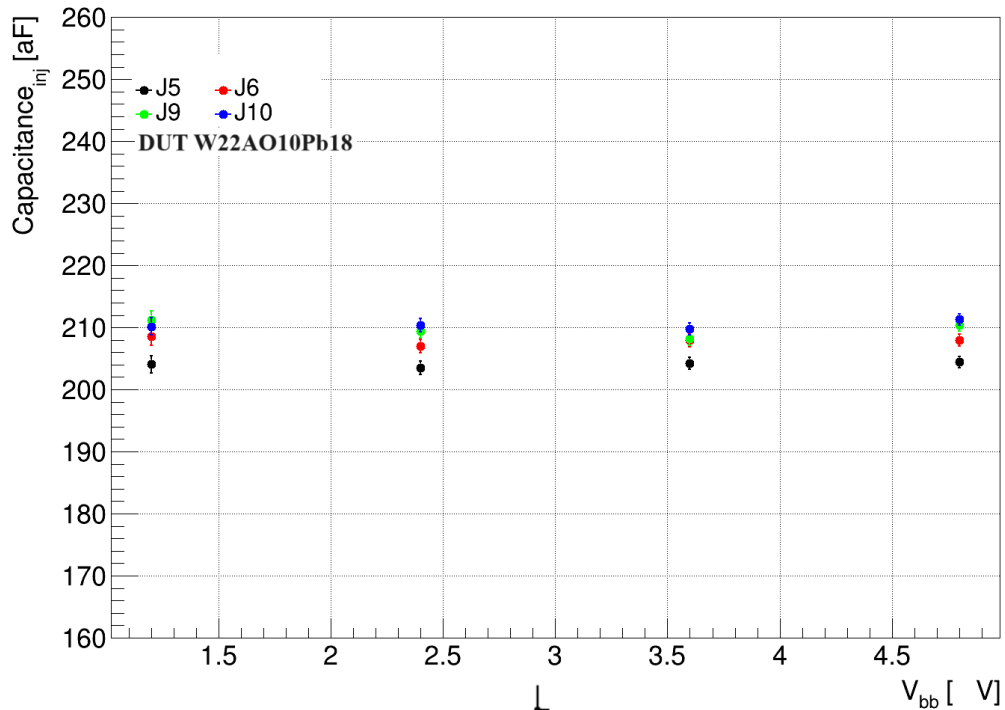
$$\rightarrow m_{55Fe} = G \cdot \frac{1}{C_{inj} + C_{conv}}$$

A linear behaviour should be observed in both case thus:

$$C_{inj} = \frac{m_{test\ pulses}}{m_{55Fe}} \text{ APTS OpAmp Activities}$$



# APTS OpAmp Injection and Conversion capacitance measurements



The  $V_{bb}$  influences the capacitances as expected.

$$C_{inj} = \frac{m_{test\ pulses}}{m_{55Fe}}$$

$$m_{55Fe} = G \cdot \frac{1}{C_{inj} + C_{conv}}$$

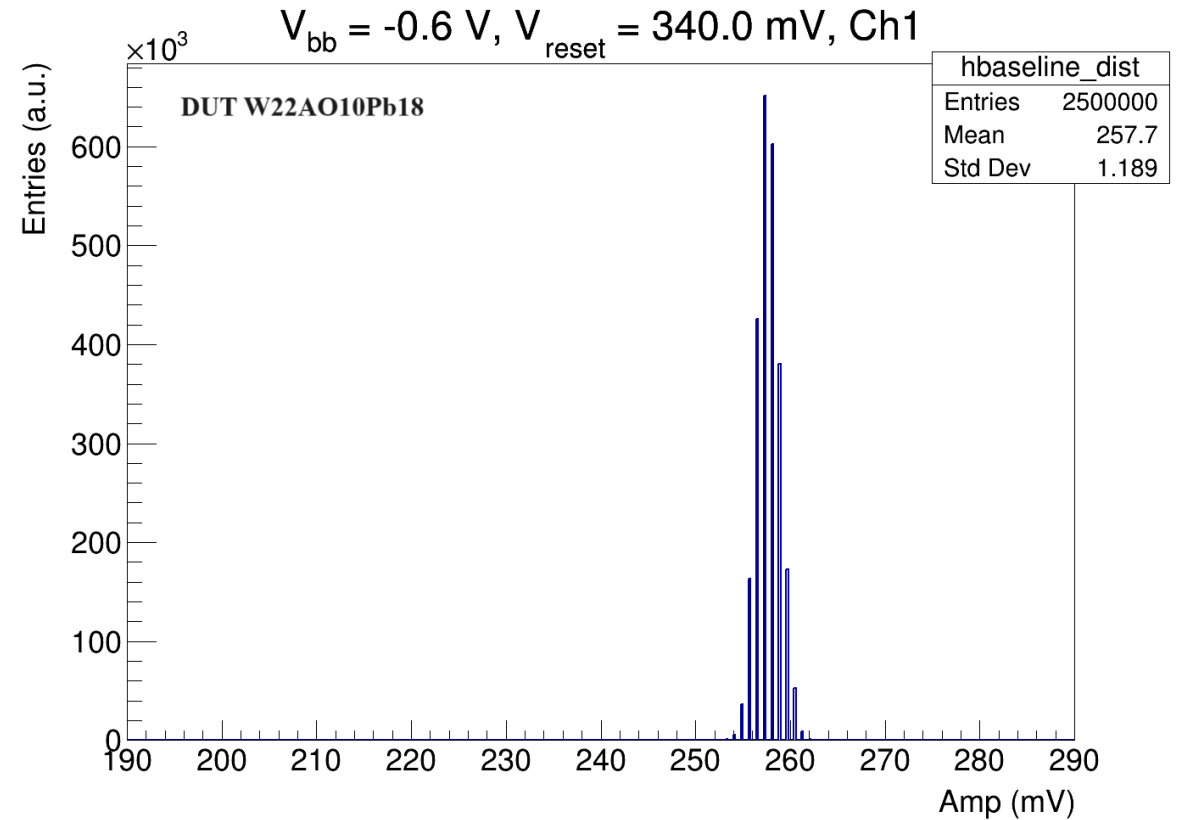
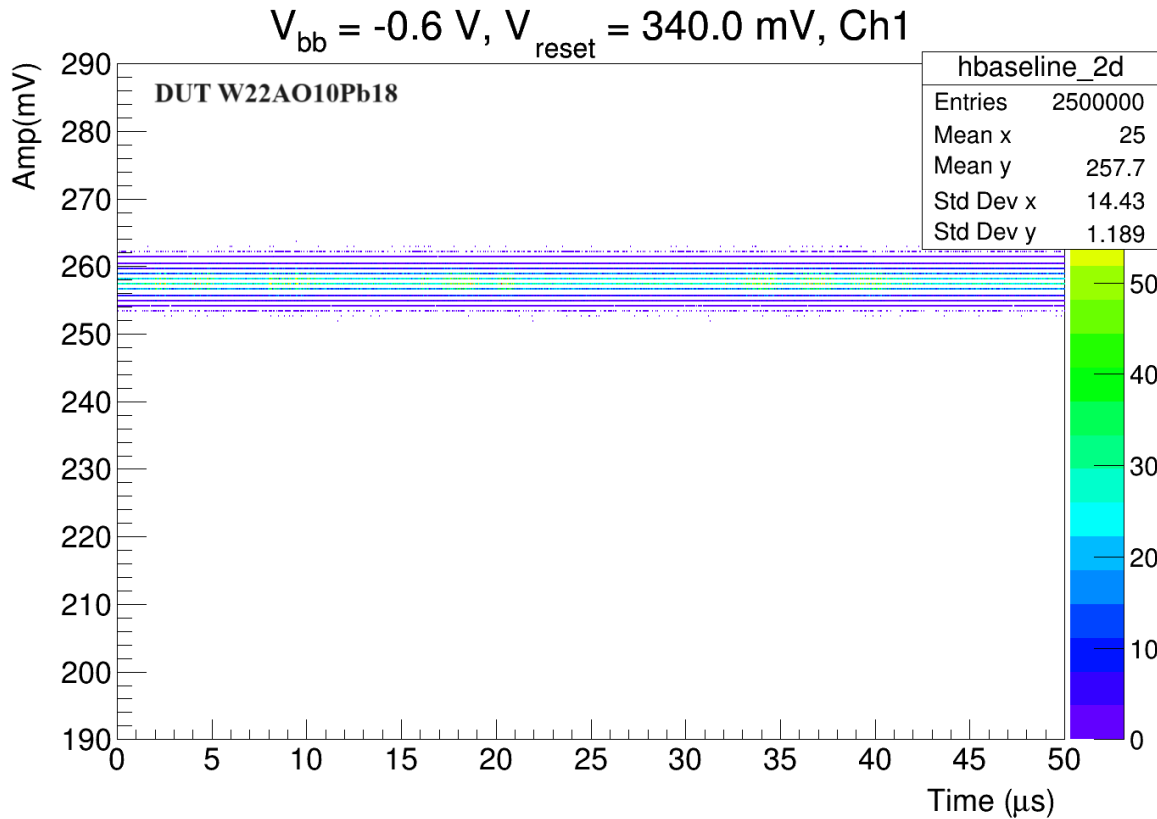
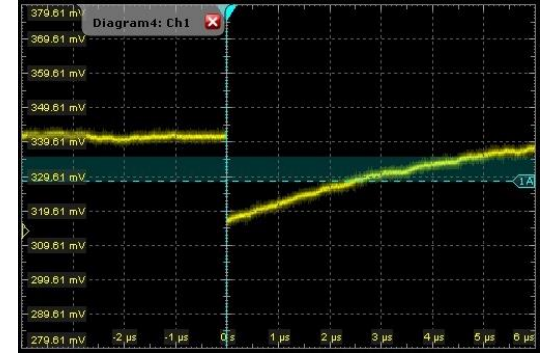
$$\rightarrow C_{conv} = \frac{G}{m_{55Fe}} - C_{inj}$$

$$m_{test\ pulses} = G \cdot \frac{C_{inj}}{C_{inj} + C_{conv}}$$

$$\rightarrow C_{conv} = \left( \frac{G}{m_{test\ pulses}} - 1 \right) \cdot C_{inj}$$

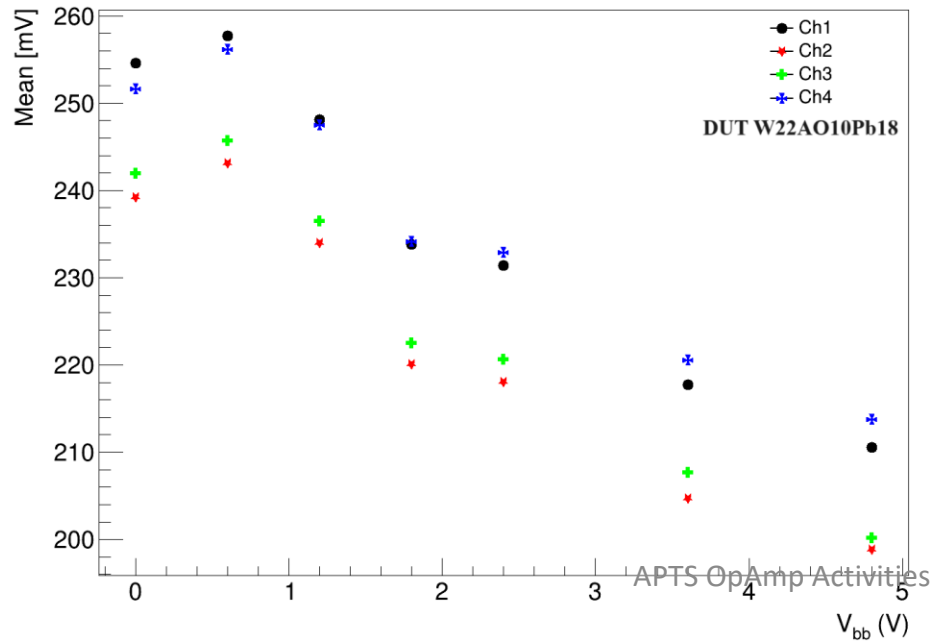
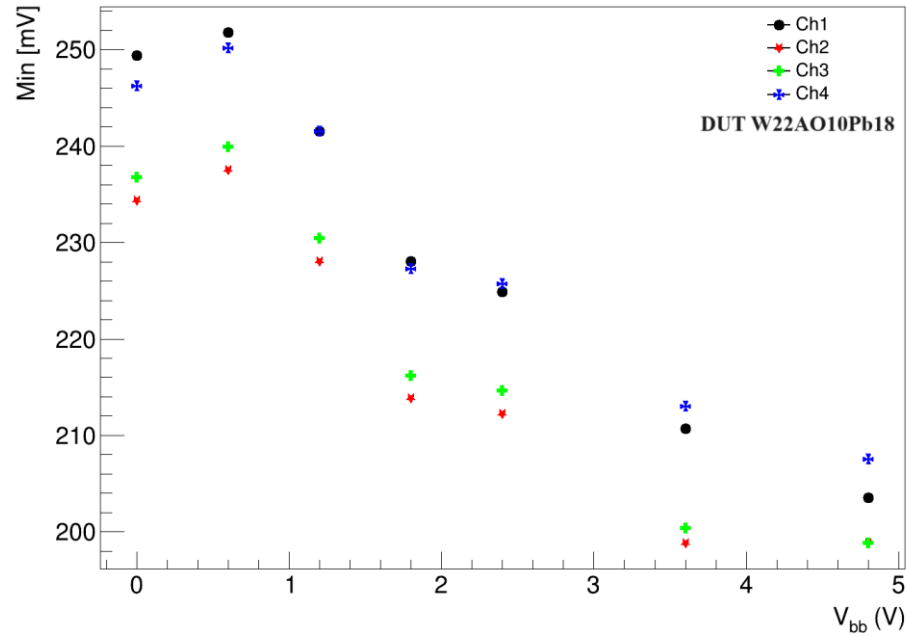
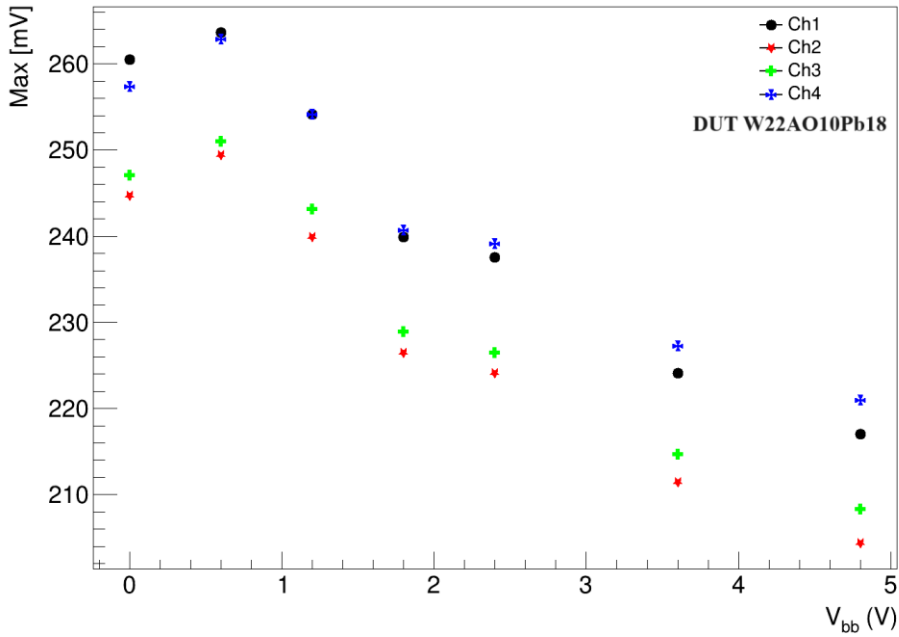
# APTS OpAmp Baseline studies for noise estimation

- Fluctuation of baseline is observed.
- Further investigations in progress.

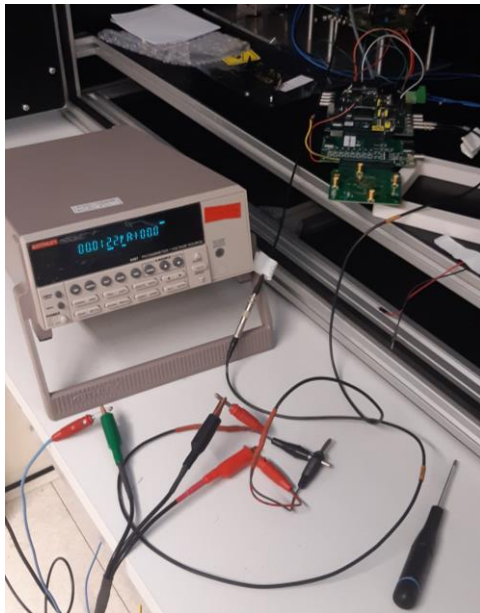
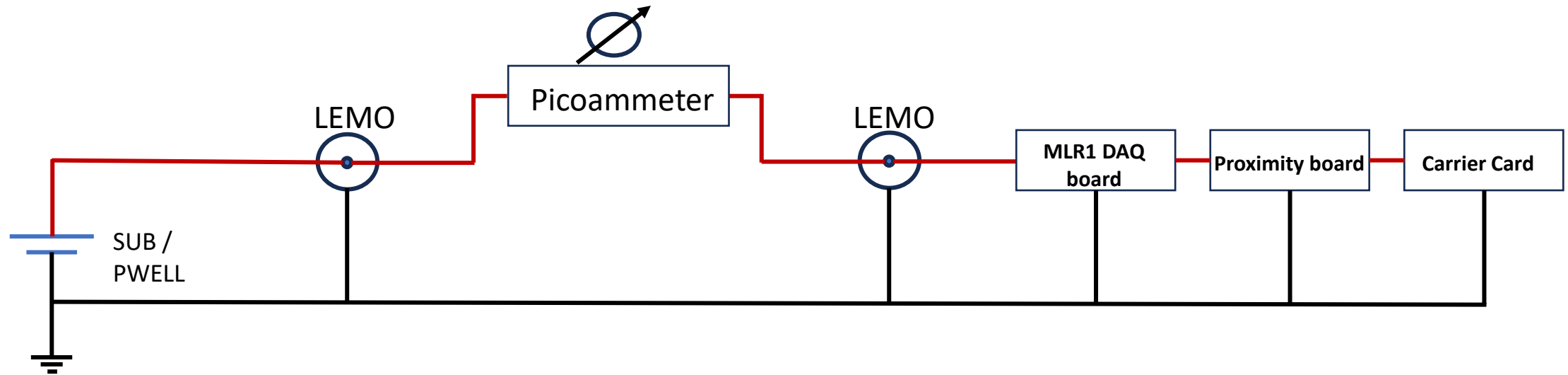


# APTS OpAmp Baseline studies for noise estimation

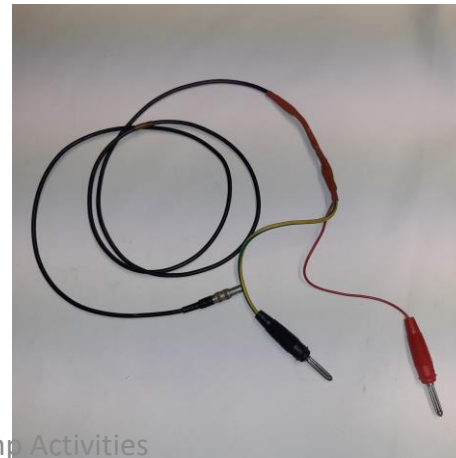
|    |    |    |    |
|----|----|----|----|
| 0  | 1  | 2  | 3  |
| 4  | 5  | 6  | 7  |
| 8  | 9  | 10 | 11 |
| 12 | 13 | 14 | 15 |



# APTS OpAmp Experimental Setup for SUB and PWELL I-V measurements

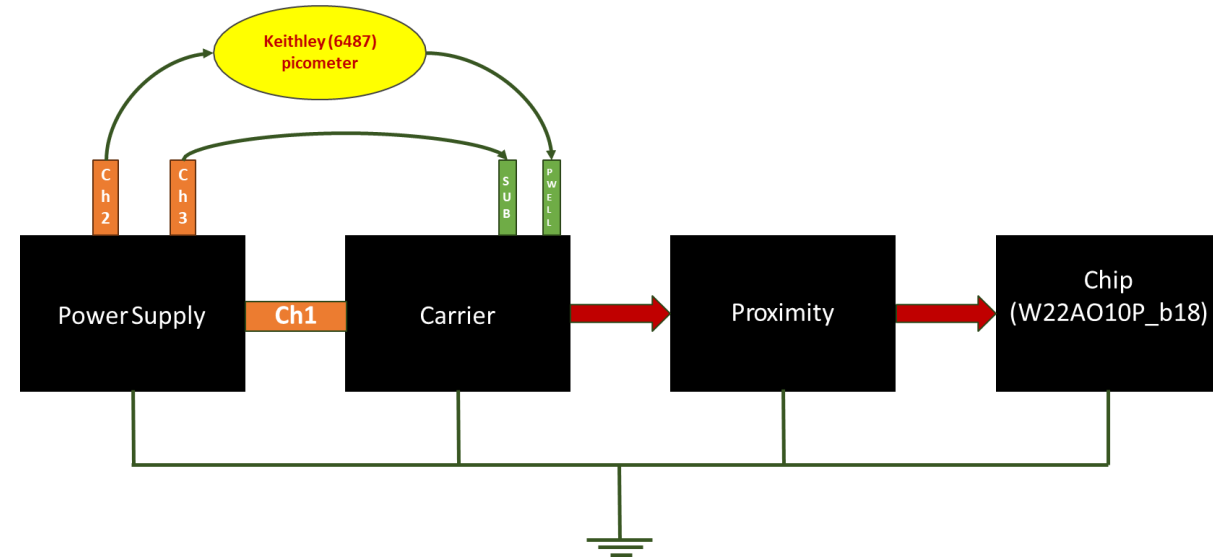
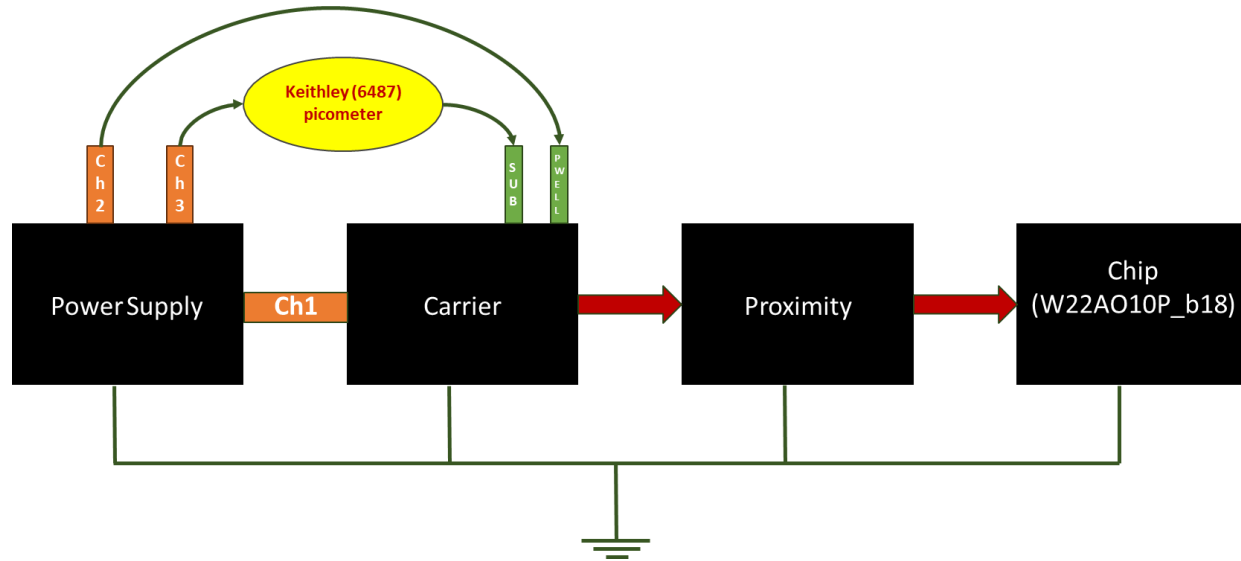


I-V Measurements of SUB and PWELL currents :  
DAQ  
DAQ + Proximity  
DAQ + Proximity + Chip



Picoammeter  
Keithley 6487

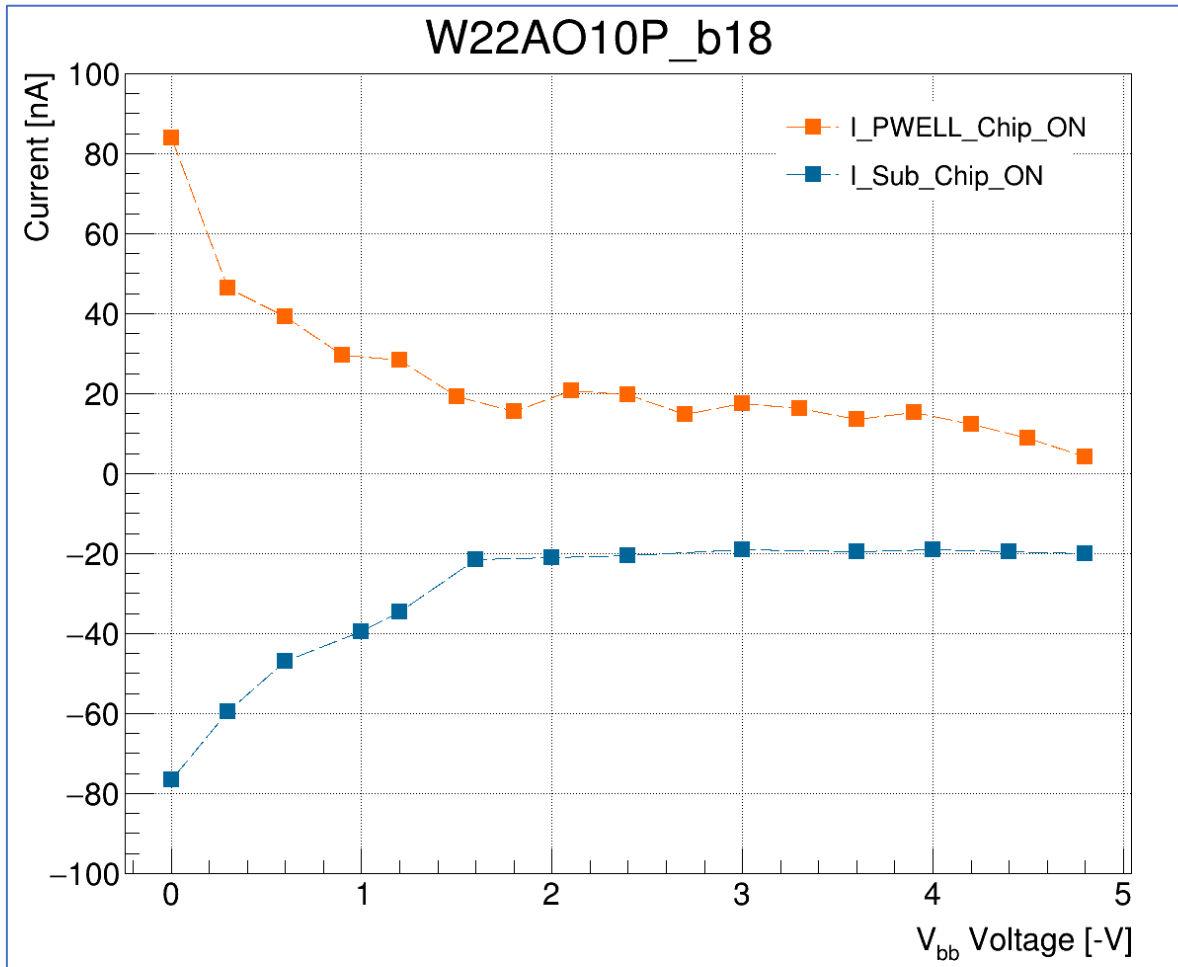
# Schematic of measurements: $I_{sub}$ and $I_{pwell}$



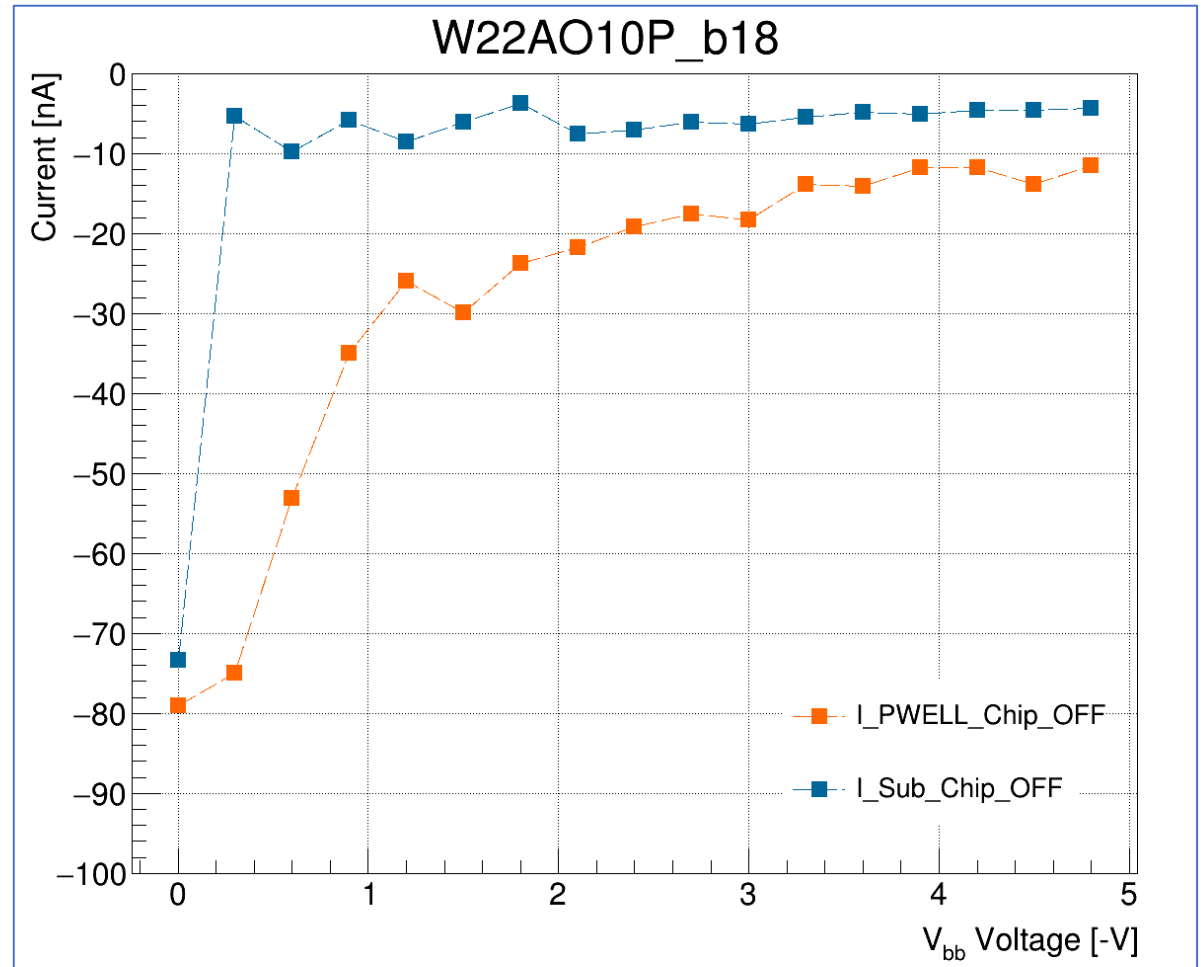


# OpAmp Bari – Power ON/OFF Comparison

**Carrier (ON) + Proximity + Chip (ON)**



**Carrier (ON) + Proximity + Chip (OFF)**



# Next Steps

- Increase configuration for AC Coupled Chip : HV (0÷20V) and PSUB (0,-0.6,-1.2,-1.8,-2.4V)
- Start  $^{55}\text{Fe}$  data acquisition for direct chip investigation
- VI Measurements: testing python script for picoammeter data acquisition