Computing R&D Workshop 2011

Tuesday, 5 July 2011

New CPU Architectures, GPU's and Framework (I) - IUSS (08:30 - 10:30)

time	[id] title	presenter
08:30	[17] HEP software on Multicore: present experience and perspectives	INNOCENTE, Vincenzo
09:30	[34] A Parallelization Exercise	LONGO, Stefano
09:50	[37] Performances of some HEP applications	CIASCHINI, Vincenzo