

Computing R&D Workshop 2011

Tuesday, 5 July 2011

New CPU Architectures, GPU's and Framework (II) - IUSS (11:00 - 13:00)

time	[id] title	presenter
11:00	[38] Event Processing Frameworks	Dr ELMER, Peter
11:20	[39] Physics Simulations on multi- and many-core architectures	SCHIFANO, Sebastiano Fabio