

PM2024 - 16th Pisa Meeting on Advanced Detectors

Friday, 31 May 2024

Electronics and On-Detector Processing - Oral session - Sala Maria Luisa (15:30 - 19:20)

-Conveners: Chiara Guazzoni; Giovanna Lehmann

time	[id] title	presenter
15:30	[77] Design of a 40 GS/sec 20 mw/Channel Waveform Sampling ASIC in 65 nm CMOS	PARK, Jinseo
15:50	[119] ASICs for "5D calorimetry" at OMEGA	DE LA TAILLE, Christophe
16:10	[481] CRYO: The Charge Readout ASIC for nEXO in Liquid Xenon (LXe)	PENA PEREZ, Aldo
16:30	[504] ALCOR: a mixed-signal ASIC for the dRICH detector of the ePIC experiment at the EIC	COSSIO, Fabio
16:50	[175] On novel front-end electronics for the ATLAS BI RPC upgrade at HL-LHC developed in SiGe BiCMOS technology with a high-resolution rad-hard Time-To-Digital converter embedded	PIZZIMENTO, Luca
17:10	Coffee break	
17:40	[241] ToASt : a 64-channel ASIC for the readout of the Silicon Strip Detectors of the PANDA experiment	MAZZA, Giovanni
18:00	[413] Tests and Characterization of a Mixed-Signal Read Out ASIC for Silicon Micro-Strip Detectors	BARBANERA, Mattia
18:20	[30] 40MHz Readout of CMS Silicon Modules in a High Intensity Beam	MONK, David
18:40	[426] Detector-embedded reconstruction of complex primitives using FPGAs	PUNZI, Giovanni
19:00	[319] An FPGA-based Trigger for the QTNM Experiment	SWINNOCK, Daniel