

A flexible data acquisition system for the MEDIPIX family detectors

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16th Pisa Meeting on Advanced Detectors - La Biodola, Isola d'Elba, May 26-June 1, 2024

The Medipix Family ASICs

The CERN MEDIPIX Collaboration developed a family of read-out ASICs for particle imaging and detection development.

The Medipix ASIC family comprises 4 generation of ASICs.

The latest iterations are:

- Timepix4 [1]:
 - 512x448 pixel matrix, 55 μm pitch, data-driven readout, ToT (1.56ns bin) and ToA (195ps bin), for timing applications
- Medipix4 [2]:
 - 320x320 pixel matrix, 75 \rightarrow 150 μm pitch, 1 \rightarrow 8 thresholds, for imaging applications

INFN and Timepix4

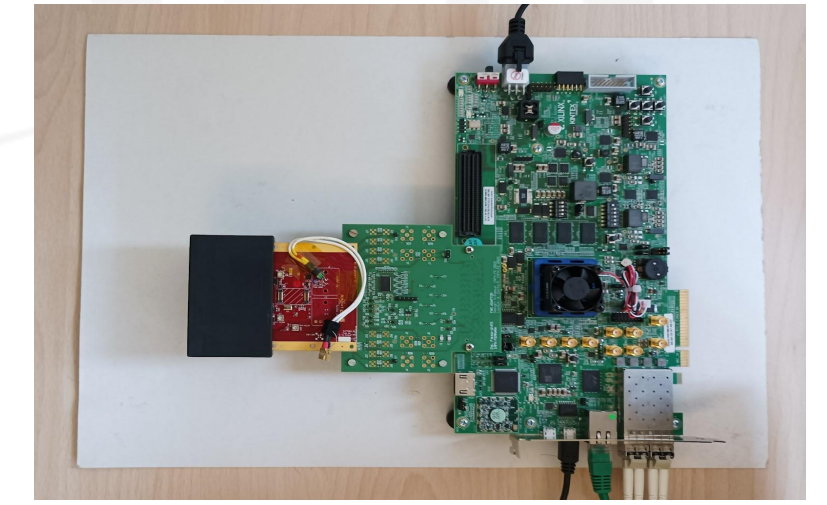
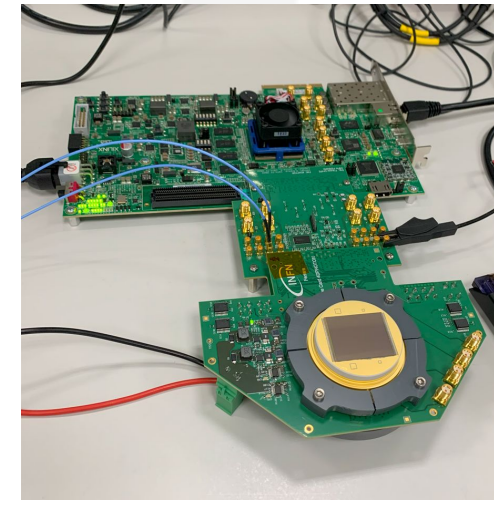
DAQ developed in the context of the 4DPHOTON [3] project:

- Development of a single photon detector with integrated front-end.

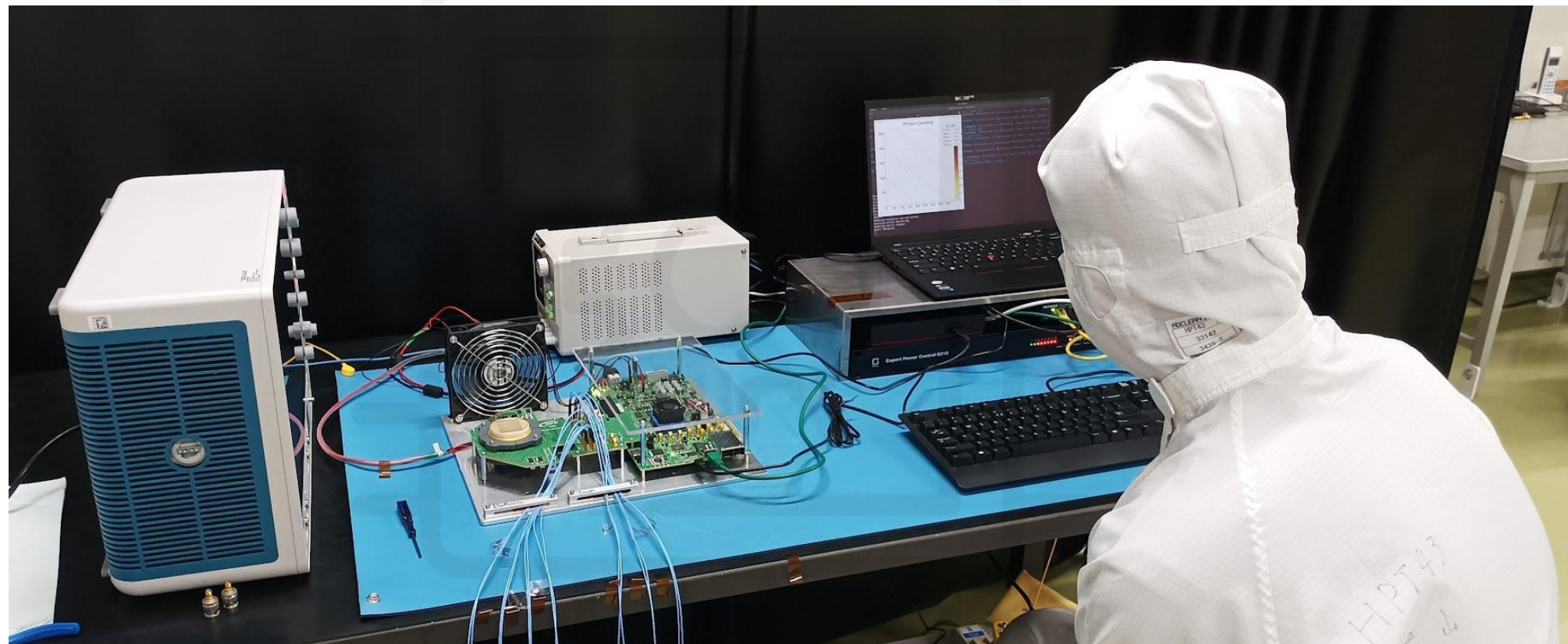
DAQ also used by INFN in other activities:

- bump bonded semicond. sensors for medical use and tracking

There is a need for a common and flexible DAQ system



The Hardware



Develop "general purpose" control card/DAQ

- Based on "consumer" hardware (Xilinx KCU105)
- Use standard protocols
 - 1G ethernet for configuration data from controller
 - 10G ethernet for detector data to storage
 - FMC VITA 57.1 for detector communication (Physical only)
- Enough resources for minimal pre processing and monitoring
- Open source and open hardware
 - Released under open licence: EUPL 1.2 Licence

The Firmware

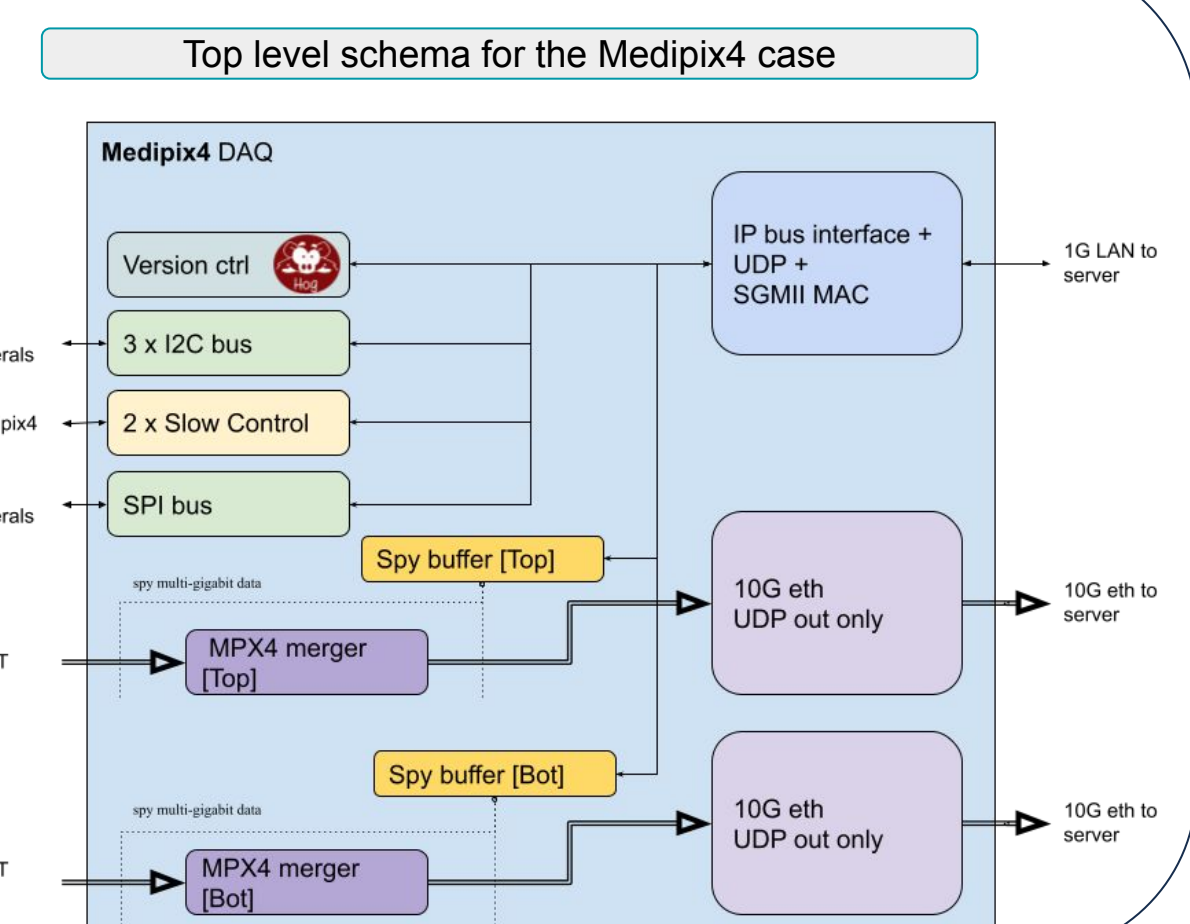
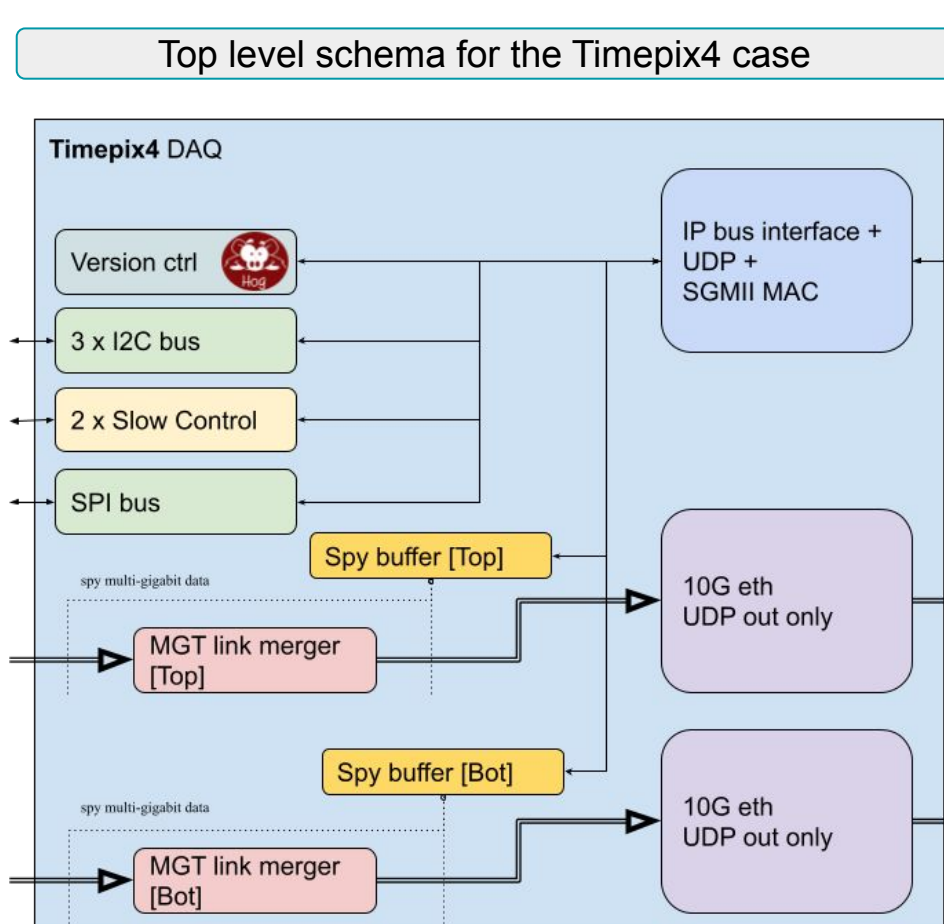
The DAQ system allows for operation with:

- Timepix4 ASIC (timing applications)
- Medipix4 ASIC (imaging applications)

Main difference \rightarrow "fast" readout:

- Timepix4: 16 MGT @10.24 Gbps (64b/66b)
- Medipix4: 16 synchronous links @320 MHz DDR

Firmware is fully configurable to support different detector cards using custom peripherals



Reduce, Reuse, Recycle

Use HDL on git (Hog) [4] to handle project in git:

- existing HDL sources included as git submodules
 - ipbus [5] ([from github.com/ipbus](https://github.com/ipbus))
 - 10G/1G UDP-IP stack ([from github.com/alexforench](https://github.com/alexforench))
 - common modules: ([from baltig.inf.n.it](https://github.com/baltig))
 - I2C master, SPI master, reset generators, debouncers, ...

Keep it open source \rightarrow user can add modules at firmware level

Future developments

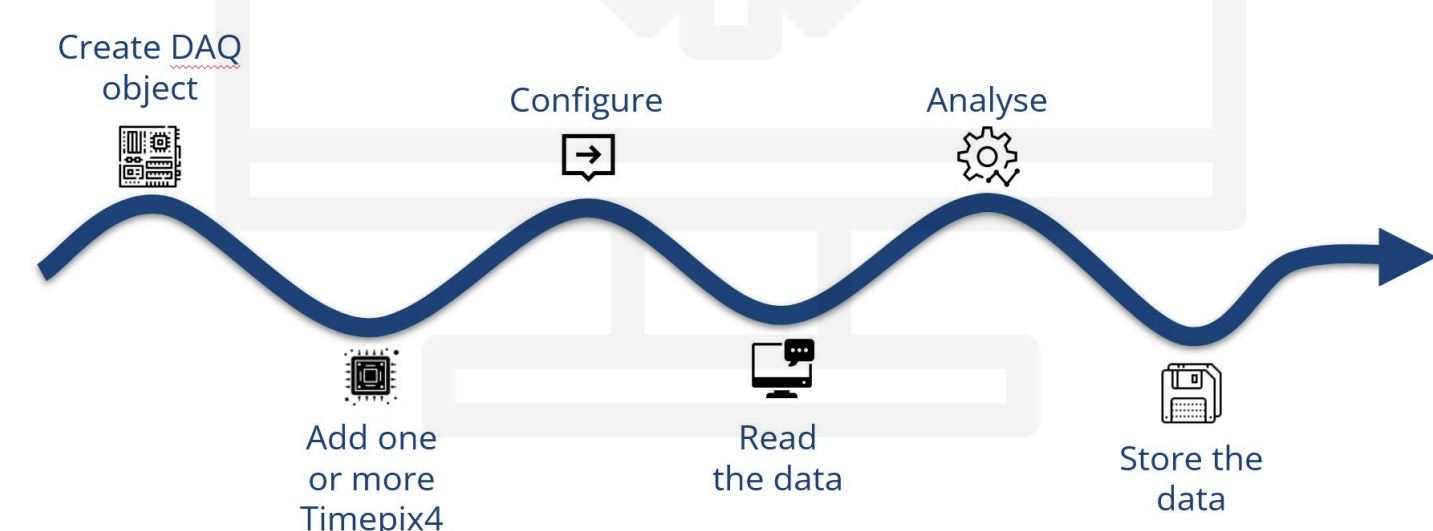
DAQ development still ongoing, planned improvements are

- Full support of the MEDIPIX4 ASIC
- Addition of Partial Reconfiguration regions in the data-path
 - Allow for data filtering, online clustering in hardware
- Exploit Hog for instantiation of peripherals masters

The software

DAQ system is complemented by a software toolset (C++)

- Allows for both
 - Low level access (single commands)
 - High level routines (equalization, calibration, data acquisition, etc..)
- Compatible with other DAQ systems
- Scalable to multi-DAQ systems



Acknowledgements

This work was carried out in the context of the Medipix4 Collaboration based at CERN, and in the framework of the MEDIPIX4 project funded by INFN CSN5. This project has received funding from the European Research Council (ERC) under the European Union's Horizon 2020 research and innovation programme (Grant agreement No. 819627, 4DPHOTON project).

References

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