Fast and low power SiPM amplifier operating in a wide temperature range



DEGLI STUDI

BICOCCA

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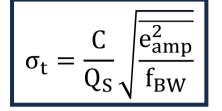
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Amplifier schematic

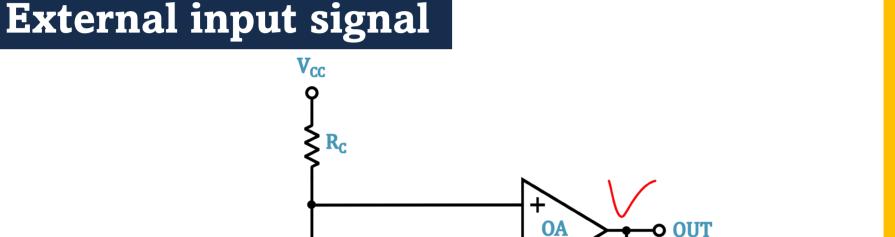
- Fast transimpedance signal amplifier for **SiPM** (Silicon **PhotoMultiplier**) characterisation.
- Study of **SiPM timing performance** in a wide temperature range (between ~ 80 K and ~ 300 K).
- Low power consumption: 135 mW @ ~ 300 K and 65 mW at @ ~ 80 K.
- **Layout** which aims at improving the amplifier speed and stability by minimising parasitic capacitance and using high-frequency dielectric.
- Very low noise $\left(<0.4-1\frac{nV}{\sqrt{Hz}}\right)$ and very fast response (< 800 ps) result in very low jitter.

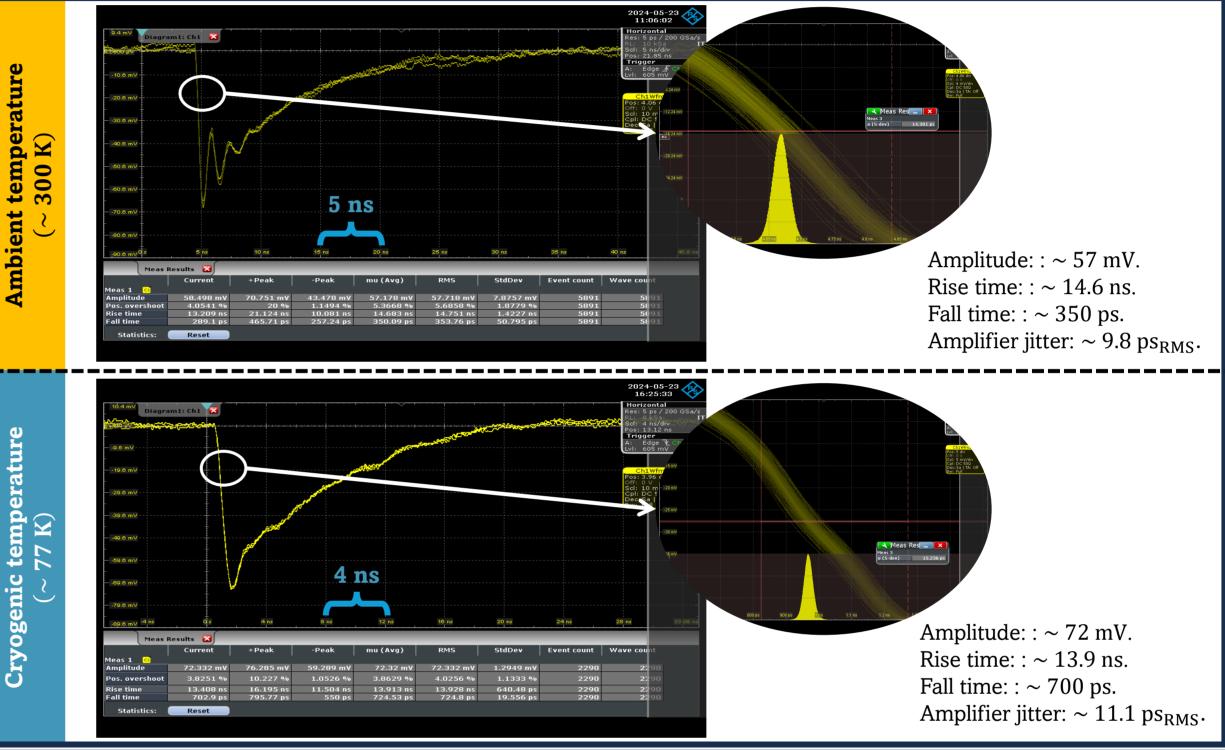
Electronics jitter



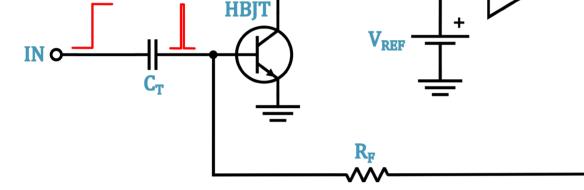
1(t)

- e²_{amp}: input referred amplifier noise.
 f_{BW}: amplifier bandwith frequency.
 Q_S: charge produced by the source.
 C: constant.
- V_{CC} **Ouput and input Simplified Amplifier** connections. **Schematic Amplifier layout** $\mathbf{R}_{\mathbf{C}}$ -**O OUT OA** HBT IN O-**Amplifier made of a:** • Fast OA (Operational Amplifier) that can also operate at One of the four **amplifiers**, cryogenic temperatures. FBK SiPM footprint, each connected to four Temperature • Low noise HBT (Heterojunction Bipolar Transistor). different selectable inputs. 16 SiPMs. sensor. 2024-05-23 11:06:02 nbient temperature





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• Step voltage input signal (70 ps rise time).

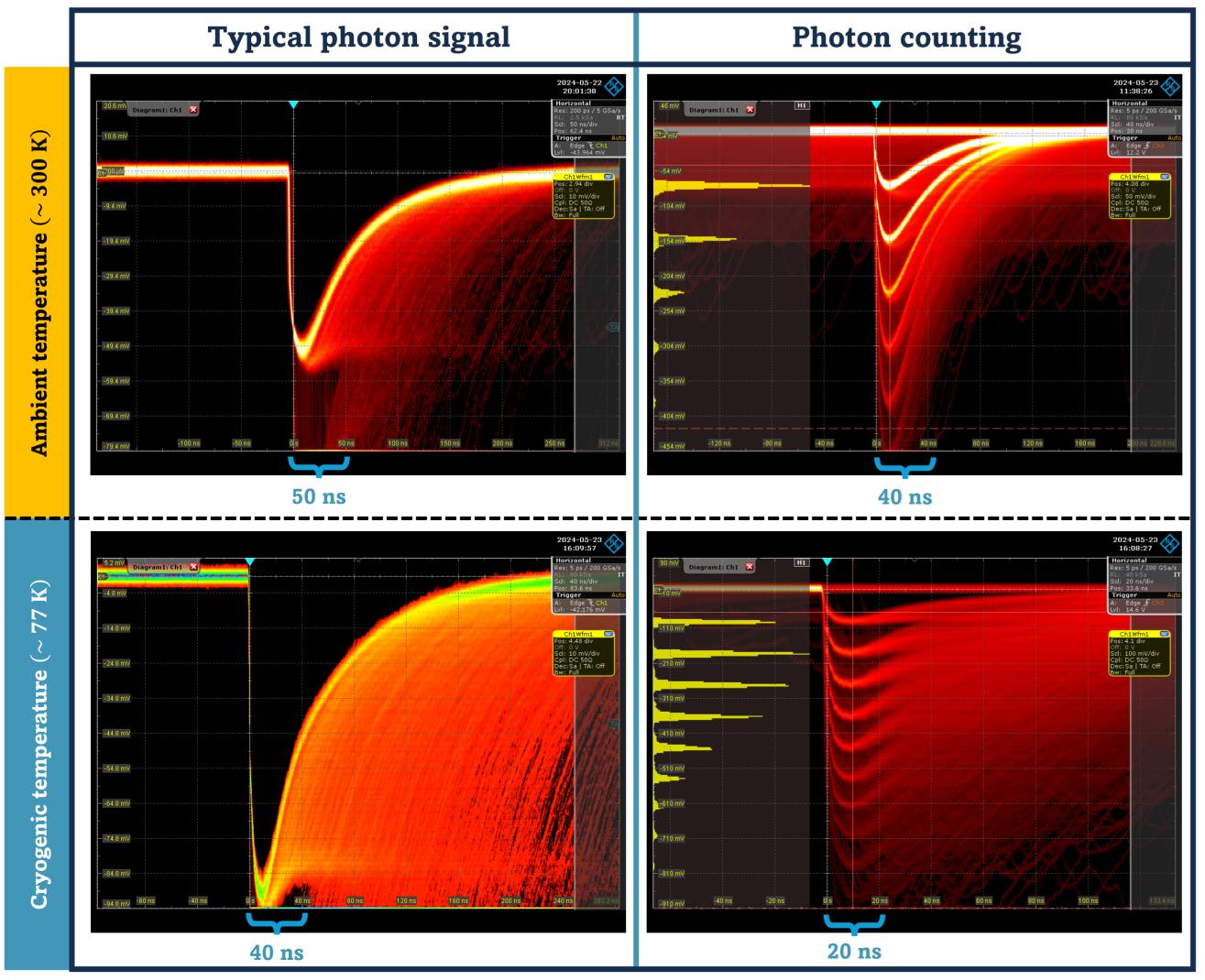
δ(t)

- Voltage signal equal to 1 photon signal (1.6 Me⁻) generated by a 1.3mm x 1.3mm Hamamatus SiPM (S13360-1350) at 4 V_{OV}.
- The signal charges a **test capacitor** $C_T = 1 \text{ pF}$.
- $\sim \delta(t)$ input current signal.
- Instrument (oscilloscope) jitter: $\sigma_{t_{system}} \sim 10.4 \text{ ps}_{RMS}$. Therefore the **amplifier jitter** is

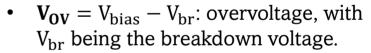
 $\sqrt{\sigma_{t_{measured}}^2 - \sigma_{t_{system}}^2}$, with $\sigma_{t_{measured}}$ being a jitter measurement.

SiPM input signal

- The amplifier was tested with a **1.3mm x 1.3mm Hamamatsu SiPM** (S13360-1350).
- The photons were produced by a 405 nm laser controlled by the Hamamatsu PLP-10 pulser (70 ps FWHM pulses)
- <u>Single photon jitter measurements.</u>
- Low amplifier jitter. It has low impact on SiPM jitter measurements: < 1% @ ~ 300 K and < 4% at @ ~ 80 K.
 - 300 K and 4 V_{OV}: 101.9 ps_{RMS} SiPM jitter (101.4 ps_{RMS} without the amplifier jitter contribution).
 - 80 K and 4 V_{OV}: 48.1 ps_{RMS} SiPM jitter (46.8 ps_{RMS} without the amplifier jitter contribution).



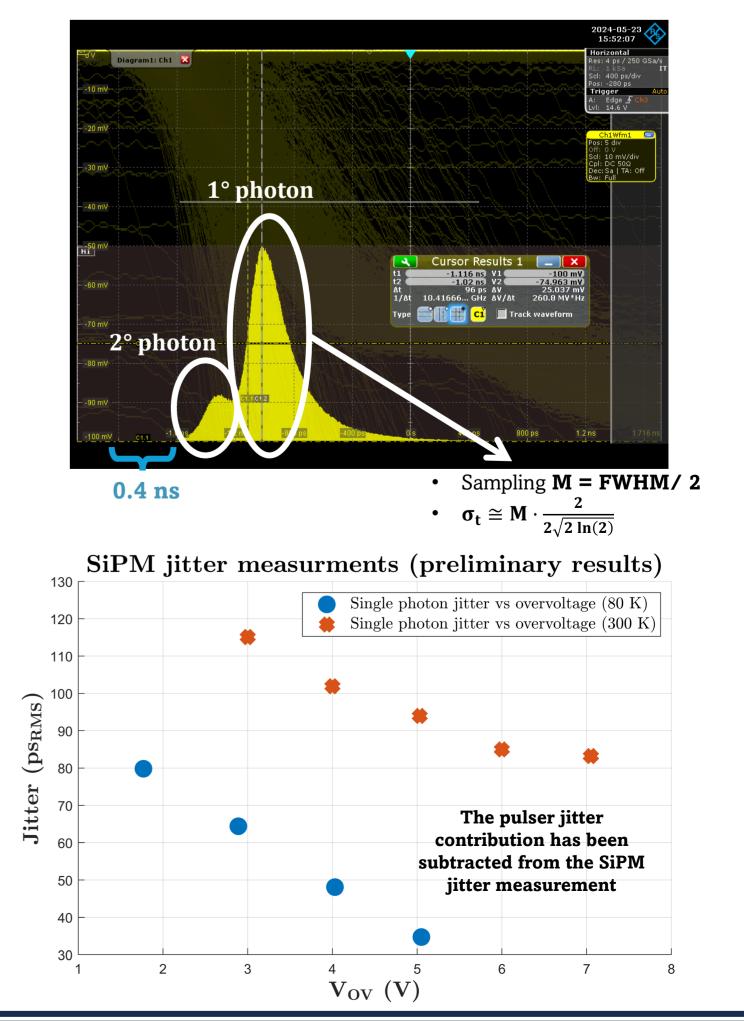
SiPM



- **C**_D: single diode capacitance.
- I_D : avalanche current.
- **C**_D: diode capacitance.
- $\mathbf{R}_{\mathbf{Q}}$: quenching resistor.
- **C**_{**Q**}: quenching parasitic capacitance
- C_G : grid parasitic capacitance.
- $\mathbf{\tau}_{\mathbf{s}} = (C_{\mathrm{D}} + C_{\mathrm{Q}})(R_{\mathrm{Q}} + R_{\mathrm{S}}).$

$$\mathbf{I}_{s} = V_{0V} \left(C_{Q} \delta(t) + C_{D} \frac{e^{-t/\tau_{s}}}{\tau_{s}} \right)$$

SiPM jitter measurements



16th Pisa Meeting on Advanced Detectors