PM2024 - 16th Pisa Meeting on Advanced Detectors Performance of the new tracker and timing ATLAS detectors at HL-LHC for pile-up suppression

The High-Luminosity LHC (HL-LHC), currently foreseen to start towards 2029, will operate at an instantaneous luminosity of up to 7.5×10^{34} cm⁻² s⁻¹ corresponding to an unprecedented average number of proton proton collisions per bunch crossing of up to 200. Efficient techniques to identify and and suppress jets originating from pile-up interactions are critical to achieve the physics potential of the HL-LHC. The ATLAS Inner Detector for the HL-LHC Run 4 will be upgraded to a full-silicon Inner Tracker (ITk). Thanks to the extended coverage of ITk, the techniques to tag and suppress pile-up currently in use will be applicable also to the high eta region, however, with expected worse performance compared to that in the central region, due to the higher amount of material and the harsher environment. The High-Granularity Timing Detector (HGTD) that will be installed in the forward region for Run 4, will improve the pile-up suppression in that region through timing information at the 30-50 ps level. In this poster an overview of the ITk and HGTD sub-detectors will be given, with a focus on their performance on the pile-up suppression for the reconstruction of high level objects. Also, the impact of a possible additional timing detector in the central region, enabling 4D Tracking beyond the Run 4 will be discussed.

New Inner Tracker (ITk)



Motivation: To face the harsh environment at HL-LHC, the current inne detector will be replaced with a new full-silicon Inner Tracker (ITk).

Finer segmentation

Extension up to $|\eta| < 4$

new sensors & front-end

Increase radiation hardness:

Sensor Technology

Pixel, 13m²

- 3D for the inner most layer
- Planar elsewhere
- pitch size 50 x 50 μm² except 25 x 100 μm50 x 50 μm for barrel most inner layer

Strip: 165m², planar n-in-p





Improved impact parameter resolution

Reduced at high $|\eta|$ due to larger amount of

High Granularity Time Detector (HGTD)

Located in front of encap calorimeter Δz envelope~ 12.5 cm @ |z| = 3.5 m, 2.4<| η |<4



Sensor Technology

- Low Gain Avalanche Diodes (LGAD)
- Cell size 1.3 × 1.3 mm²
- 50 μm active thickness

Time resolution of LGAD+ASIC < 50 ps



Motivation: Improve pile-up suppression in the reconstruction in the forward region

 At mu=200, 5 -7 vertices from forward tracks are merged due to larger z₀ resolution at large |η |



- The merged vertices can be resolved using timing information of the tracks from the HGTD
- Main strategy is to reconstruct the time of the primary vertex using timing information of the tracks from the HGTD
- Limitation: performance of reconstructing the time of the primary vertex depends on number of forward tracks in the event and event topology

B-Tagging and Pile-up Jet tagging

ITk Strip TDR

ITk Pixel TDR



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