

State-of-the art electronics for PET scanners and other applications

PSMR2024 – Elba Italy, May 2024



Technology overview

• Microelectronics:

- ASICs reading and digitizing fast signals from SiPMs, MCPs, etc.
- Radiation tolerant ASICs
- IP blocks: amplifiers, discriminators, charge integrators, DACs, ADCs and TDCs

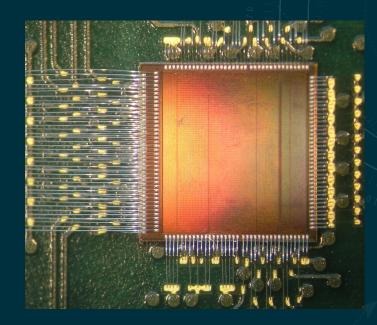
Readout systems

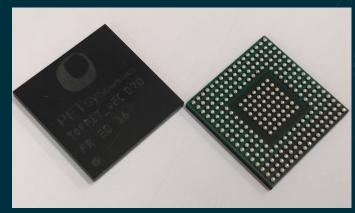
- Front-end electronics boards interfacing to sensors
- Data acquisition and system control (including firmware and software)
- Complete readout solutions, scalable up to hundred thousands electronics channels

Main Features:

PETsys

- Digitizes time and amplitude of SiPM signals
- 64 independent channels in 5x5 mm²
- Accepts positive and negative signal polarity
- Noise 1.2 -1.5 mV -> 1 p.e. 30 mV
- Charge integration: 10 bit
- TDC time binning: 30 ps
- Low power : 4 8.2 mW / Channel
- Event rate: up to 500 kHz per channel
- On-chip calibration circuitry
- Generates digital event record every time the trigger conditions are fulfilled

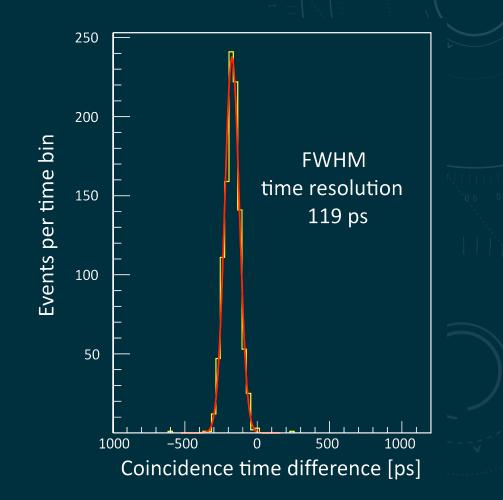






TOFPET2 time resolution (CTR) LYSO 2x2x3 mm³ : 119 picoseconds

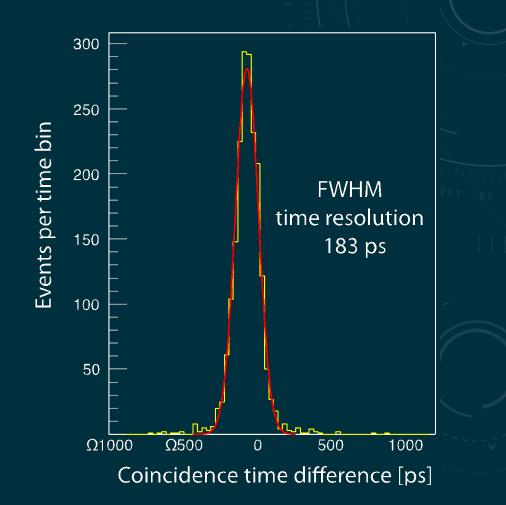
- Hamamatsu MPPC S13361-3050AE-04
- LYSO:Ce,Ca 2x2x3 mm³
- 3 V over-voltage
- 4 p. e. threshold
- 15 °C
- Na22 Gamma source





TOFPET2 time resolution (CTR) LYSO 3x3x20 mm³ : 183 picoseconds

- Hamamatsu MPPC S13361-3050AE-04
- LYSO:Ce,Ca 3x3x20 mm³
- 3 V over-voltage
- 4 p. e. threshold
- 15 °C
- Na22 Gamma source

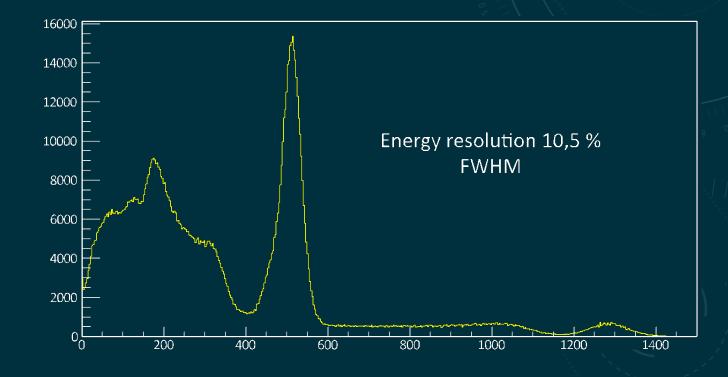




Energy resolution: 10,5%

Na22 point source LYSO: 3 x 3 x 5 mm³ KETEK-PM3325_WB SiPM

Corrected for SiPM non-linearity

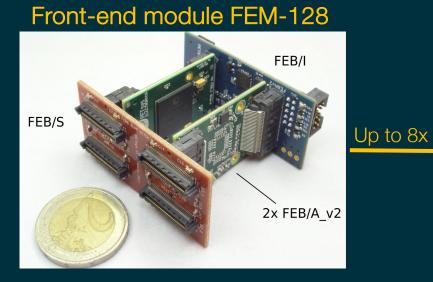




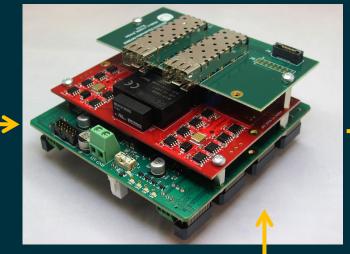
PETsys Readout System

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FEB/D-1k module



Up to 16x FEB/Ds

PCIe DAQ module



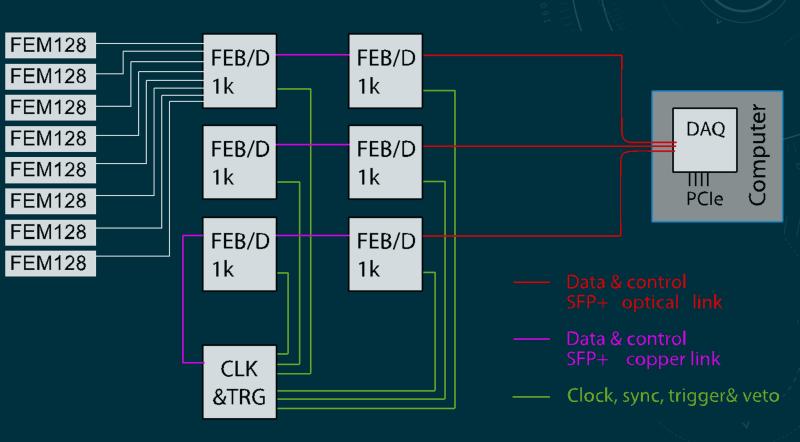
CLK & TRG module





Example of a readout system for a PET scanner with up to 16'384 channels

- Each FEB/D-1k module controls 16 ASICs in 8 FEM128
- The CLK&TRG module allows coincidence selection in the firmware
- The DAQ card receives and transfers the digital data to the computer



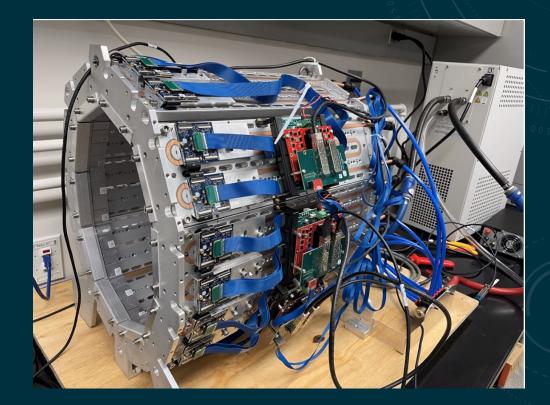


PET for proton therapy UT Texas



TPPT Consortium, IEEE NSS MIC RTSD, 2022, MIC-04-385

Brain PET at Cornell University



Zeng, X, Wang, Z, Tan, W, et al., Med Phys. 2023; 50: 3401–3417

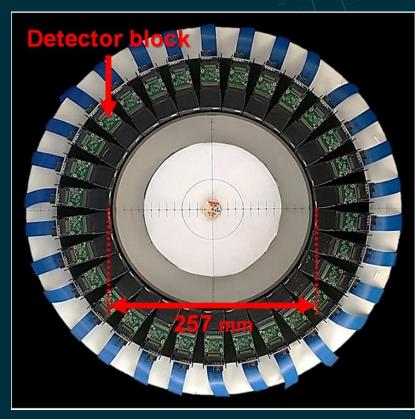


Preclinical PET in Japan



National Institute for Quantum and Radiological Science and Technology

Brain PET in South Korea



Park, K.; Jung, J.; Choi, Y.; Leem, H.; Kim, Y. Sensors 2021, 21, 5566



Solutions for larger number of channels

FEM-256 module

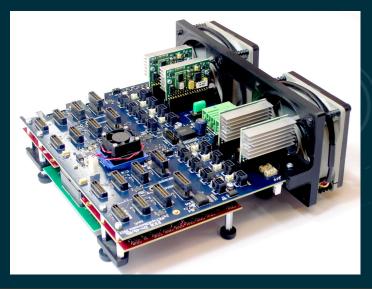
	FEB/	D-1K	FEB/D-8K					
	FEM-128	FEM-256	FEM-128	FEM-256	FEM-512*			
Max # of FEMs per FEB/D	8	4	16	16	16			
Max # of channels per FEB/D	1024	1024	2048	4096	8192			
Max # of FEB/Ds	16	16	16	16	16			
Max # of channels	16384	16384	32768	65536	131072			
Max data rate per 1 FEM (Mcps)	40	40	40	40	40			
Max data rate per FEB/D (Mcps)	100	100	100	100	100			
Max data rate per DAQ card **	230	230	230	230	230			

* To be developed under request.

** Max rate can be extended using more DAQ cards. Tested with 2 DAQ cards up to 400 Mcps.



FEB/D-8k module

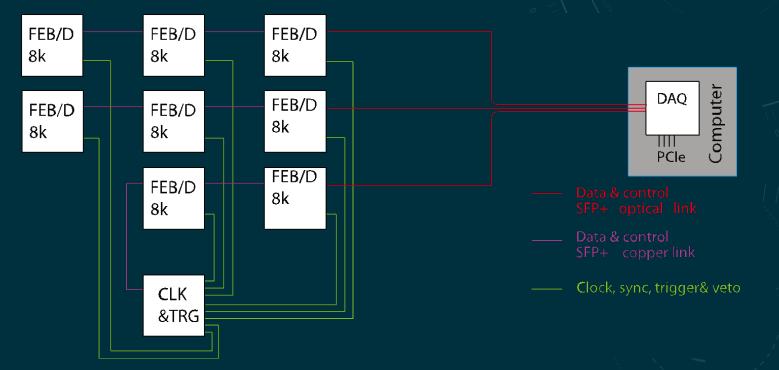




Solutions for larger number of channels

Readout for a PET scanner with up to 131'072 channels

- Each FEB/D-8k module controls and reads up to 128 ASICs.
- Needs FEB/D-8K and FEM256 (available) or FEM512 (development under request).
- It is possible to use several DAQ boards to handle larger total data rates.



With 64 channel CLK-TRG board, the system could be expanded up to 524'288 channels



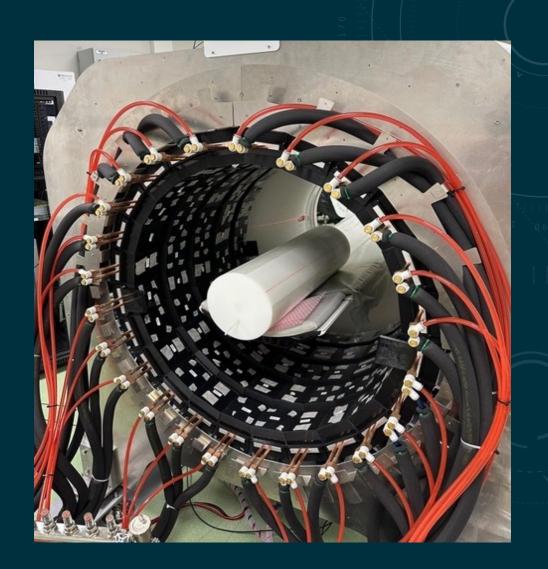
Solutions for larger number of channels

IMAS Total-Body PET Scanner (I3M Valencia, Oncovision):

30'720 electronic channels

• Presented at IEEE NSS/MIC 2023

See presentation by Antonio Gonzalez at this conference





DAQ Firmware and Software

- PETsys provides coincidence filtering in firmware, reducing the data rate to the DAQ computer by at least an order of magnitude.
- PETsys also provides DAQ Software allowing to control, monitor and calibrate the system. The DAQ software produces the data (channel, timestamp and charge) for list mode.
- ASIC calibration, configuration and data acquisition are controlled with a Python/C++ API and execution programs are provided for automation and scripting.
- There is also an easy-to-use graphical user interface, controlling the same commands and system temperature monitoring.

PETsys Electronics TOFPET2 Data Acquisition Software (on tagus21.tagus.lip.pt) _ 2											- ×		
File Help													
GBE OPFP_KX7 OFF Working Data Folder											Cho	ose	
System Configuration	SIPM Bias Settings			ASIC Thr	ASIC Threshold Settings			ASIC Calibration Pov					
	PreBDV	0.00	-	+	vth_t1	20		•	Discriminator	s 🗹	FEM	ON	
FEB/D Topology Detect	BDV	0.00	-	+	vth_t2	20		+	TDC		BIAS	ON	
	ov	0.00	-	+	vth_e	15		F	QDC		Temper	Read	
Edit Maps	🌶 Edit config.ini				A 5	🛓 Save 📝 Edit			Start Calibration			Startup	
Data Acquision Data Processing													
File Name Choose					Raw Fil	Raw File Name					Choose		
Mode QDC 🕶						Out File Name					Choose		
Time 0.0 – +					Convert	Convert Raw to Singles			raction (9	6) 100) –	+	
HW Trigger 💫 On 💿 Off					Data F	Data Format Binary • Number of Hits 1 - +						+	
Acquire Acquire & Process					Process								
					PETsys E	ectronics !	SA						



New developments

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New PETsys TOFPET3 ASIC

- New ASIC in the TOFPET series suited for high performance applications:
 - 64-channel analog front-end with baseline stabilization, pulse tail cancelation, dark noise rejection and gain configuration
 - Three 10-bit digitization per event (2 TDC, 1 QDC)
 - New timing and energy circuits with outstanding performance
 - TOFPET3 contribution to CTR: 24 ps FWHM
 - Energy measurement (3000 pe, G=3.5e6) with non-linearity of 1% and resolution of 1.1% (photopeak 511keV)
 - Four additional channels with sums of 16, 32 or 64 cannels (configurable)
 - Advanced triggering features: selective readout of group of channels seeded by energy of one channel
 - Max event rate per channel 500 MHz, Output rate 3.8 Gb/s
 - Low power consumption

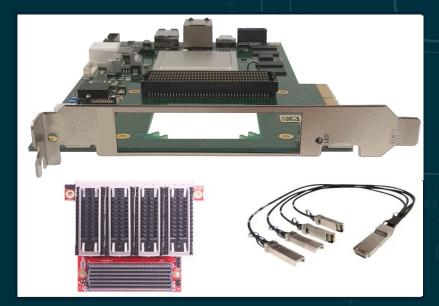
Evaluation-kit available in the fall 2024.

See presentation at this conference



Handling very large data rates

- New DAQ card with PCIe gen3 x16:
 - Up to 1.8 Gcps -14.4GB/s (8x more than the currently available DAQ card).
 - 20 individual links to FEB/D.
- Mezzanine with QSFP to SFP or QSFP to fiber optics: maintains full compatibility with existing systems.
- 4x SFP to maximize FEB/D throughput allowing up to 400 Mcps from a single FEB/D.
- QSFP connector also under development.



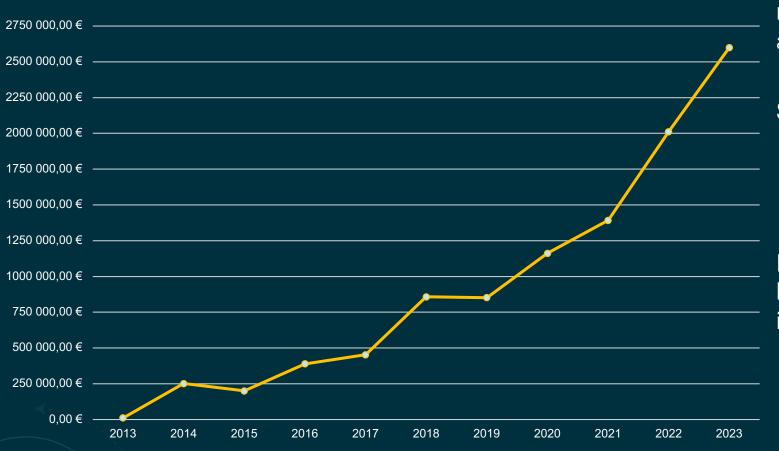




R&D projects, awards and international recognition

- CERN/CMS TOFHIR ASIC, 2019-2023
- CMS (CERN) Industrial Award, 2023
- TPPT project (TOF PET for Proton Therapy), 2020-2023
- R&D Project with GE (USA) for PET, 2021-22
- R&D demonstrator project with EU/PT2020 support
- 3 Seals of Excellence from the European Commission, 2016-2017
- Selected to the Web Summit 2016 (Lisbon)
- Award winner at the European Venture Contest 2015 (European Commission)

Evolution of Sales 2013-2023



A total of 13 PET scanners based on our readout solutions are currently deployed or are under construction.

Several applications outside the area of PET: SPECT, LIDAR, mining, cargo scanning, astronomy, elementary particle physics,

In addition to commercial companies, we have many top universities and research institutions among our customers:

CERN, GSI, NASA, Lawrence Livermore, Sandia labs, Oak Ridge national lab, Harvard, EPFL, QST (Japan), Beida & Qinghua (China), ...

ETsys

Flectronics



Thank you for your attention

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