

Designator
ML2_Alimentazione.SchDoc



Designator
ML2_MainPage.SchDoc



Designator
ML2_MisTemp.SchDoc



Designator
ML2_UART.SchDoc



Designator
ML2_Address_Switches.SchDoc



Designator
ML2_Vbias_Measure.SchDoc



MH5
Mount_Hole

MH6
Mount_Hole

MH7
Mount_Hole

Per Fissaggio Dissipatore

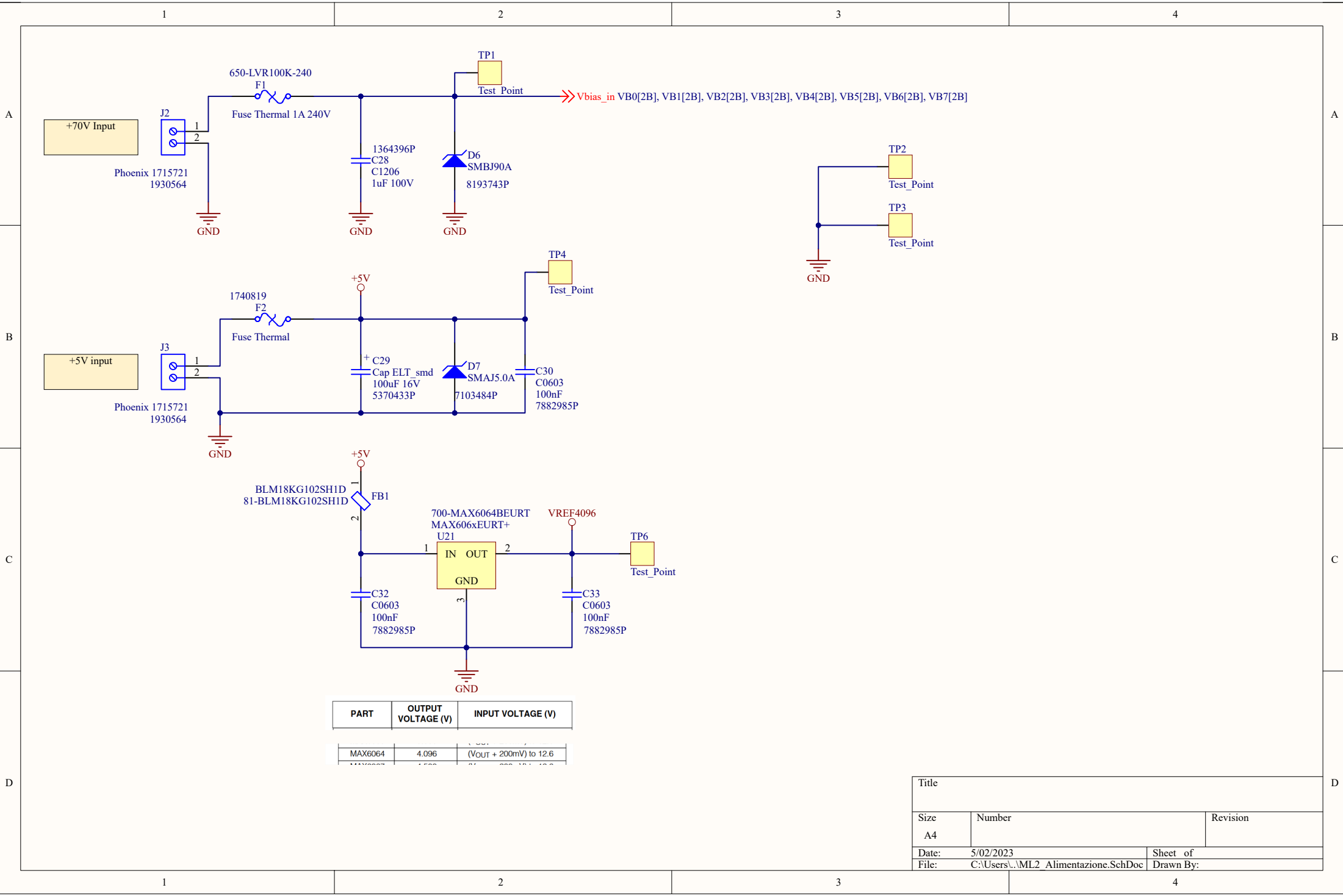
MH1
Mount_Hole

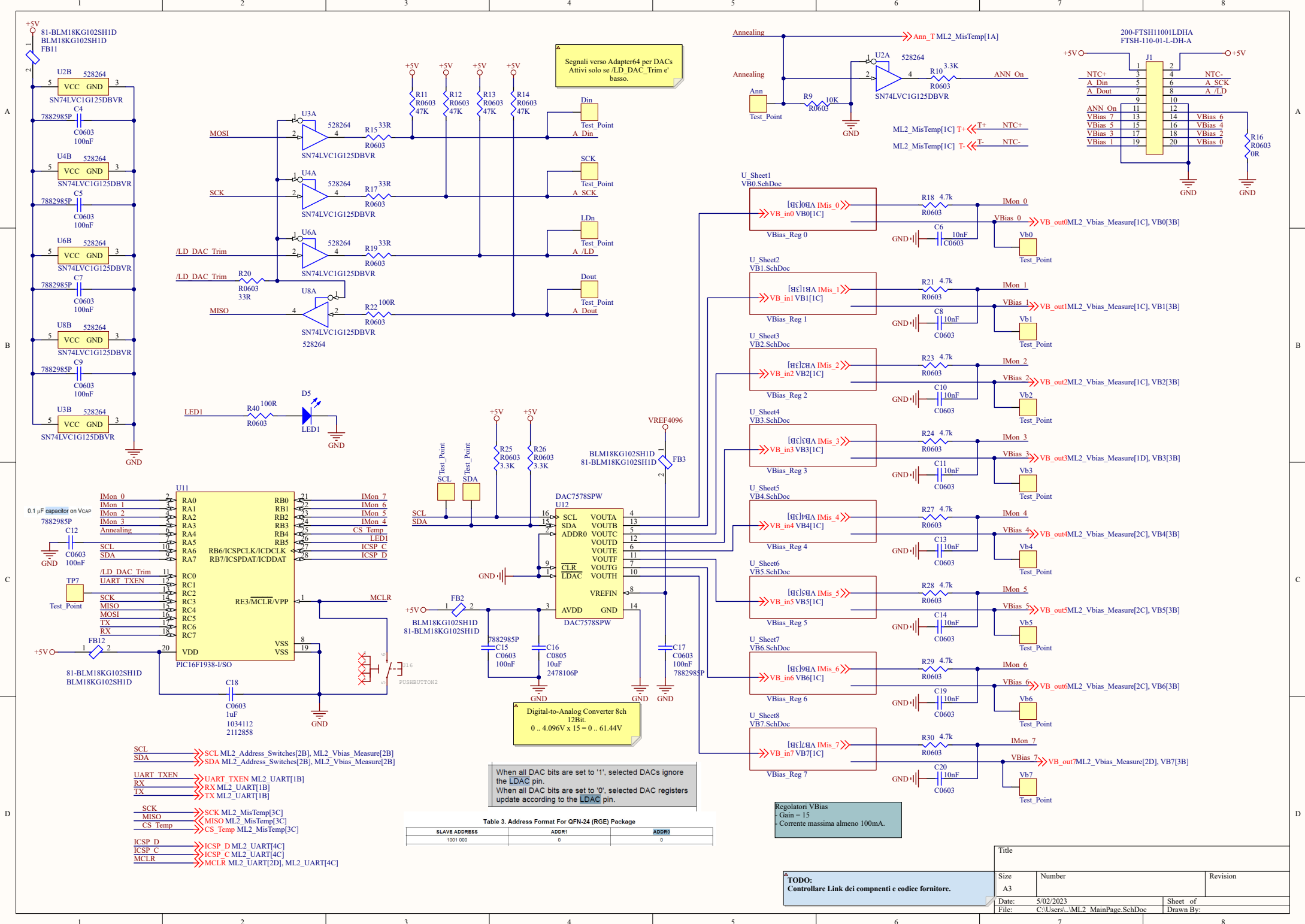
MH2
Mount_Hole

MH3
Mount_Hole

MH4
Mount_Hole

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Size A4	Number	Revision
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File:	C:\Users\...\ML2_TOP_schematic.SchDoc	Drawn By:





Segnali verso Adapter64 per DACs
Attivi solo se /LD_DAC_Trim e'
basso.

Digital-to-Analog Converter 8ch
12Bit.
0 .. 4.096V x 15 = 0 .. 61.44V

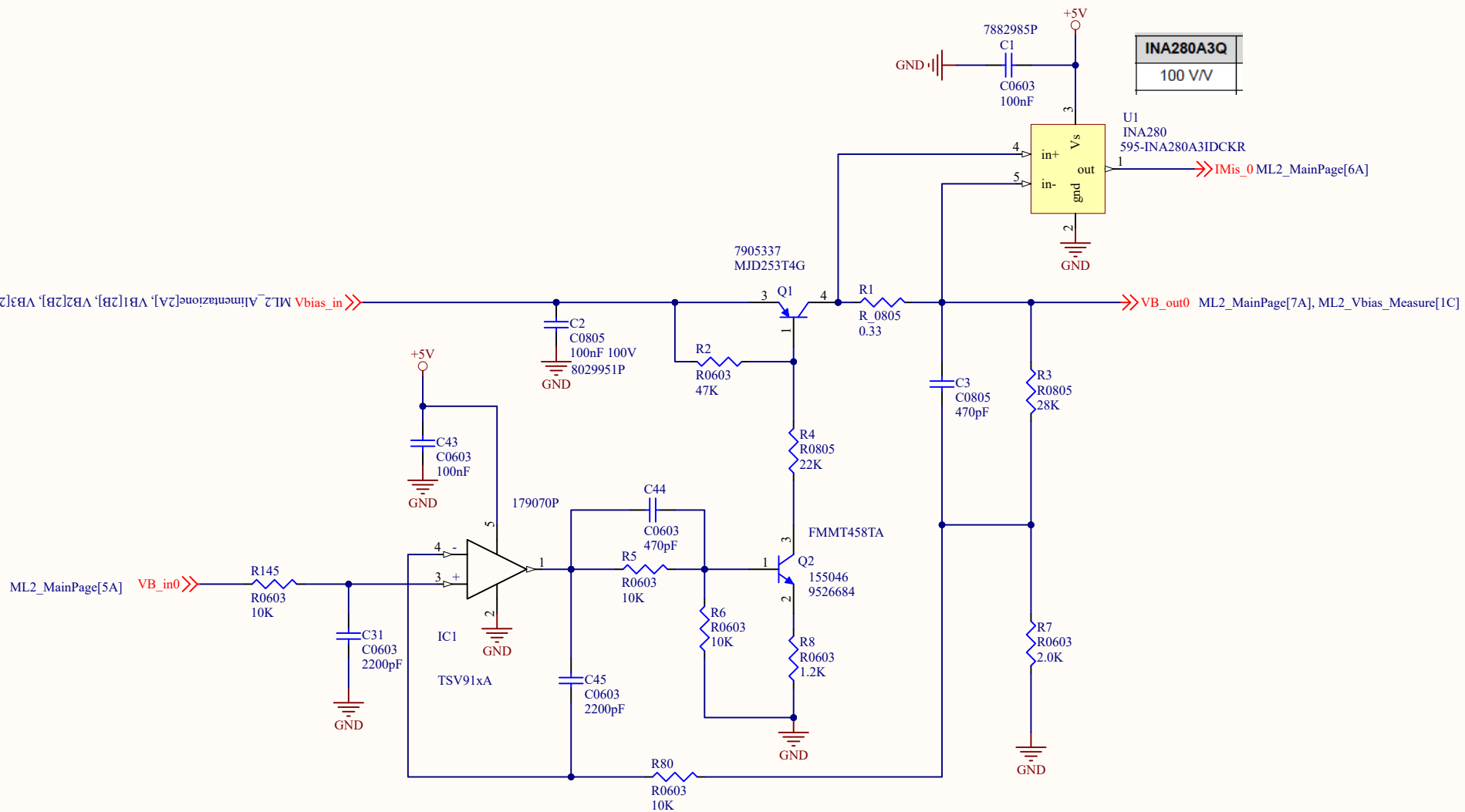
When all DAC bits are set to '1', selected DACs ignore
the LDAC pin.
When all DAC bits are set to '0', selected DAC registers
update according to the LDAC pin.

Table 3. Address Format For QFN-24 (RGE) Package		
SLAVE ADDRESS	ADDR1	ADDR0
1001 000	0	0

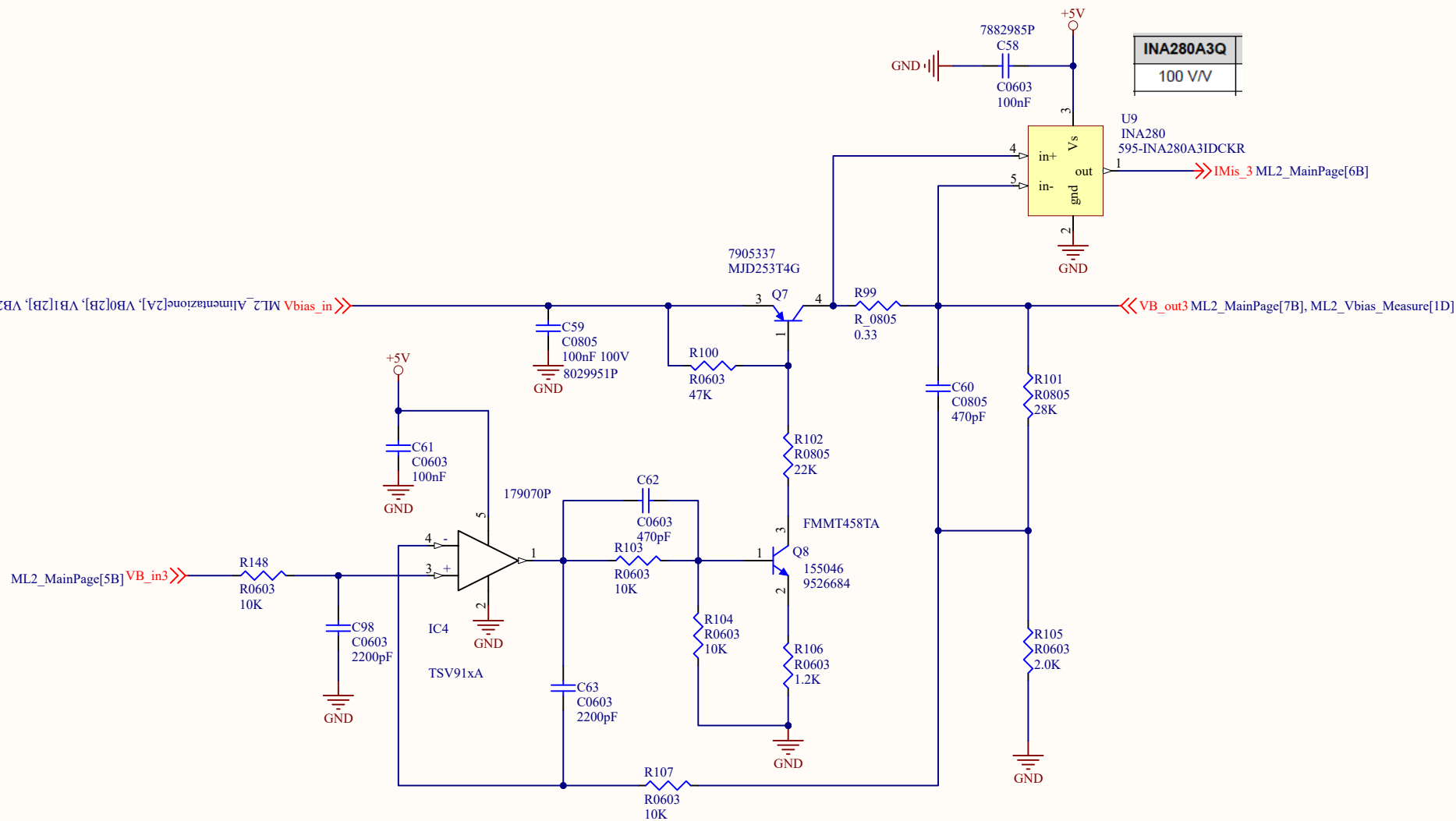
Regolatori VBias
- Gam = 15
- Corrente massima almeno 100mA.

TODO:
Controllare Link dei componenti e codice fornitore.

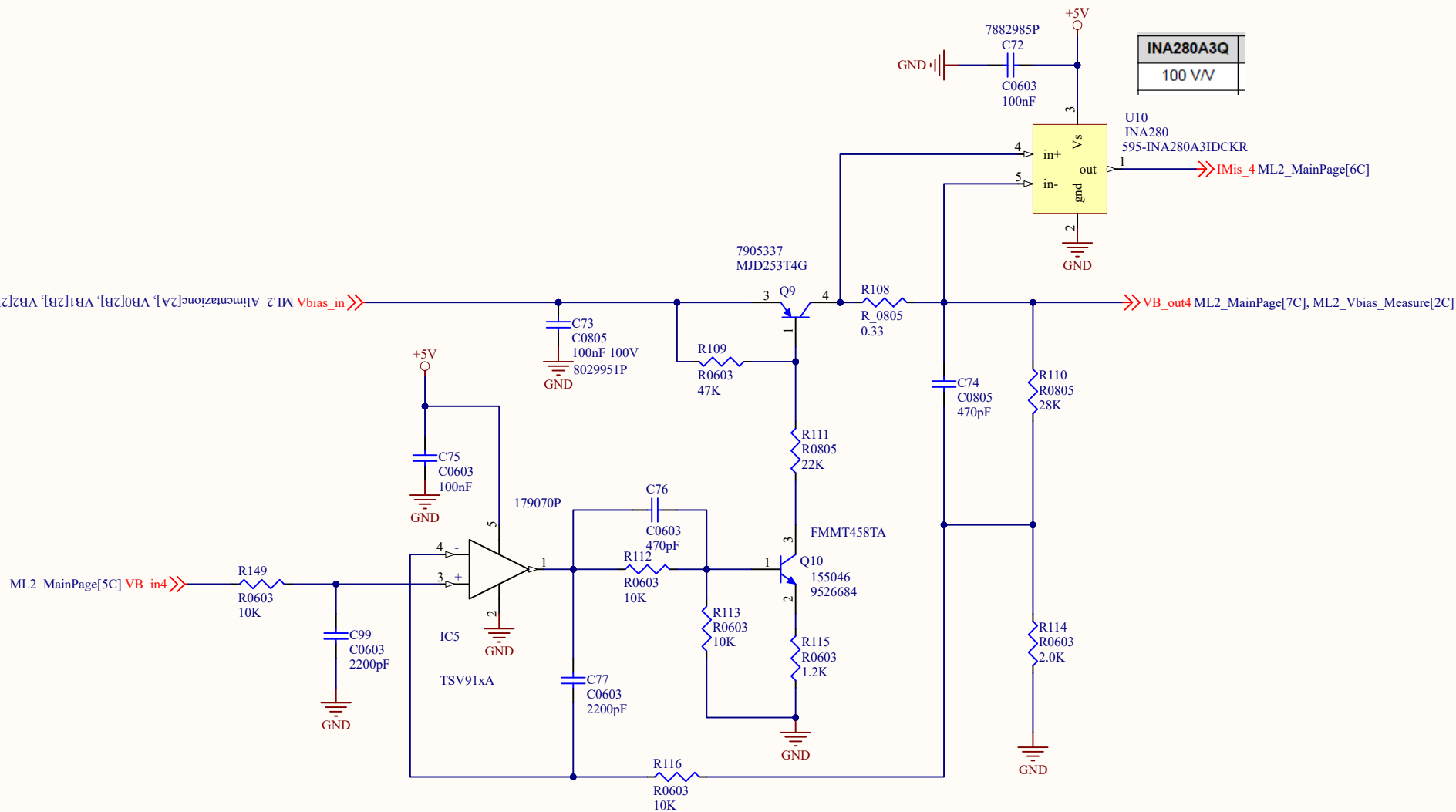
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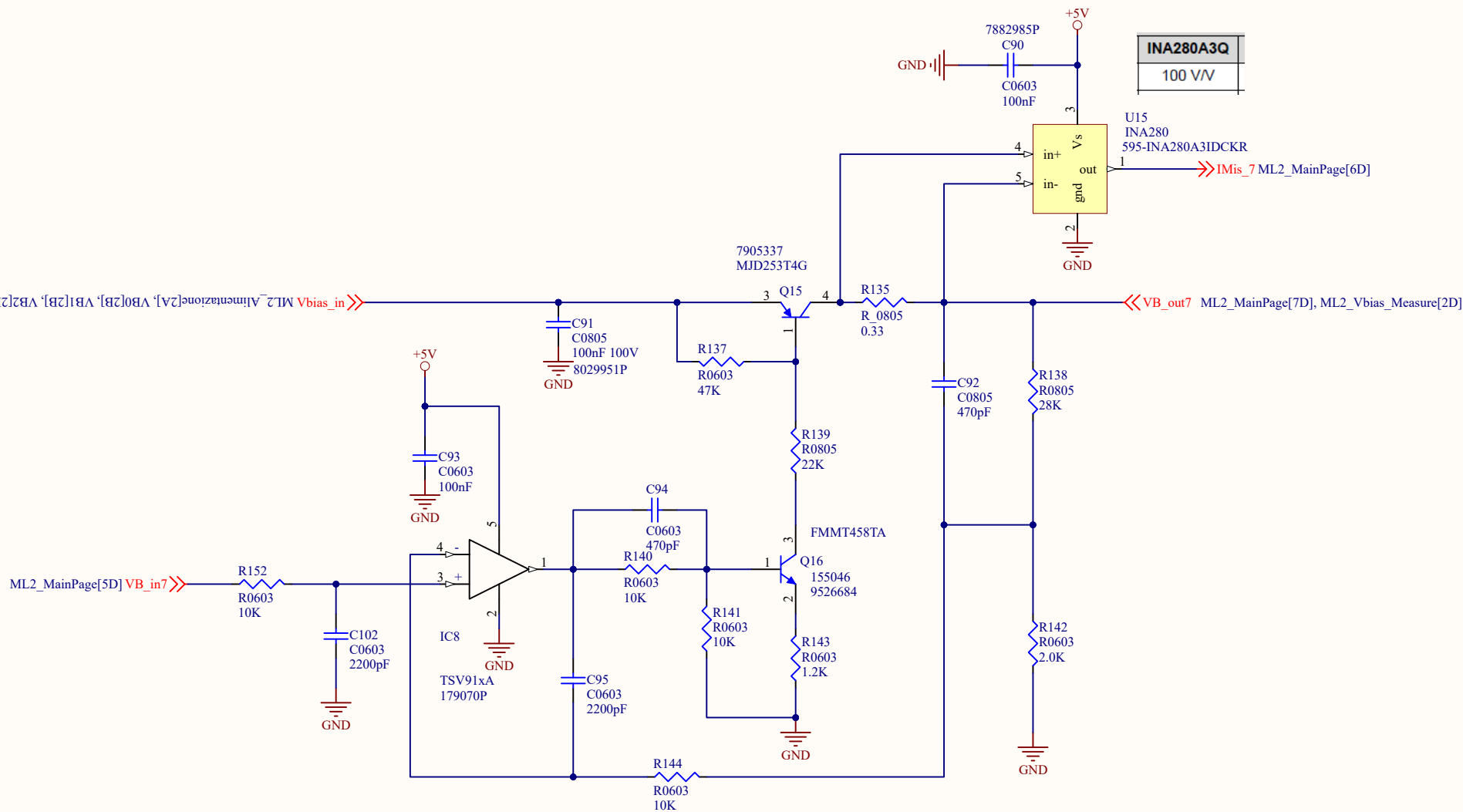
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Title		
Size A4	Number	Revision
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File: C:\Users\...\VB3.SchDoc	Drawn By:	



Title		
Size A4	Number	Revision
Date: 5/02/2023	Sheet of	
File: C:\Users\...\VB4.SchDoc	Drawn By:	



Title		
Size	Number	Revision
A4		
Date:	5/02/2023	Sheet of
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1

2

3

4

A

A

B

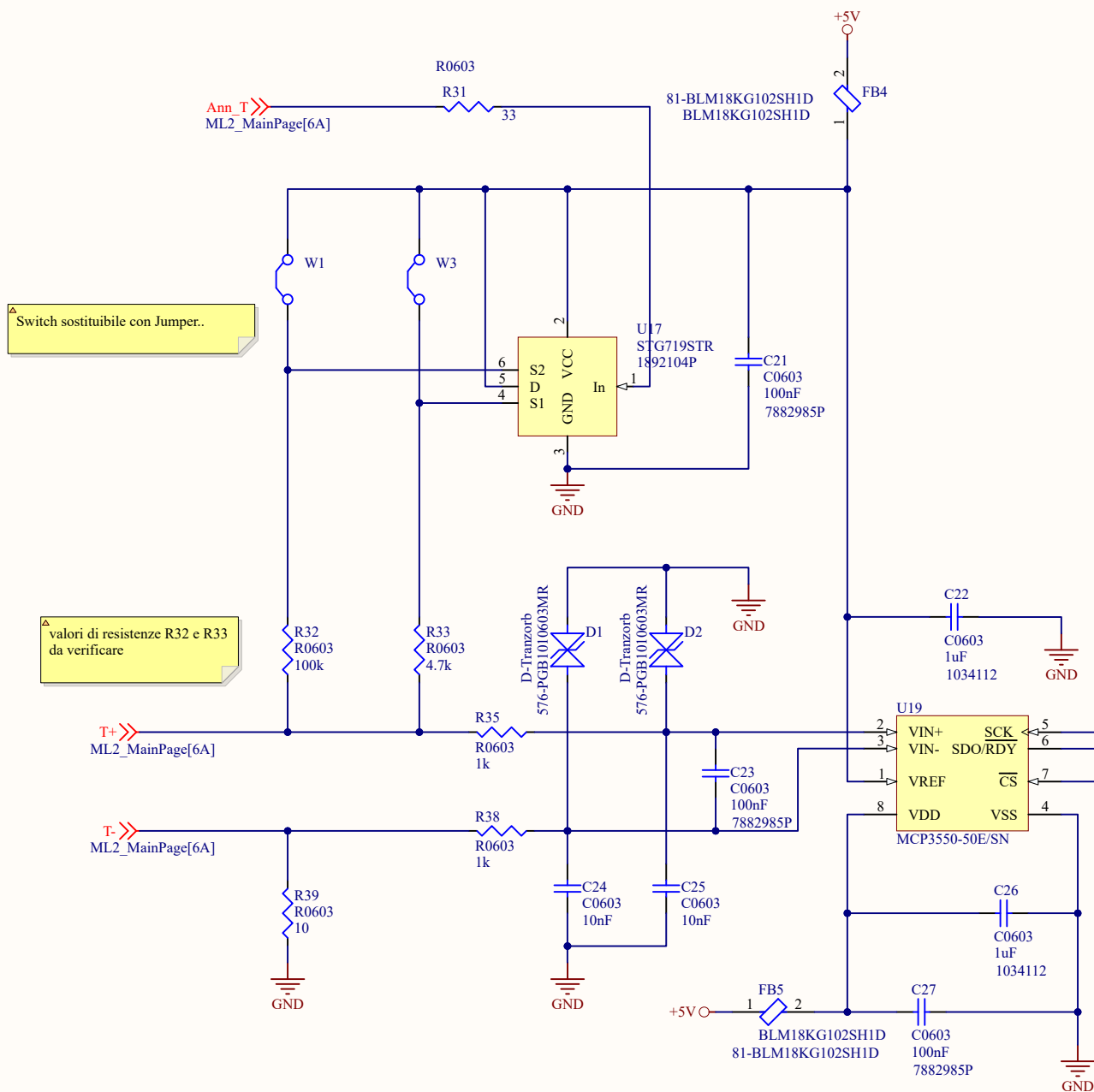
B

C

C

D

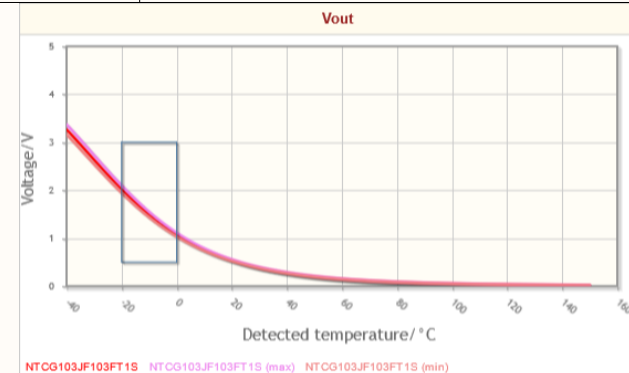
D



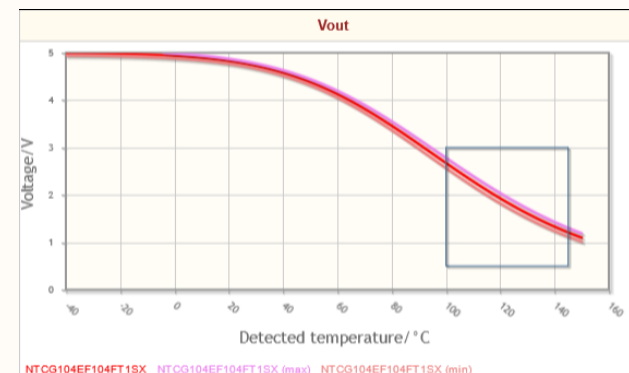
Switch sostituibile con Jumper..

valori di resistenze R32 e R33 da verificare

Partitore con 100K



Partitore con 4.7K



Title		
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A4		
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1

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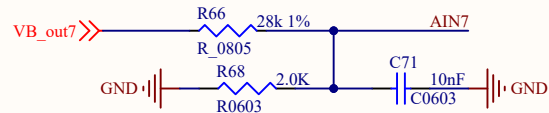
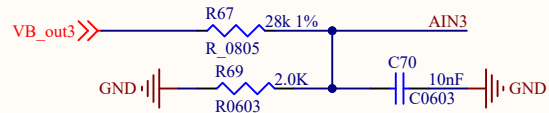
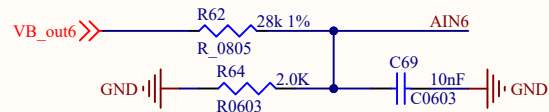
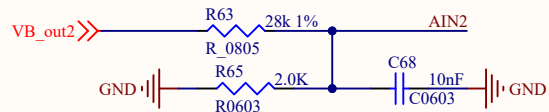
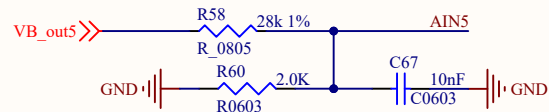
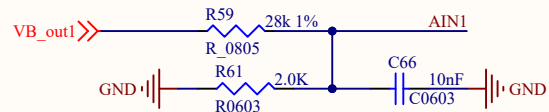
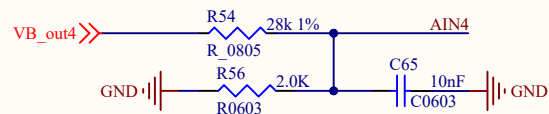
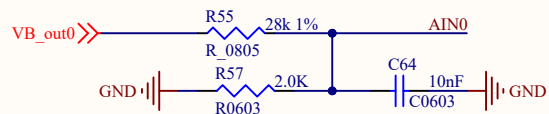
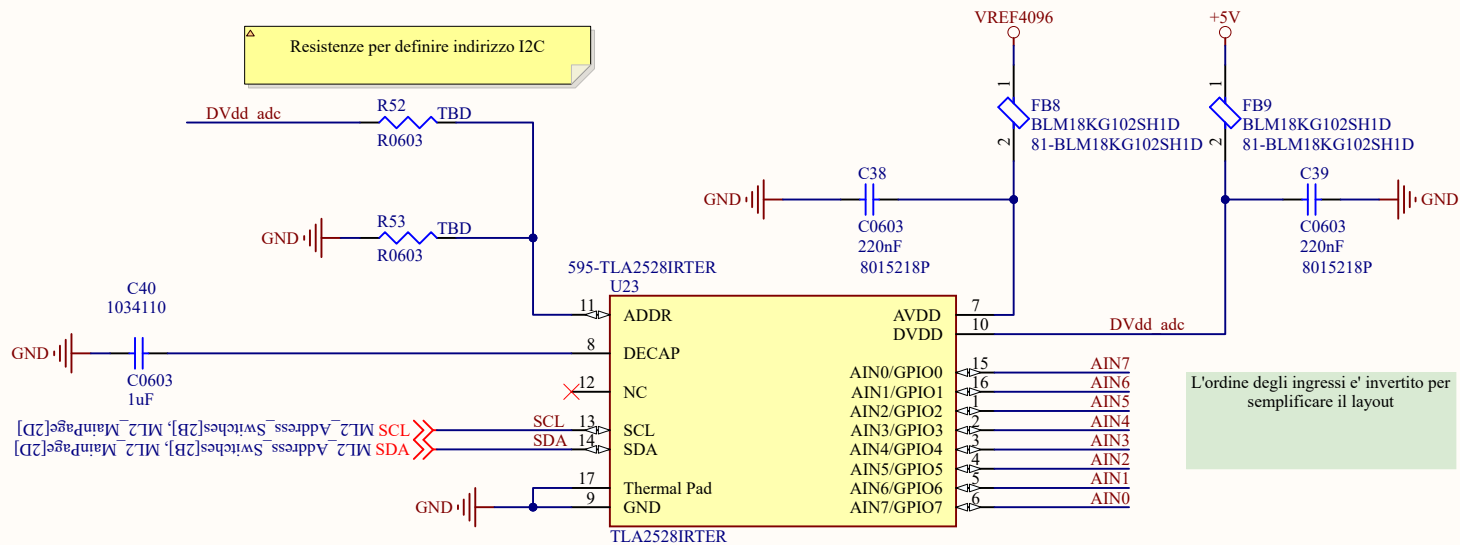
4

1

2

3

4



Title		
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