



JUNO-TAO FEB board pre-production test

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JUNO-TAO FEB board





- 1 FEB for each tile.
- 2 channels for each FEB.
- 4024 tiles/8048 channels.
- Analog signals from FEB will be transferred to FEC via differential pairs, 3-4 m inside the SS tank, and then about 10 m outside the tank.

The FEB (Front End Board) boards will be paired to the SiPMs tiles and hosted on the copper shell inside the JUNO-TAO cryostat. They will amplify the charge signal from the SiPMs, convert it in a voltage signal and send it to the FEC (Front End Controller).



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FEB board





- SiPM elements (4x4 array) of each tile are splitted in 4 Transimpedance Amplifier (TIA).
- Each TIA reads out 4 SiPM elements.
- Two gain stages (TIA + Adder) reduces TIA instability.
- 2 Vpp output dynamic
- Dynamic range 1-250 photoelectrons
- Low background PCB
 - It works at -50°C

output

signals

connector

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FEB board pre-production test bench

- Power/ground lines and FEB output lines decoupling is checked to prevent from short circuit.
- Power supply test is done, i.e. check that the current is about 100 mA for both voltages +2V and -2V.
- Pulser tests are done for all the 4 FEB inputs.



The voltage signal sourced by the pulser is sent both to the oscilloscope and to an adapter board, that does the signal voltage-tocharge conversion.

The charge signal provided by the adapter board is then amplified by the FEB board and the differential output signal is sent to the oscilloscope by means of an active probe.





FEB gain linearity



- 93 FEB tested
- FEB max output voltage vs integral input voltage is plotted
- For each FEB linearity plots are produced for 4 channels









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- Gain distribution of 93 FEB boards is plotted for the 4 channels
- Width values are about 4%









- ✓ 50 FEB boards already sent in China to the experiment site.
- We are completing the test for the remaining 7 boards, in order to have all the 100 preproduction boards fully tested.
- We are developing an automatic test procedure in order to test all the production FEB boards (about 4050 boards). A very simple and fast system can be implemented to test and characterize FEBs with a simple microcontroller.
- A FEB Tester board was already designed by our group, in order to perform this kind of automatic test. Gerber file was sent to the manufacturer.



