MDI:
from here to the TDR

E.P.

What is the time line?

- More than ever: the sooner the better
  - backgrounds influences the design of the whole detector

Main question

- The present design (with confortable backgrounds) is based on "Siamese twins" QD0: new concept never built before

  - prototype will be build and tested in ~ 1 year (?) who?
    We are just "theoreticians" in the QD0 team...

  - should we pursue from now a fail-safe more conservative design? (un-confortable quantity of tungsten and CPU time needed in this case...)

  - impact on all the detectors

Background studies

- Team and resources

  - More people needed by some subdetectors: DCH, PID, SVT

  - Core Geant4 team: at present Giovanni Marchiori and me, shortly a third person with years of expertise will join us from Atlas,

  - CPU resources & disk space:

    - Order of 10^4 cpu hours, 1 Tbyte

    - Central repository? Where? Book-keeping: who?

Consistency

- We will simulate the whole thing gathering information from the whole community

- Clear documentation from all the sources is mandatory