Workshop sul Calcolo nell'I.N.F.N. Loano 22 - 26 maggio 2023





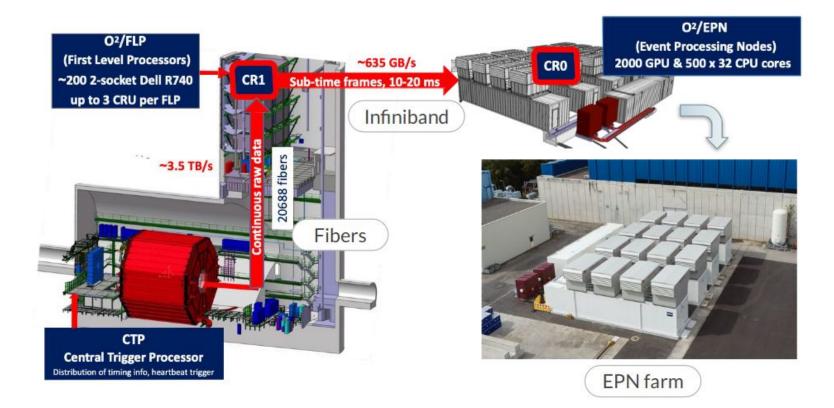
Calcolo ALICE in Run 3 su GPU e architetture eterogenee: validazione e attività in produzione

F. Noferini INFN sez. Bologna

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ALICE DATA flow in Run-3





ALICE on heterogeneous architectures

- ALICE underwent several upgrades in view of Run 3 to deal with a much higher luminosity wrt Run 1/2
- One of the major upgrades is O² (offline-online) software/framework with the goal to support continuous readout (triggerless) in data taking and synchronous processing/reconstruction
- Software was completely revised to allow for
 - a high level of paralyzation in all steps (data processing, MC, analysis)
 - allowing to exploit GPU resources (strictly retired for synchronous processing)
 - providing a common framework to cover all sync and syncs needs (data processing layer, DPL)
 - I/O and CPU optimization in analysis (RDataFrame)
 - o ...



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O² software is flexible enough to support also other king of resources, e.g. HPC and ARM



Potential applications

There are 3 potential areas where ALICE activity can be expanded (in **bold** what will be covered in this talk)

- GPU
 - The role of GPUs in ALICE already presented in many discussion (e.g. <u>link</u>, <u>link2</u>). In this talk we will focus on its effectiveness in production activity

- HPC
 - Some results on MC simulation were presented in the past \rightarrow Marconi/Cineca (<u>link</u>) and other (<u>link</u>)

- ARM
 - Some preliminary studies were performed and reported in this talk



Using GPU in Run 3 (reconstruction) with ALICE O²

Processing on dedicated farm at experimental site

- 250x Event Processing Nodes (EPNs) with 2x32 core CPUs (to be expanded soon)
- 8x AMD Graphic Processing Units (GPUs)
- ~1600 GPUs required to process 50 kHz Pb-Pb collisions

\rightarrow GPU usage is mandatory for sync reconstruction and calibration

- > All GPU software written in a generic way
- Same software runs on GPUs of different vendors and on the CPU

ALICE reconstruction

- Rough corrections/calibrations for all detectors
- Full reconstruction of TPC (data reduction on GPU + space distortion corrections)
- TPC ITS tracks matching (for a small subsample)
- Tracks propagation to outer detectors (TRD, TOF)
- Global track fits
- Primary and secondary vertices
- PID hypothesis
- CTF and calibrations as output

When the EPN farm is not (fully) used for synch. processing, it will be used for asynch. processing of the raw data stored on the disk buffer EPN will perform ~1/3 of the Pb-Pb asynchronous processing

ASYNC

- Full correction of TPC distortions (nominal resolution), full calibration for all detectors
- TPC ITS tracks matching
- Tracks propagation to outer detectors (TRD, TOF)
- Global track fits
- Primary and secondary vertices
- PID hypothesis
- Calibration/QC and AOD as output
- Different relative importance of GPU / CPU algorithms compared to synchronous processing
- TPC part faster than in synchronous processing (less hits, no clustering, no compression





Reconstruction time covered by GPUs

Synchronous processing (50 kHz Pb-Pb, MC data, processing only)

Asynchronous processing (650 kHz pp, real data, calorimeters not in run)

Processing step	% of time	Processing step	% of time
TPC Processing (Tracking, Clustering, Compression)	99.37 %	TPC Processing (Tracking)	61.41 %
EMCAL Processing	0.20 %	ITS TPC Matching	6.13 %
ITS Processing (Clustering + Tracking)	0.10 %	MCH Clusterization	6.13 %
TPC Entropy Encoder	0.10 %	TPC Entropy Decoder	4.65 %
ITS-TPC Matching	0.09 %	ITS Tracking	4.16 %
MFT Processing	0.02 %	TOF Matching	4.12 %
TOF Processing	0.01 %	TRD Tracking	3.95 %
TOF Global Matching	0.01 %	MCH Tracking	2.02 %
PHOS / CPV Entropy Coder	0.01 %	AOD Production	0.88 %
ITS Entropy Coder	0.01 %	Quality Control	4.00 %
Rest	0.08 %	Rest	2.32 %

Running on GPU in baseline scenario

Running on GPU in optimistic scenario



Reconstruction time covered by GPUs

Max ~6.5x speedup, since 85% of the compute power is in the GPU

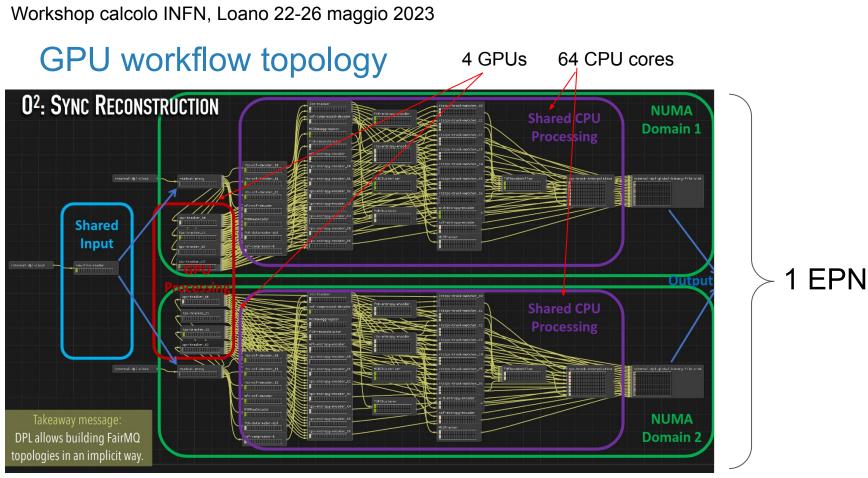
- **Today**, offloading the ~60% of the async to the GPU should yield a speedup around **2.5x**.
- We remove 60% of the CPU time, while we are still CPU-bound, but we have some overhead CPU resources for driving the 8 GPUs.
- In the optimistic scenario, by offloading 80% we might get close to **5x**.

Asynchronous processing (650 kHz pp, real data, calorimeters not in run)

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TPC Processing (Tracking)	61.41 %
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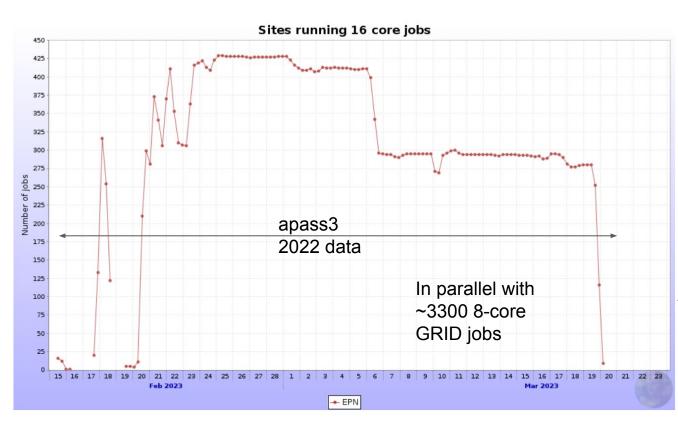


Credits G. Eulisse and D. Rohr, CHEP 2023

ALICE



Production activity on EPN (GPU)



Full reprocessing of 2022 HIR pp@13.6 TeV data profited of EPN node availability

1/3 of the processing on EPN (conf 2 NUMA domains but only 1 GPU+64 cores per domain).

Next processing on EPN is now configured to run with 2 FULL NUMA domains (4 GPU, 64 cores per domain): 1 EPN job/domain expected to replace (today) ~16-20 GRID/CPU 8-core jobs



Test on ARM architecture

We joined tests on an ARM node provided by E4 (one test node with 256 cores, 227 GB ram) in the context of Spoke-2

Requirements:

- cvmfs and local software installation
- Network access to external ALICE CCDB (ALICE token auth. → grid cert)
- CentosOS

Test full workflows for

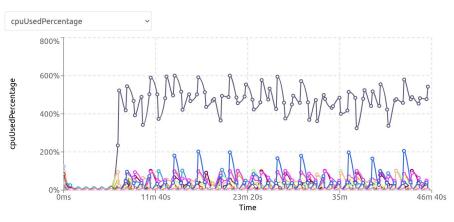
- Data reconstruction (GRID configuration)
- Full simulation

NB|| build on cvmfs for arm were sporadically provided (now built by default for all official production tag)



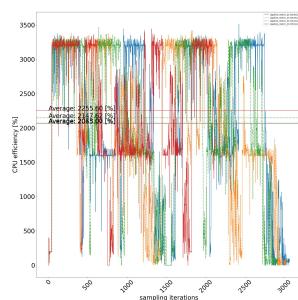
Test on ARM architecture (II)

Data reconstruction



- GRID setting tuned for 8 cores per node
- CPU efficiency consistent with what observed in the GRID node
- Physics results consistent with the one of GRID jobs (unfortunately we cannot test with exactly the same software tag)

NB|| CPU usage improved

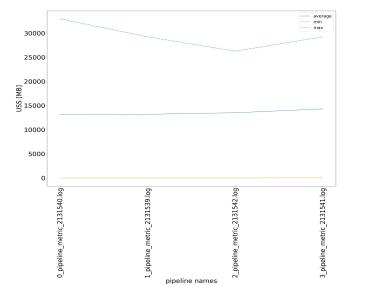


Simulation

4 concurrent 16 workers MC job (actually we fill half of the node in this test)

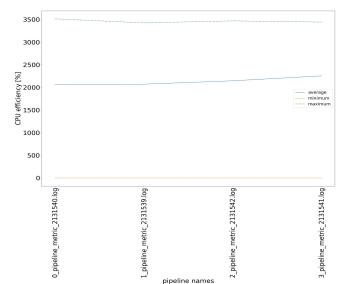
MC workflow is more aggressive than data reconstruction (cpu overbooking). If more CPU are available it will use them (up to a factor 2 more). RAM usage is the hard limit (2GB/core)

Test on ARM architecture (III)



Simulation

PbPb 10TFx50ev=500ev x 4job = 2000ev export NWORKERS=16 export CPULIMIT=16 export MEMLIMIT=32000 \rightarrow Tot 64 cpu, 128 GB mem

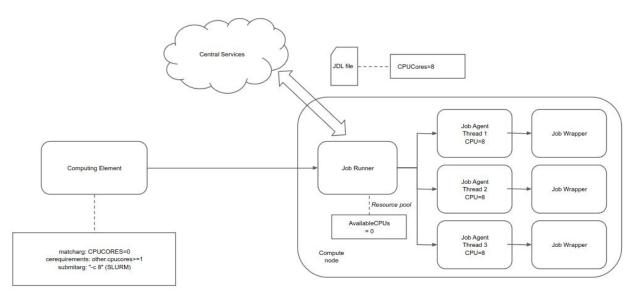


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Whole node configuration

Whole-node Scheduling Run Through



Credits S. Weisz , ALICE Tiers workshop 2022



A new design of the Job runner in the worker node allows to instantiate more Job Agents (tasks) in a single slots and assign to them part of the resources available (e.g. in a 8-core slot we can run 8 single-core tasks)

This level of flexibility allows us to deal with a whole node configuration (full node reserved to an ALICE job)

- ideally for HPC resources
- potentially also in other scenarios



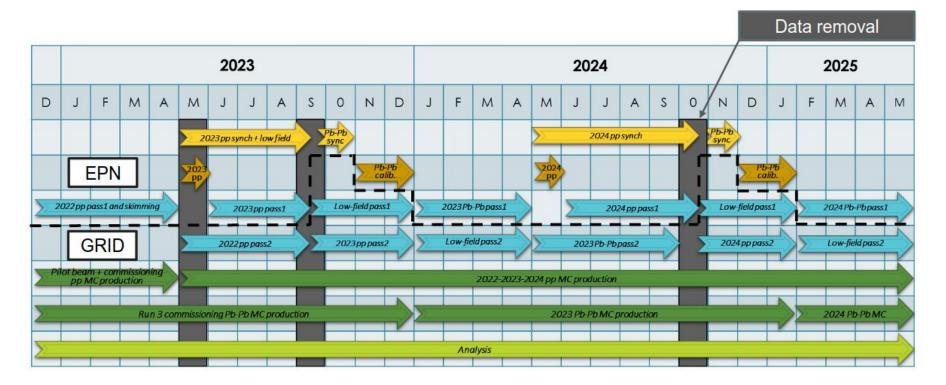
Thanks for your attention!







2023-2024 processing timeline





Generic software and GPU Benchmarks

Vendor/architecture-independent software:

- All algorithms are written in generic C++, and can be dispatched to HIP, CUDA, OpenCL on GPUs or OpenMP on CPUs using small wrappers → good code maintainability
- GPU libraries linked dynamically on demand → can distribute same binary software to CPU and GPU nodes
- Benchmarking of the synchronous software completed in August 2020:
 - GPU performance @ 50kHz Pb-Pb
 - ~1600 AMD MI50 and ~1100 NVIDIA Quadro RTX 6000
 - Compatible with our previous estimates <2000 GPU including 20% margin
 - GPU Memory optimization
 - 128 orbit TF (~ 11 ms) needs 24 GB
 - EPN Full System Tests performed with 70 orbit TF
 - Validated processing rate of 1/230 of assumed rate at 50 kHz Pb-Pb (nominal 1/250)
 - Max. server memory consumption 280 GB and CPU load 44 cores (+20% in the final setup)