

Thursday	Friday	Saturday
9:00 Plenary	9:00 Parallel	9:00 Plenary
30 min - Project overview 30 min - Report from Valencia 30min - Accelerator overview	- SVT - PID - IFR - EMC	- BKGD - SVT - DCH - PID - IFC - Elec / Trigger / DAQ - Computing - Summary & Outlook
10:30 Break		
11:00 Plenary	10:30 Break	
100 min - Workshop planning - Detector - Elements of sub system - R&D, computing & BKGD	11:00 Parallel	
	- SVT - IFR	
13:00 Lunch	13:00 Lunch	
13:30		
- European Project (Computing & Background)	- BKGD - Electronics	
14:00 Break	15:30 Break	
14:30 Parallel	16:00 Plenary	Preliminary Parallel Sessions Topics & Organizer
- BKGD - DCH - EMC - Computing	- Computing	
	17:00	
	- Discussion with system Conveners	Comp - Morandin BKGD - Calderini SVT - Rizzo DCH - Finocchiaro PID - Leith EMC - Hitlin IFC - Calabrese Elec - Trigger/ DAQ - Dubois/Luitz ?
16:00 Parallel	19:00 Dinner	
- BKGD - PID - Elec / Trigger / DAQ - Computing		