

Fine Time Measurements Calibration

4 TDCs for each pixel, 4 pixel for each column, 8 columns for each chip

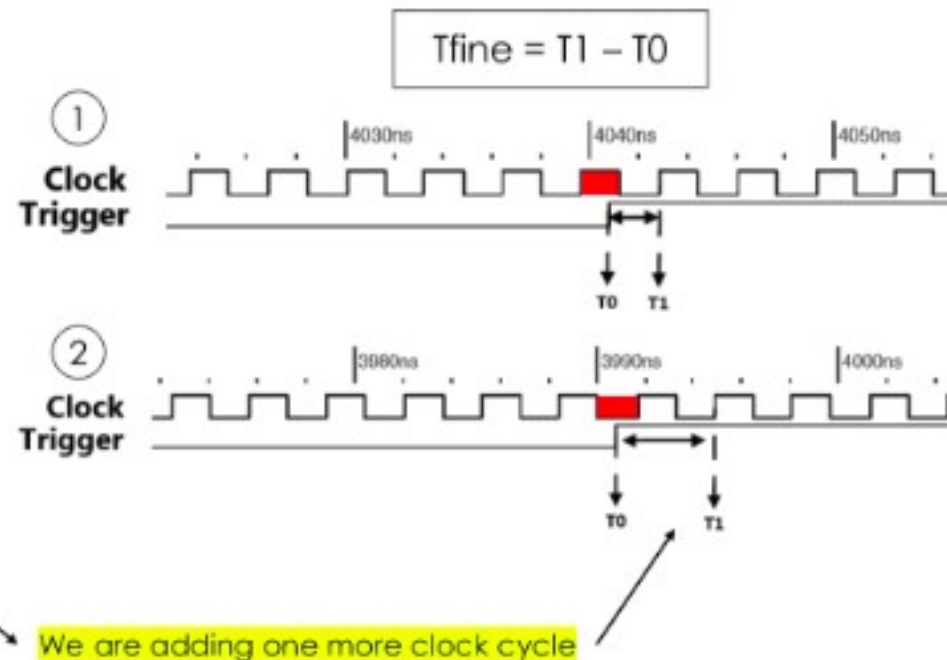
128 calibrations * 6 chip

768 total calibrations

TDC principle of operation

[source: Fabio Cossio]

- Low-power TDC with analogue interpolation to measure **phase** between event **trigger** (T_0) and **clock** (T_1)
- To avoid metastability issues
 1. If trigger occurs **BEFORE** clock falling edge the signal is sampled w.r.t. **NEXT** clock rising edge
 2. If trigger occurs **AFTER** clock falling edge the signal is sampled w.r.t. **SECOND NEXT** rising edge



Fine time measurement

[source: Fabio Cossio]

- $IF = MAX - MIN$
- $LSB = clk_period / IF$
- $CUT = (MAX + MIN) / 2$
- 32 pixels, 4 TDCs \rightarrow 128 entries LUT with MIN, MAX values
- Apply fine time correction for each hit data:

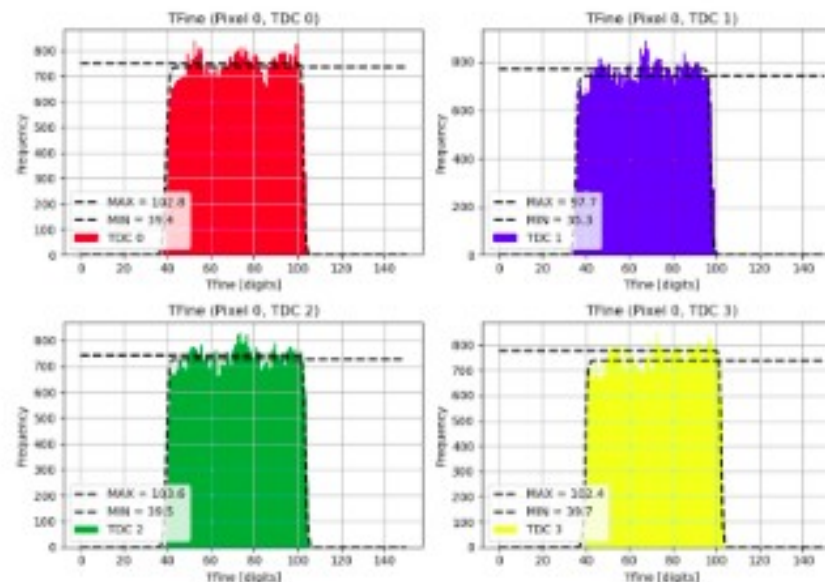
```
if Tfine < CUT:
```

```
    fine_time = ( Tfine - MIN ) / IF
```

```
if Tfine > CUT:
```

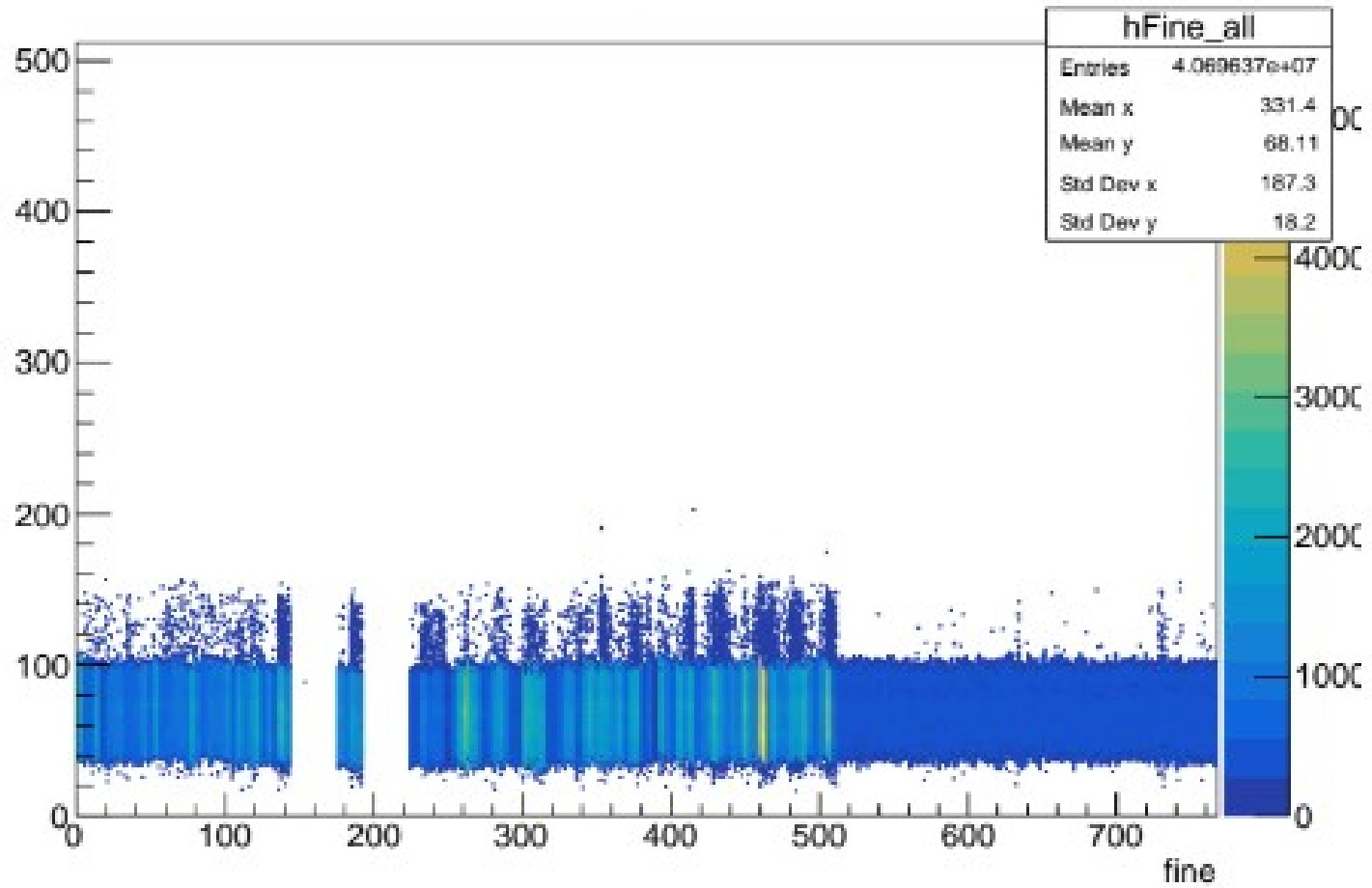
```
    fine_time = ( ( Tfine - MIN ) / IF ) - 1
```

```
Timestamp = Tcoarse - fine_time
```

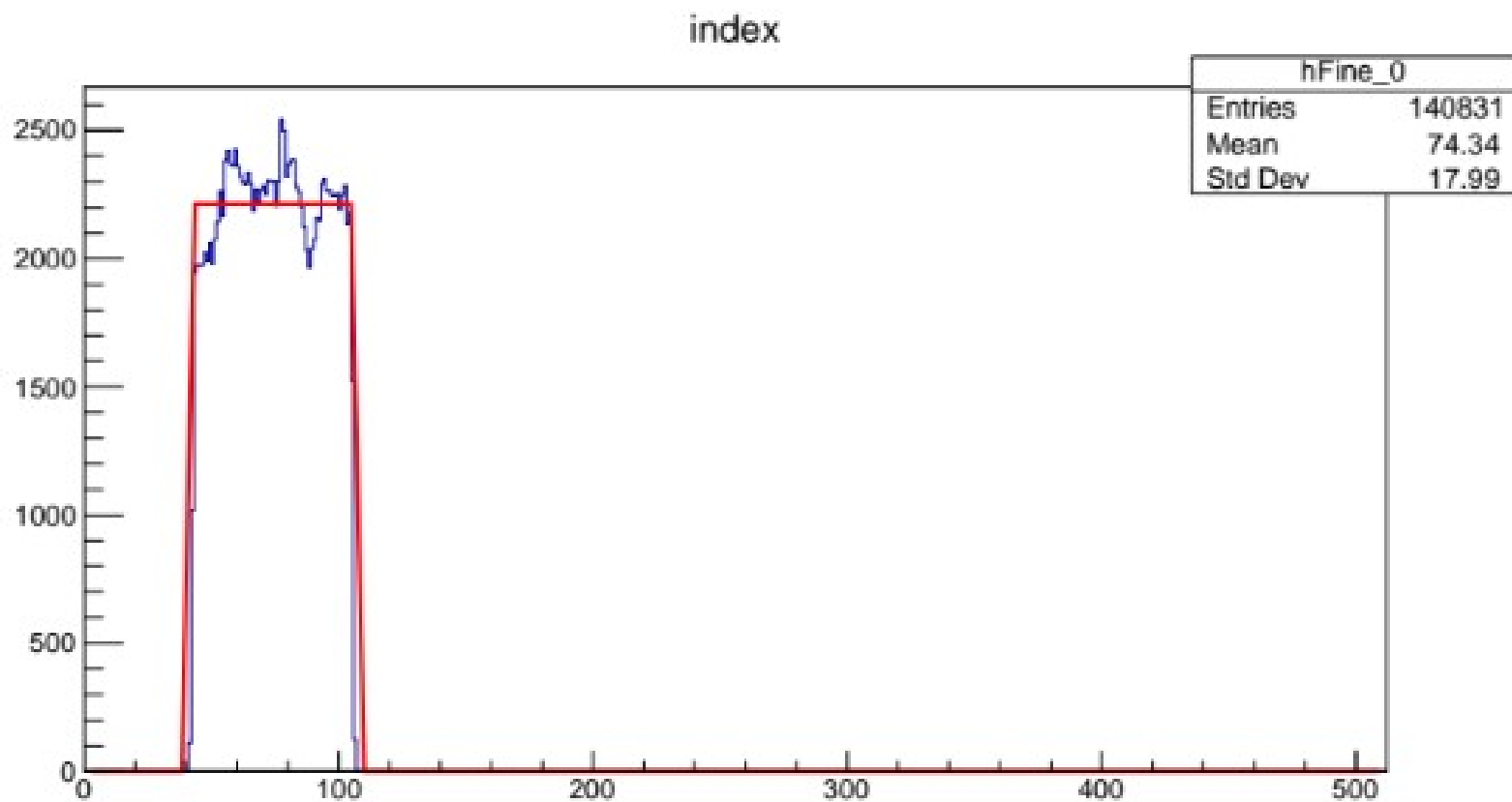


We remove the additional clock cycle

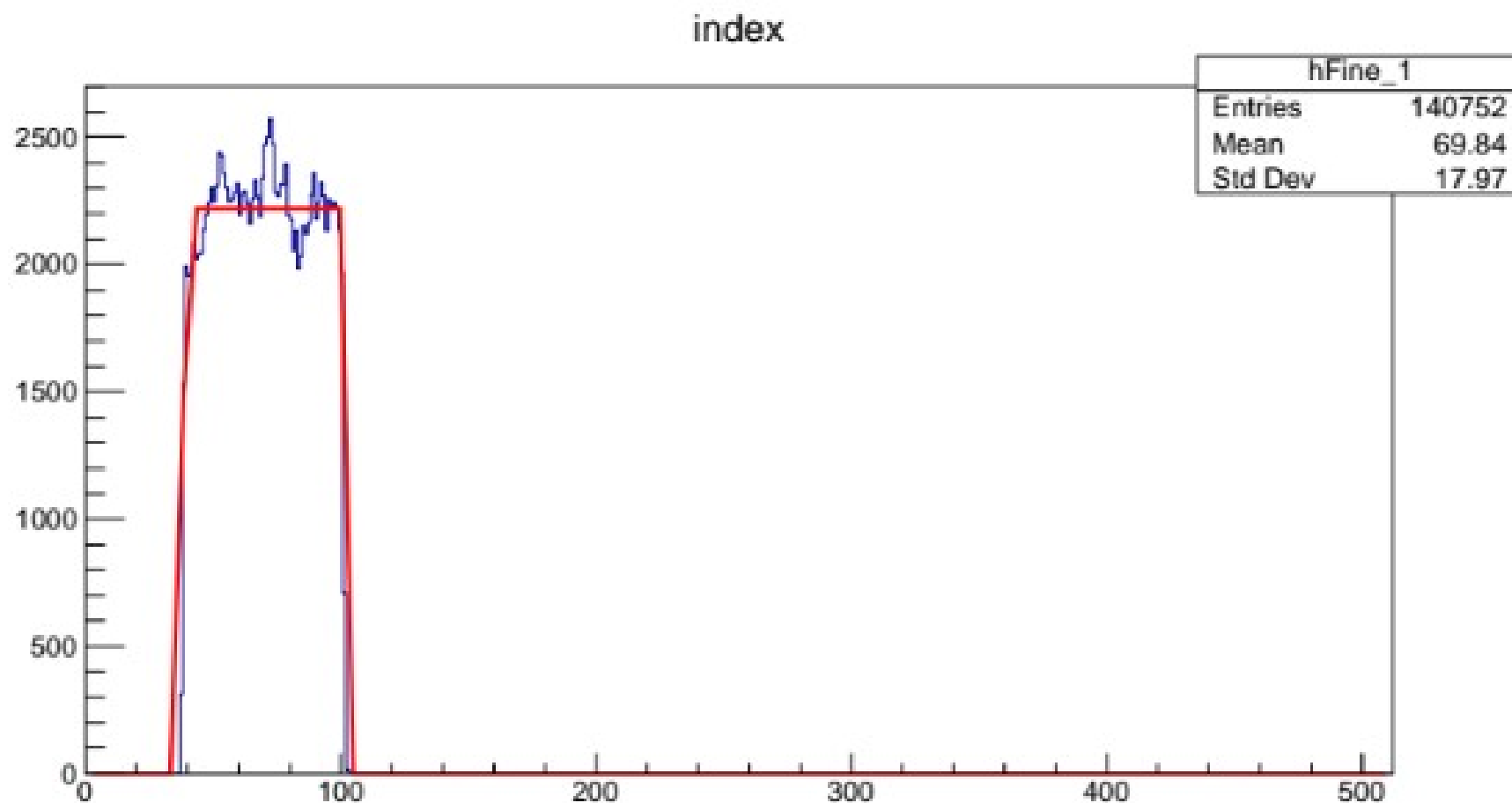
index



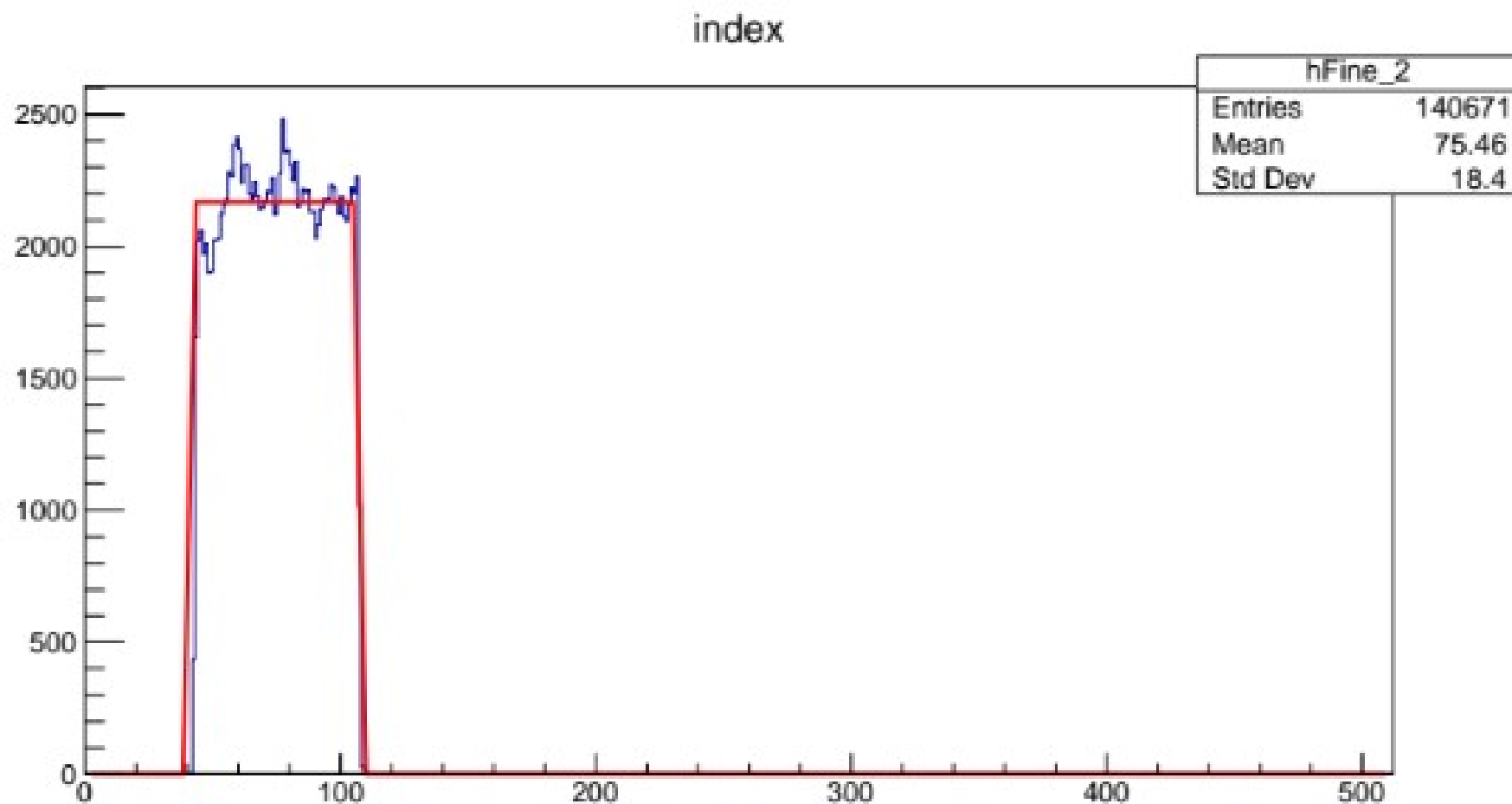
TDC 0, Channel 0, Chip 0 (HAMA1)



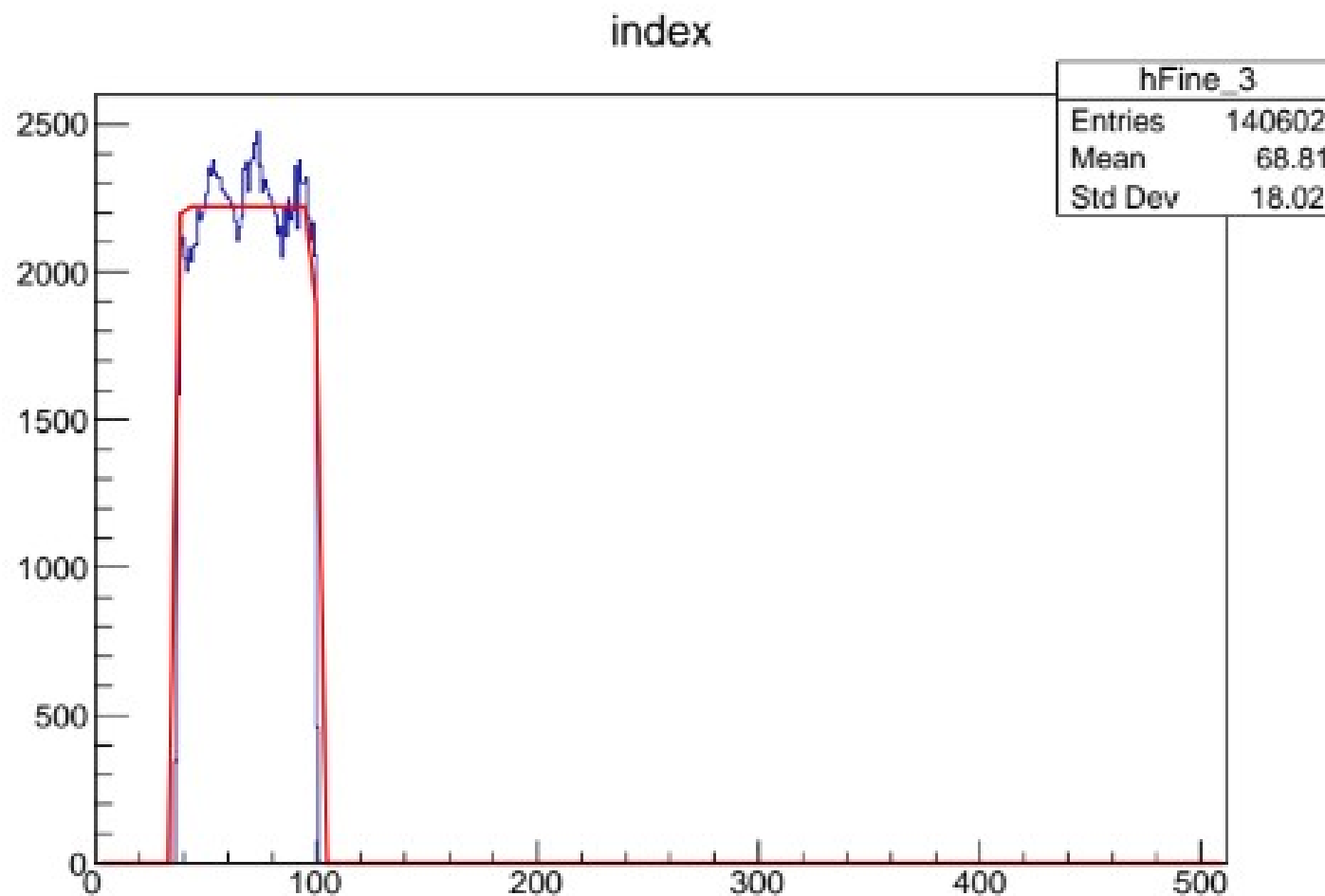
TDC 0, Channel 0, Chip 1 (FBK)



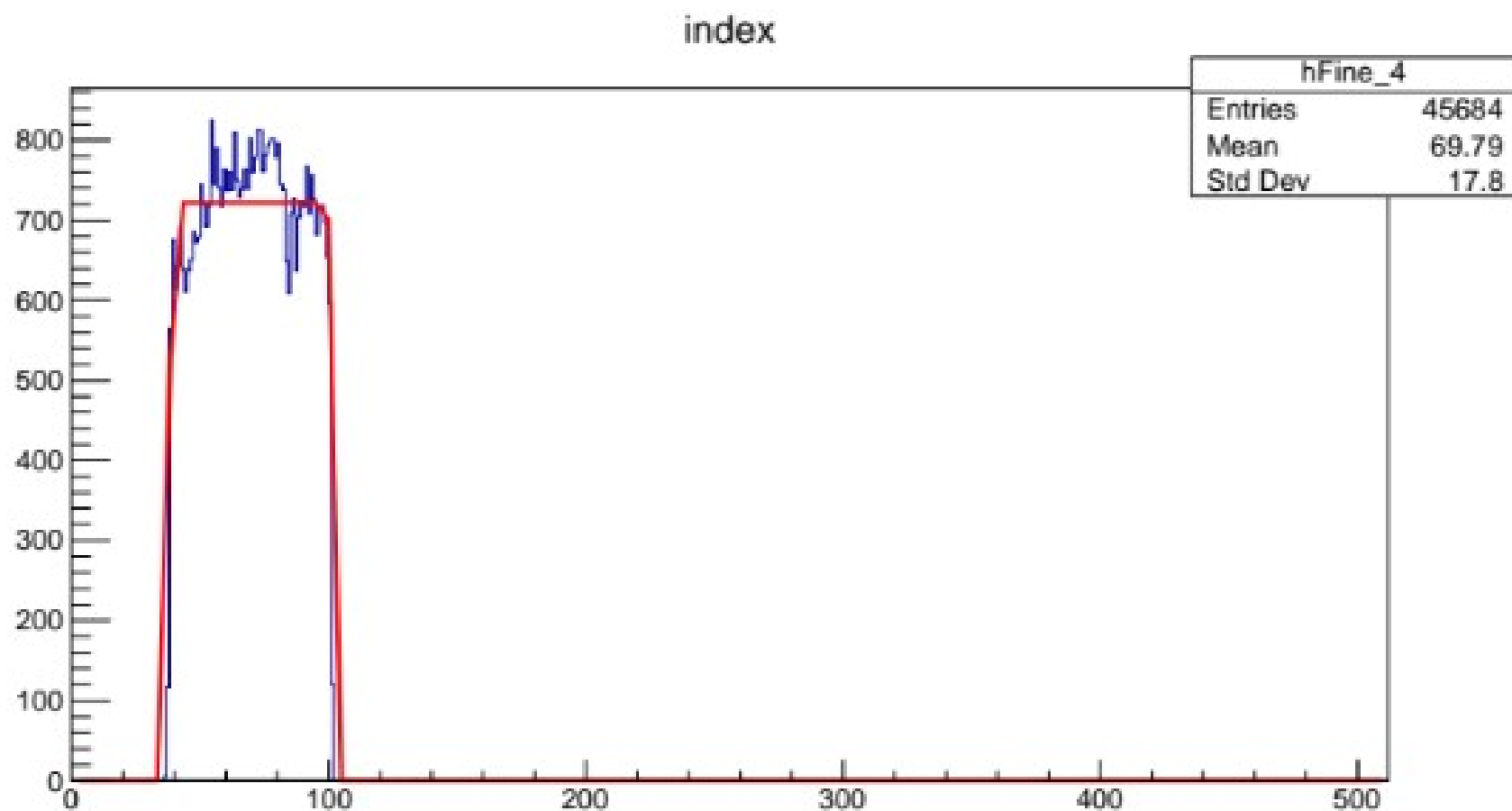
TDC 0, Channel 0, Chip 2 (HAMA2)



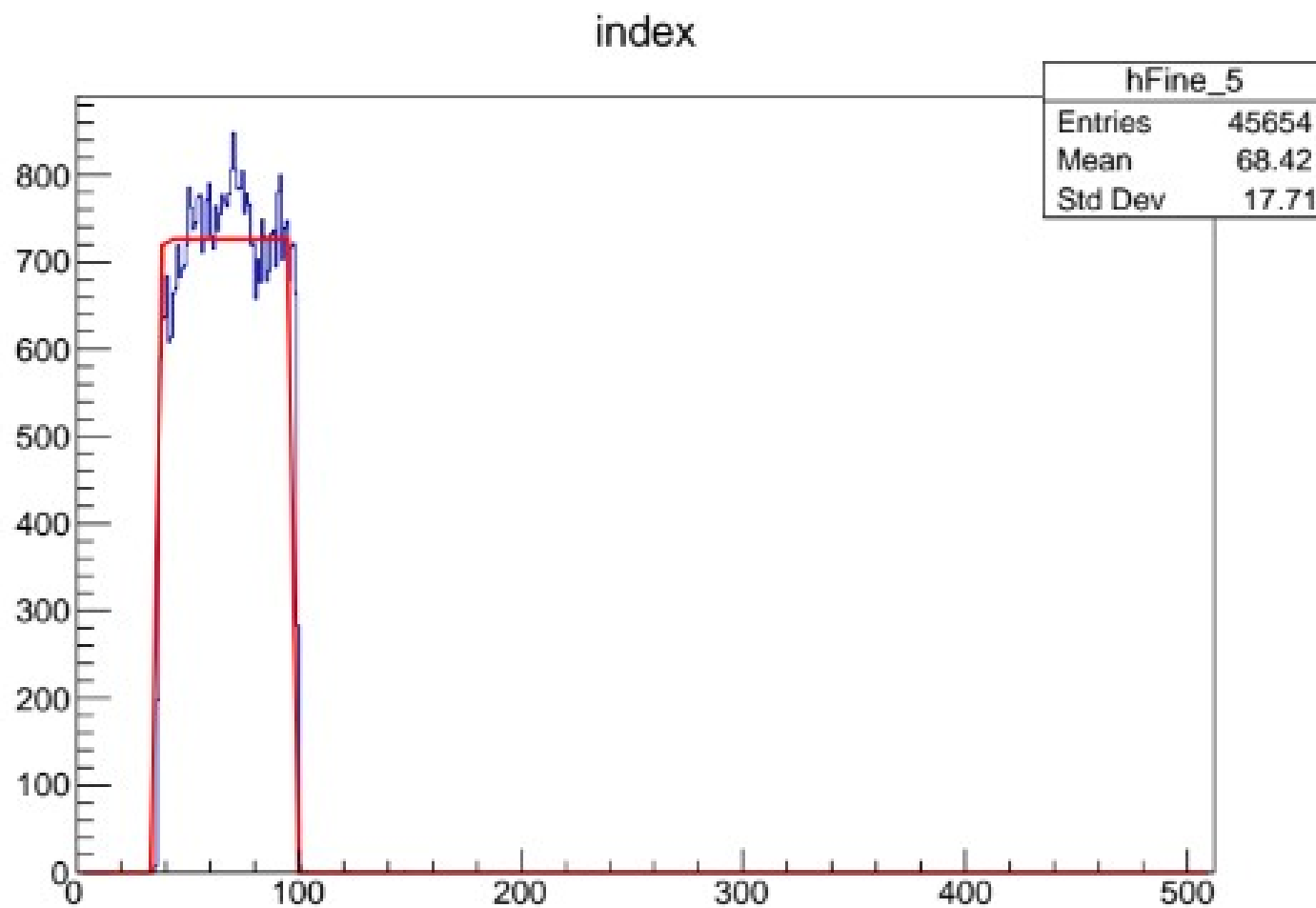
TDC 0, Channel 0, Chip 3 (SENSL)



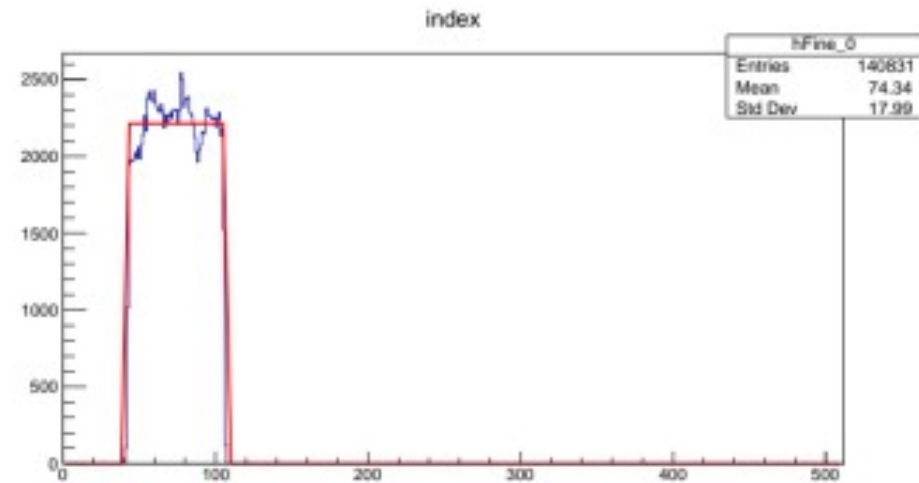
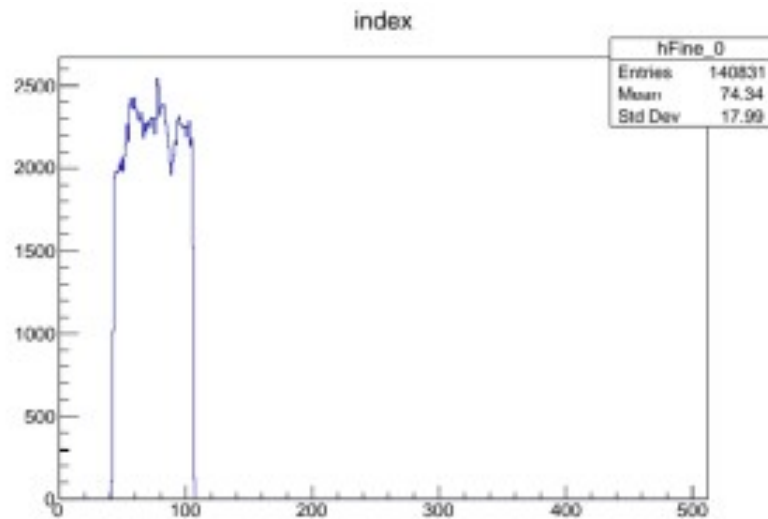
TDC 0, Channel 0, Chip 4



TDC 0, Channel 0, Chip 5

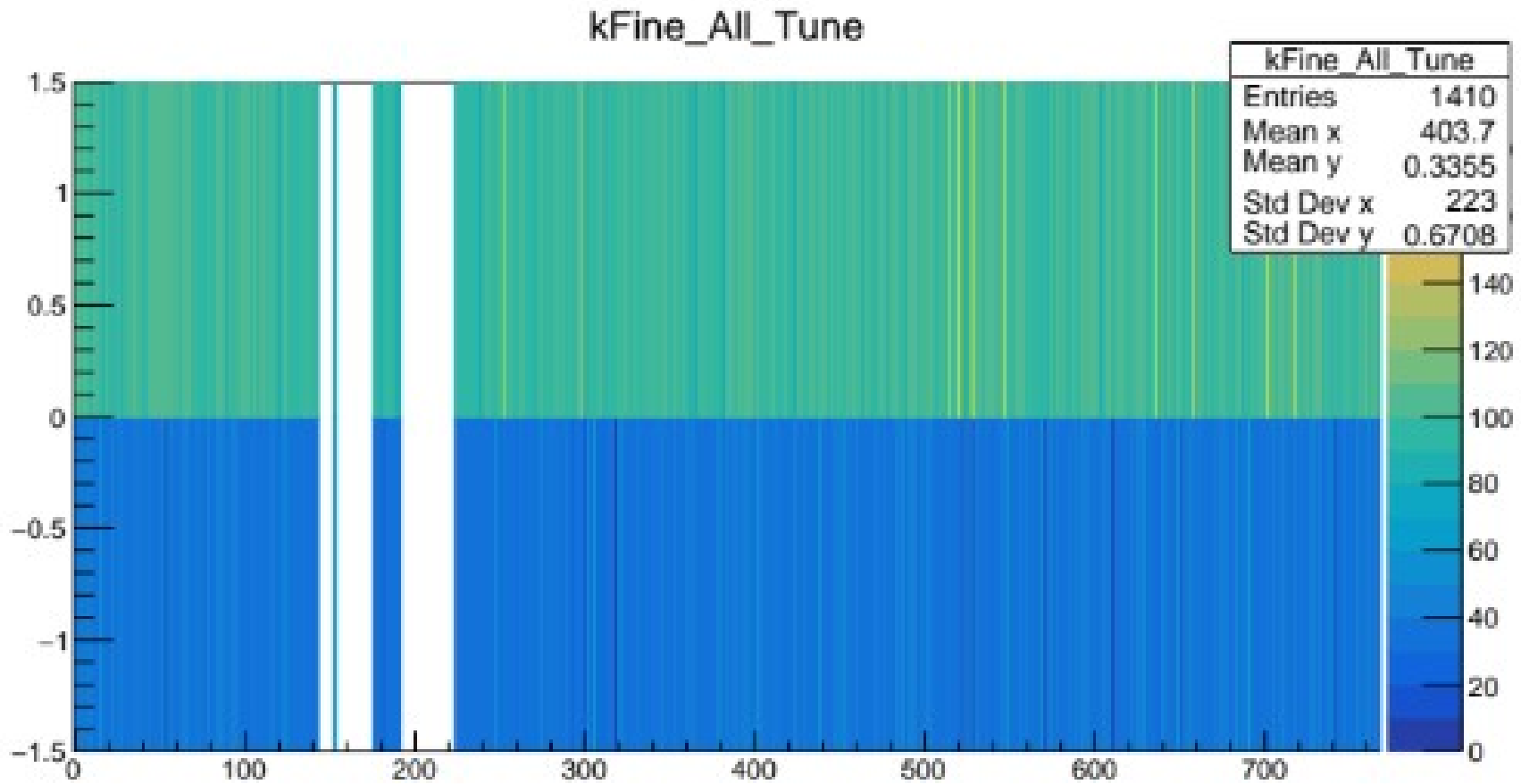


Fit & Fit Parameters

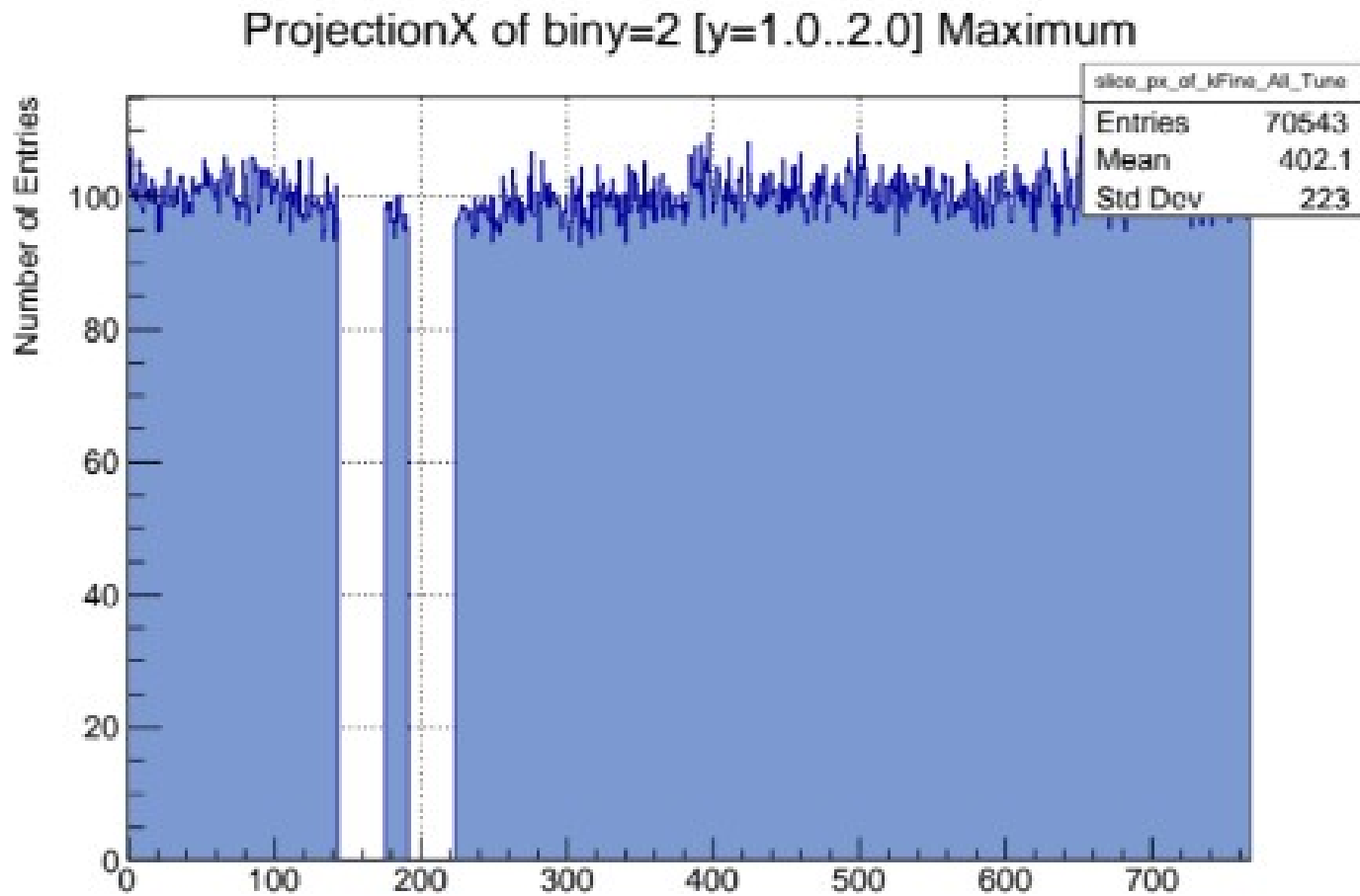


EXT NO.	PARAMETER NAME	VALUE	APPROXIMATE ERROR	STEP SIZE	FIRST DERIVATIVE
1	p0	2.21438e+03	5.67038e+00	0.00000e+00	7.01718e-01
2	p1	1.06486e+02	1.00002e+00	0.00000e+00	-4.33218e+03
3	p2	4.81133e-03	1.00000e+00	0.00000e+00	-1.26750e+04
4	p3	4.27597e+01	1.00038e+00	-0.00000e+00	4.46111e-08
5	p4	6.86736e-03	1.00000e+00	0.00000e+00	-9.14841e+06
root [6] □					

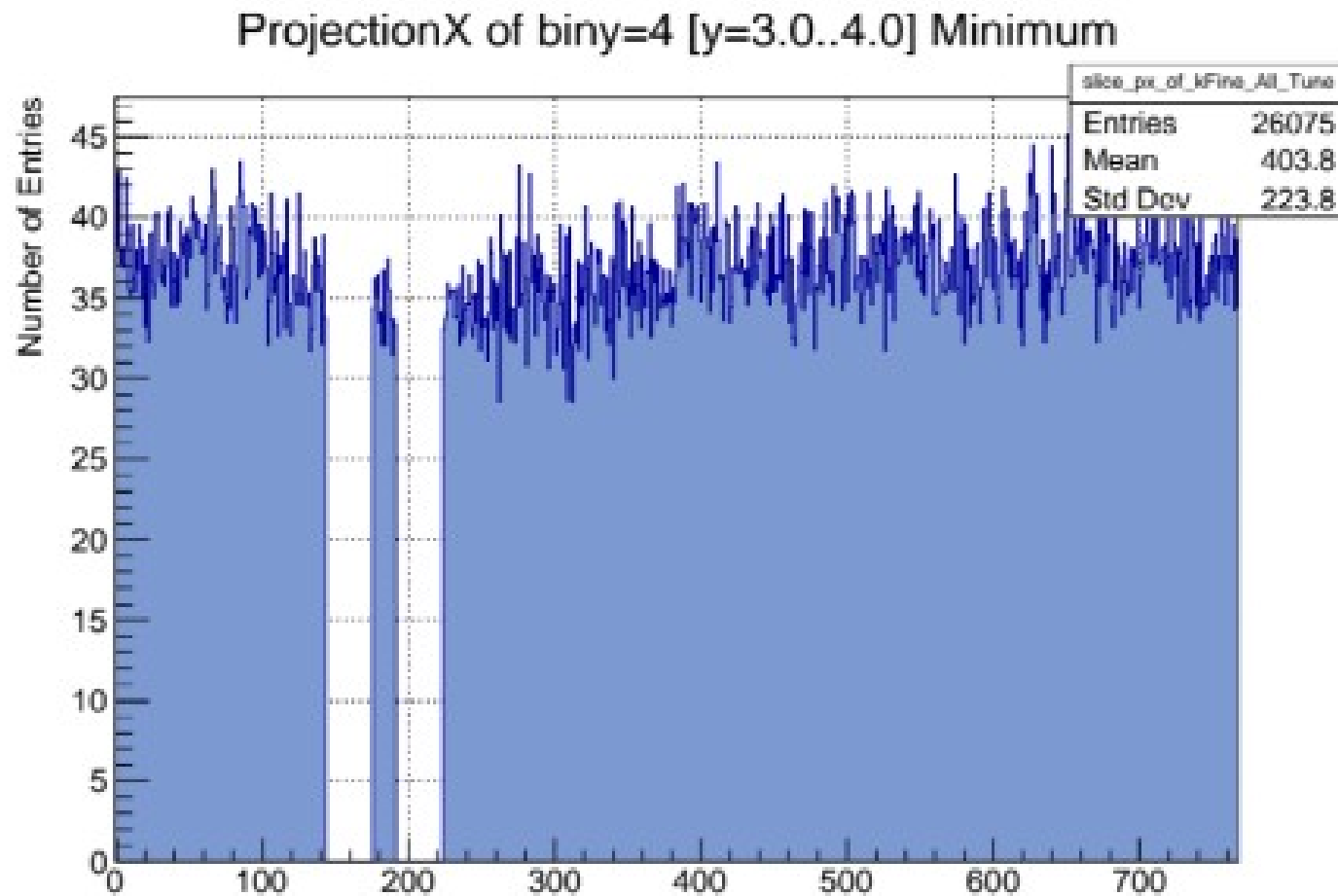
Max & Min Parameter Values



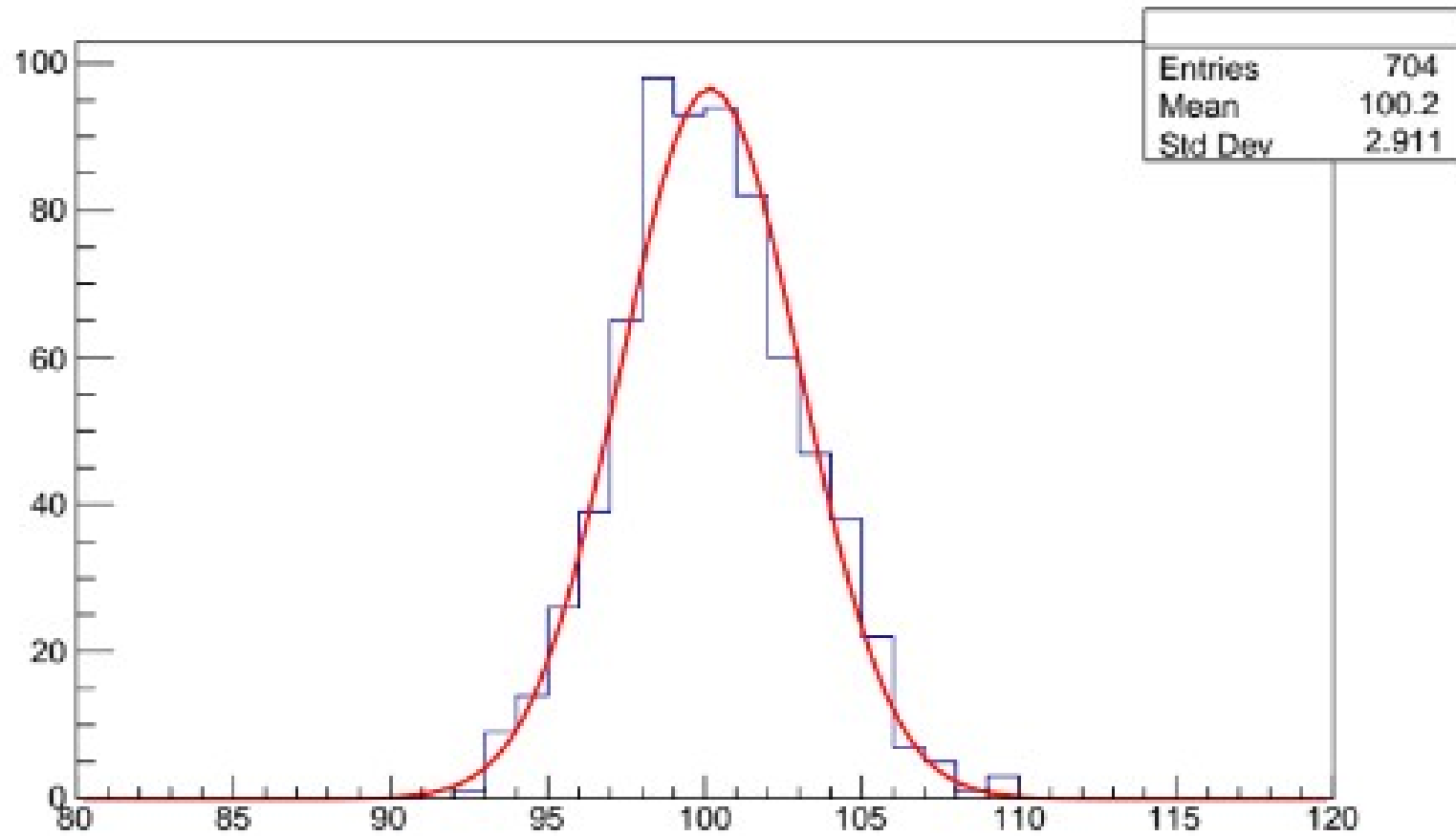
Max Values



Min Values



Max Distribution



Min Distribution

