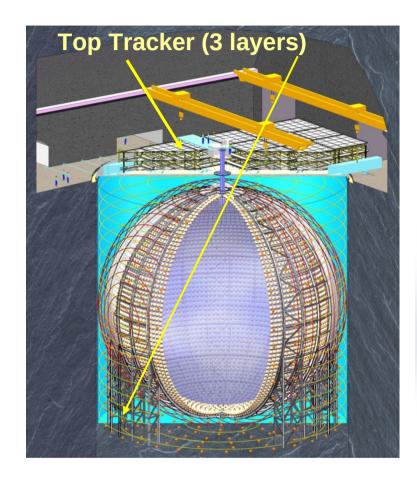
# Top Tracker electronics status

A. Paoloni on behalf of TT-electronics group

JUNO EU-AM meeting

25 October 2022

# JUNO Top Tracker



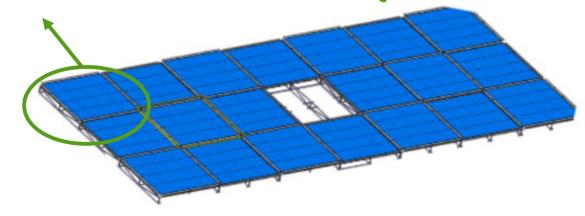
Top Tracker in numbers: 63 walls 496 modules 64000 channels Two levels trigger

#### Needed to:

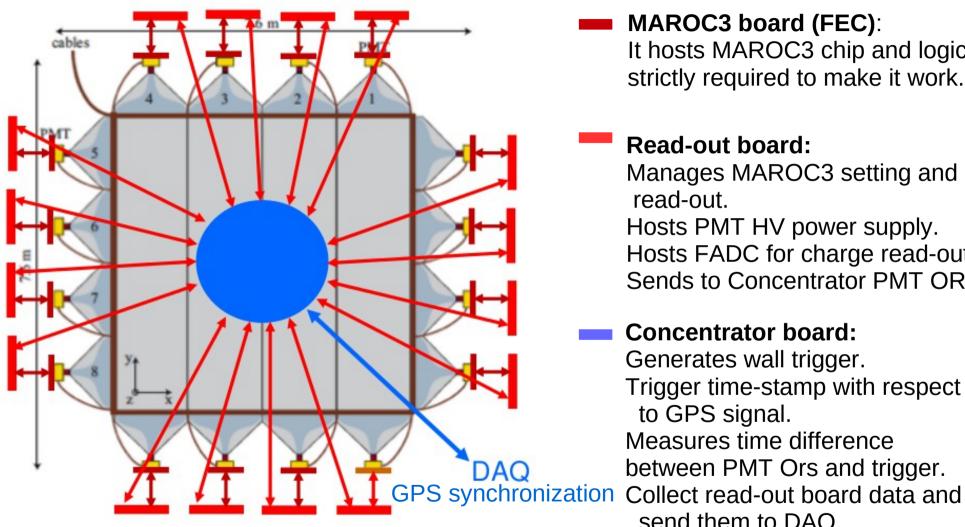
Studies on cosmogenic background. Monitor Central Detector and Water Cerenkov efficiency and tracking performances on cosmic rays.







## Top Tracker electronics



MAROC3 board (FEC): It hosts MAROC3 chip and logic

strictly required to make it work.

**Read-out board:** 

Manages MAROC3 setting and read-out.

Hosts PMT HV power supply. Hosts FADC for charge read-out. Sends to Concentrator PMT OR.

**Concentrator board:** 

Generates wall trigger.

Trigger time-stamp with respect to GPS signal.

Measures time difference

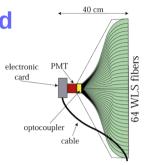
send them to DAQ.

Deliver DCS commands to readout boards.

## TT wall read-out electronics

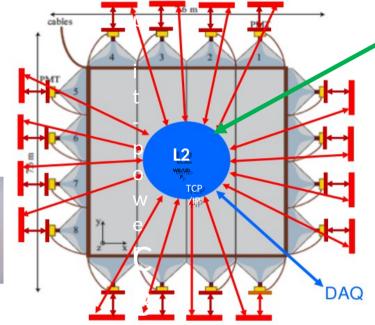
#### Split power board





H7546 Hamamatsu 64 ch MAPMT





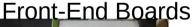
L2 trigger (GTB)



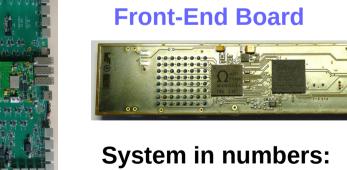


TT endcap:

Read-out and







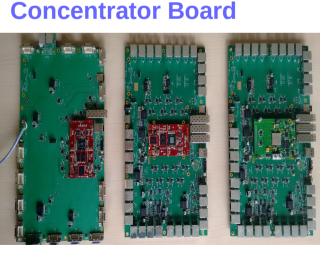


1000 FEB+ROB

**63 Concentrator + Split power boards** 

**1 L2 GTB** 

+ power supply + cabling



### TT wall read-out electronics

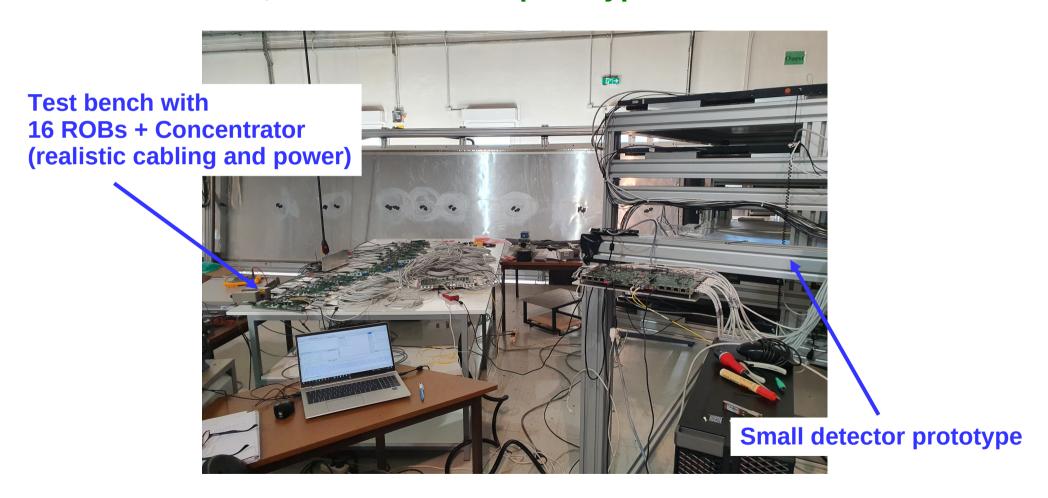
System in numbers: 1000 Front-End Boards + Read-Out Boards 63 Concentrator + Split power boards 1 L2 GTB + power supply + cabling

TT walls+MaPMTs 1200 FEB 80 Split power boards Already on site. Concentrator and L2 GTB: See dedicated presentations

ROB production started Concentrators tender placed (see next slides)

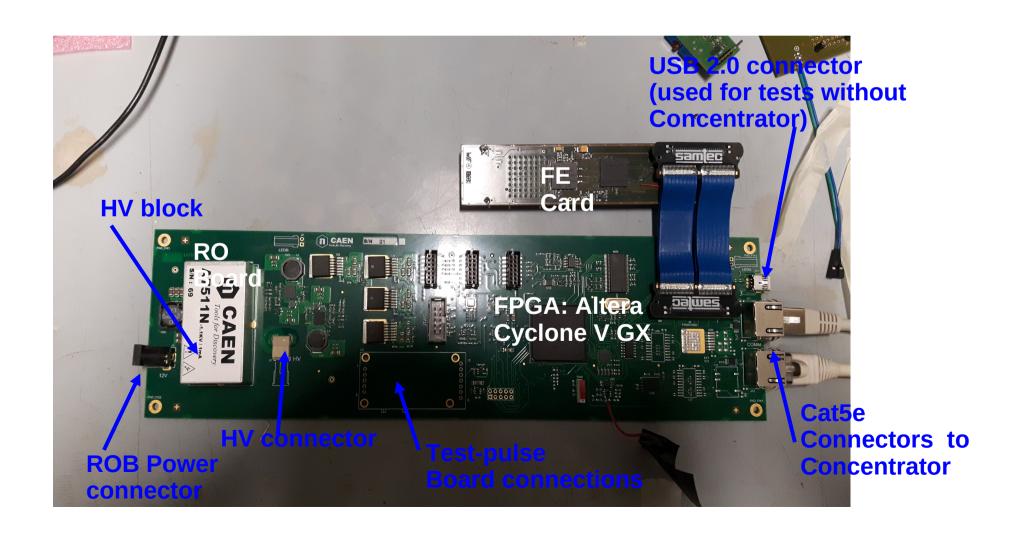
## Test of TT Wall electronics chain

Tests with prototype concentrators at Strasbourg, during November 2021, both on detector prototype and on test-bench.



### Read-out board

Pre-production board tested. Final production started. Finalization of firmware ongoing. SAMTEC cables between Read-out and Front-end boards procured.



# Read-out board production

#### **ROB** production status:

Components procurement for production accomplished by CAEN. Test protocol (CAEN and LNF) defined for mass production. Estimated time for production: about 6 months (Tests included).

Pre-serie of 20 units Assembly Pre-serie of 20 units Qualification Test 1st Bunch Assembly and Test (500 units) 2st Bunch Assembly and Test (500 units) Start End
23/12/21 25/02/22
28/02/22 25/03/22
28/03/22 24/06/22
14/06/22 23/09/22

ROB Production Plan - January 2022 Update					
2021 2022					
wk51 2021 - wk 8 2022	wk9-12	9	wk13-25		wk 26-38
		Ass.	Test	Ass.	Test



## Two ethernet cables from each ROB



### Concentrator

Concentrator mother-board with SOM mezzanine. Fan-in data from 16 ROBs.
Xlinx Zynq Ultrascale+ FPGA on SOM.
Spartan 6 FPGA for White-Rabbit interface.

Protoypes tested.
FDR on January GM.
SOM PCBs, Xlinx Zynq FPGA and RAM procured.
Opto-electronics converters also procured.
143 kEuro allocated for production.
Tender started, firm choosen, but troubles with components procurement (extra-cost/delays).

**SOM** mezzanine