

Short summary of the visit to Italy

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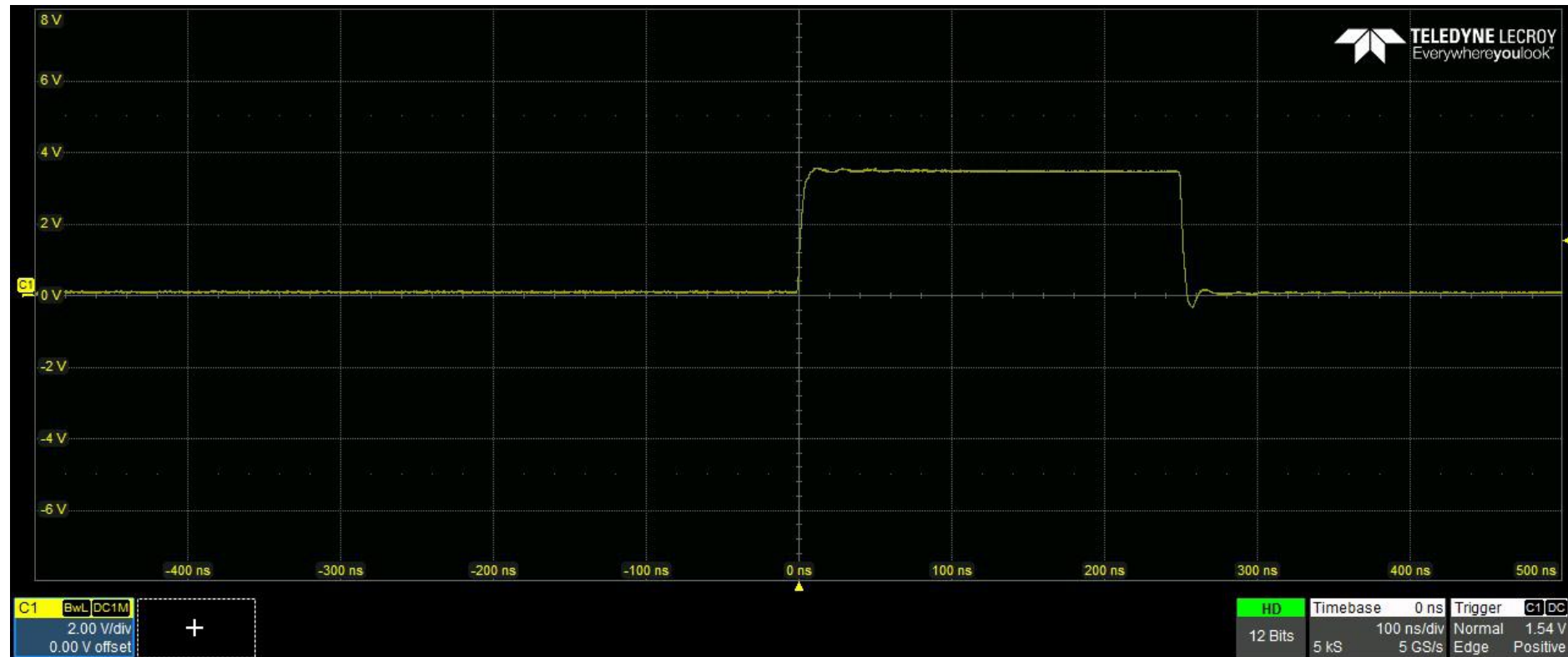
Visit summary

- Duration: 4-13/10
- 6 days working at LNF (LIME 2 lab)
- 1 day visiting the LNGS and talking to GSSI people
- Mounting of four termination resistors in the Trigger module inputs to make them 50 ohms impedance.
- Connection of 4 more inputs for future implementations.
- **Installation of FPGA IDE** (Intel Quartus Prime) program to allow local compilation and programming of the FPGA circuit in the Trigger Module. Soon it will be possible completely update the FPGA from Brazil. Thanks a lot to Stefano for troubleshooting in order to makes things work in the local computer.
- Test of a modified firmware to allow the reading of data from the module -> not working now
- Test of second modified firmware to implement a periodic Trigger output option -> WORKING

Periodic Trigger features

- 250 ns wide
- 4 options of frequency:
 - 5 Hz
 - 10 Hz
 - 100 Hz
 - 1000 Hz

selectable through a new configuration register



Trigger pulse output using the periodic-trigger mode (at LNF).

**Thanks to all that helped during
this visit and for all the talks.**

They were really nice days in Italy.