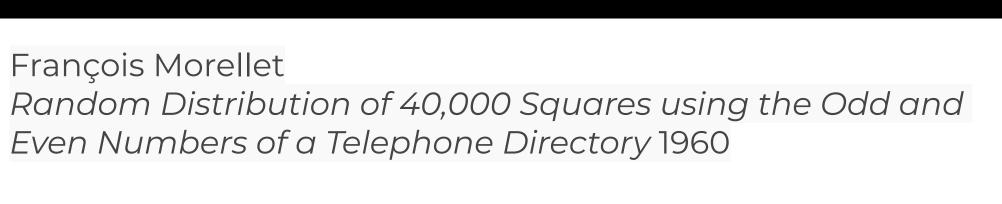






the value of unpredictability



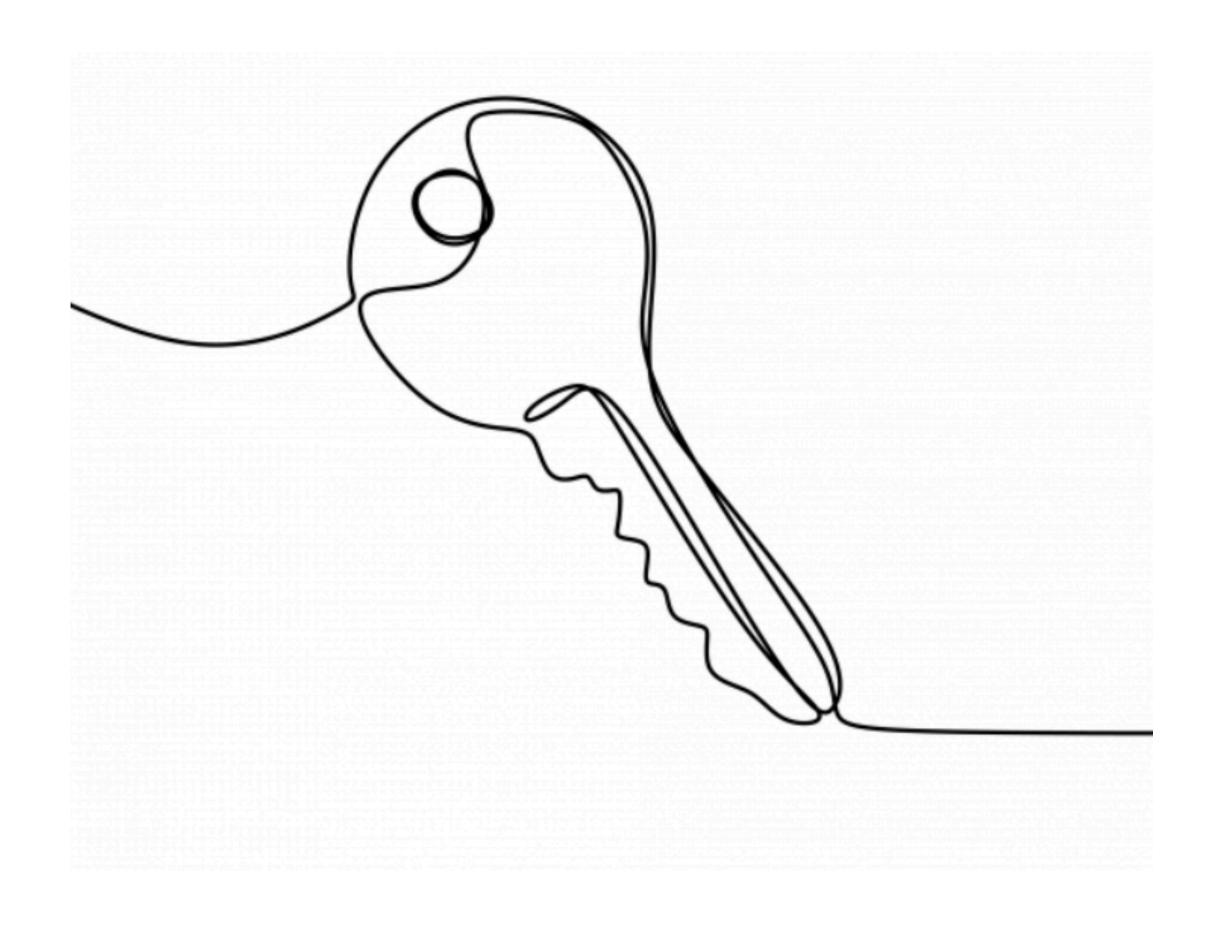






1. what for?

WHAT DOORS TO HOMES & DIGITAL VAULTS HAVE IN COMMON?



In the Cryptographic world, keys are assembled using

UNPREDICTABLE BIG PRIME NUMBERS*

extracted from random bit streams

* in the RSA (Rivest–Shamir–Adleman) protocol, up to 2048 bit long

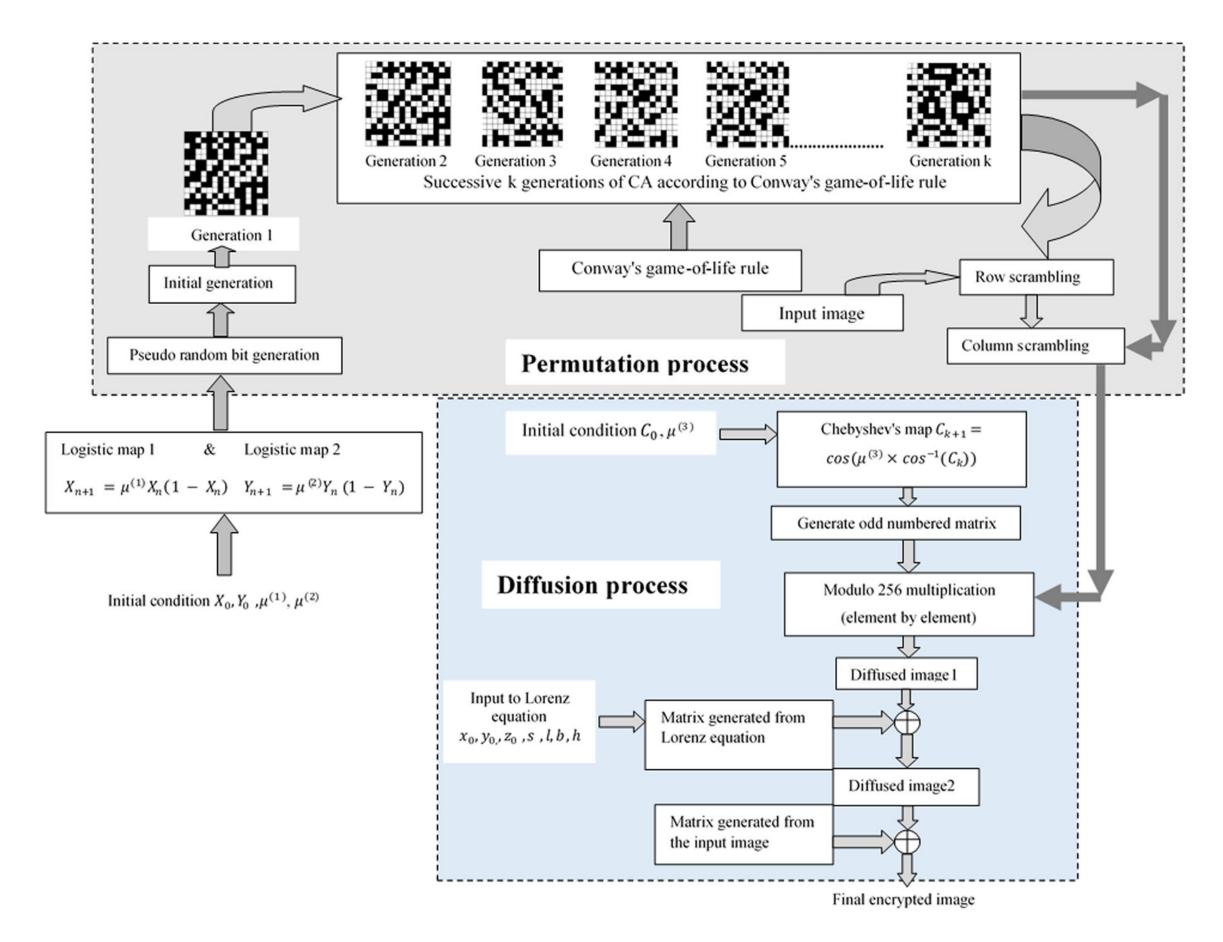


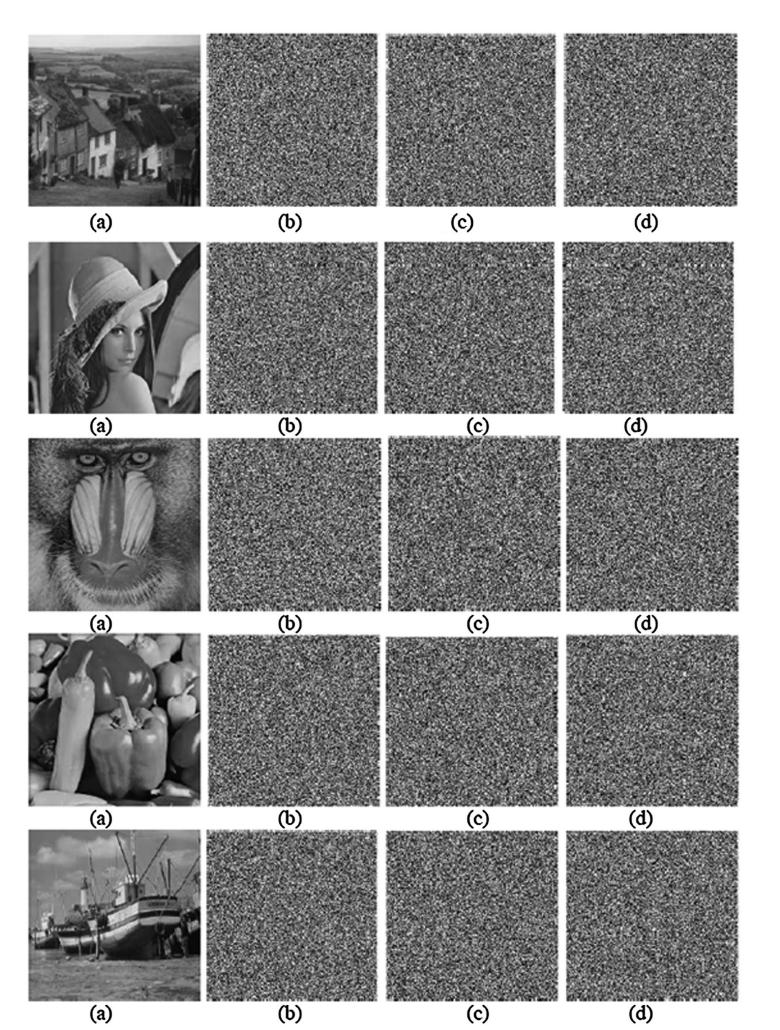


1. what for?

WHAT DOORS TO HOMES & DIGITAL VAULTS HAVE IN COMMON?

Image encryption is also relying on random single-bit arrays:





chaos

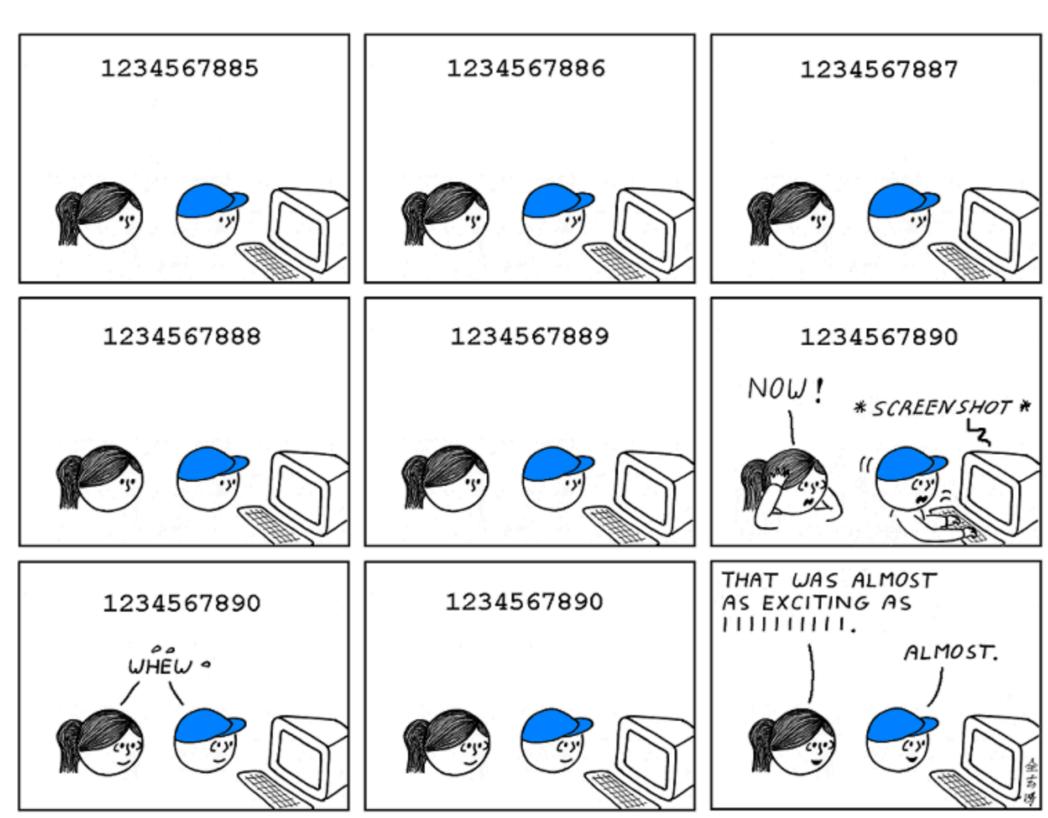
RINDOM

there is definitely a hype about Random bit streams, not only for crypto but also for gaming, virtual reality, Monte Carlo simulations and IoT (notably car security, smart houses, drones to guarantee authentication and secure transmission & control)

Generating Cryptographic Keys: Will Your Random Number Generators (PRNGs) Do The Job?

by Chuck Easttom (guest) on 22. February 2017

Key Management



https://www.cryptomathic.com/news-events/blog/generating-cryptographic-keys-with-random-number-generators-prng



tps://xkcd.com; Randall Munro

2. market potential:



Primary Market:

cybersecurity & simulation

Quantum Random Number Generators: A Ten-year Market Assessment

Report IQT-QRNG-0121 Published January 19, 2021

Main findings: expected market volume of \$7.2B by 2026

- most relevant segment: Data Centers [\$3.1B]
- ▶ significant interest by financial service providers for improved Monte Carlo simulations & secure service access [\$2.2B]



Secondary Market: gaming & gambling

Georges de la Tour The dice players (1651 c.a.)

Main applications:

Replacement market of

"physical gambling devices"

[~ 7 Million cabinets worldwide, 5 years lifespan ⇒ ~1.4M new devices/year]

Random number streaming to on-line platforms



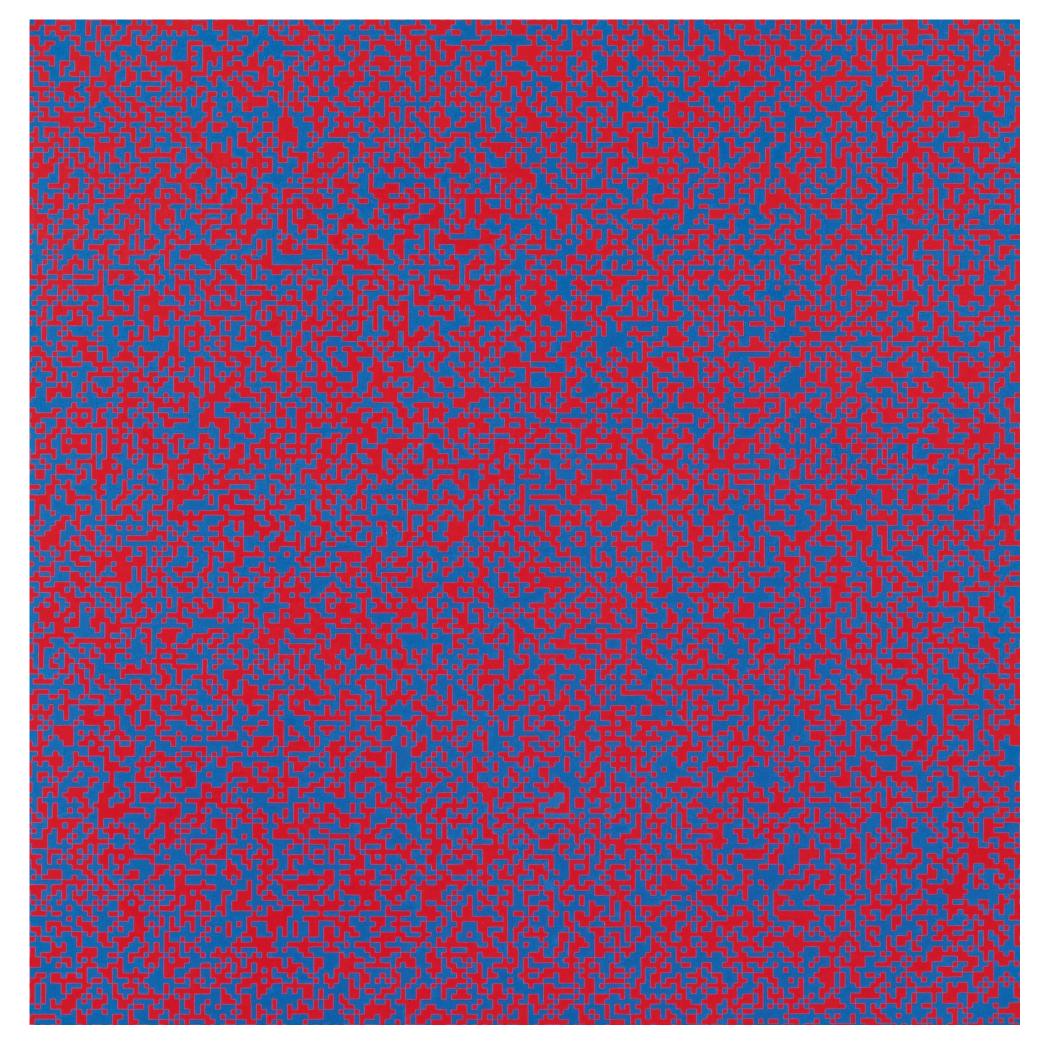
3. the essence of random number generation:

HOW TO GENERATE AN UNPREDICTABLE RANDOM NUMBER?

It is always nice to consider an artist's point of view:

"With Random Distribution, the purpose of my system was to cause a reaction between two colours of equal intensity. I drew horizontal and vertical lines to make 40,000 squares. Then my wife or my sons would read out the numbers from the phone book (except the first repetitive digits), and I would mark each square for an even number while leaving the odd ones blank. The crossed squares were painted blue and the blank ones red. For the 1963 Paris Biennale I made a 3-D version of it that was shown among the Groupe de Recherche d'Art Visuel installations (and re-created it again on different occasions). I wanted to create a dazzling fight between two colours that shared the same luminosity. This balance of colour intensity was hard to adjust because daylight enhances the blue and artificial light boosts the red. I wanted the visitors to have a disturbing experience when they walked into this room – to almost hurt their eyes with the pulsating, flickering balance of two colours. I like that kind of aggression."

excerpt from https://www.tate.org.uk/context-comment/articles/65-38-21-4-72



François Morellet (1926-2016)

Random Distribution of 40,000 Squares using the Odd and Even

Numbers of a Telephone Directory 1960

MOMA, New York

3. the essence of random number generation:



HOW TO GENERATE AN UNPREDICTABLE RANDOM NUMBER?

PRNG

$$x_n \equiv ax_{n-1} + b \pmod{m}$$

an example of linear congruential generator

J. Von Neumann: Anyone who considers arithmetical methods of producing random digits is, of course, in a state of sin.

Von Neumann, John (1951). "Various techniques used in connection with random digits" (PDF). National Bureau of Standards Applied Mathematics Series. 12: 36–38.

TRNG

(True Random Number Generators)

are essentially coin flipping,
namely get bits out observing unpredictable
natural phenomena



http://glee.wikia.com/wiki/File:281735_1342370254-coin-flip.gif.gif RNDOM POWER 3. the essence of random number generation:



HOW TO GENERATE AN UNPREDICTABLE RANDOM NUMBER?

PRNG

(PseudoRandom Number Generators)

Fast, cheap & reasonably easy. However:

- software Random Number Generation is PSEUDO
- code can be bugged
- and it may have a BACKDOOR



Two Years of Broken Crypto

2006

Debian's Dress Rehearsal



changes to commercial software to weaken encryption, and lobbying for encryption standards it can crack.

TRNG

(True Random Number Generators)

Extracting bits from the observation of natural phenomena is not trivial and you may suffer from

- "coin bias" by the embodiment of a great principle
- weakness against environmental parameters
- a significant "attack surface", conditioning the device in use
- low bit rate



HOW DO WE DO IT?

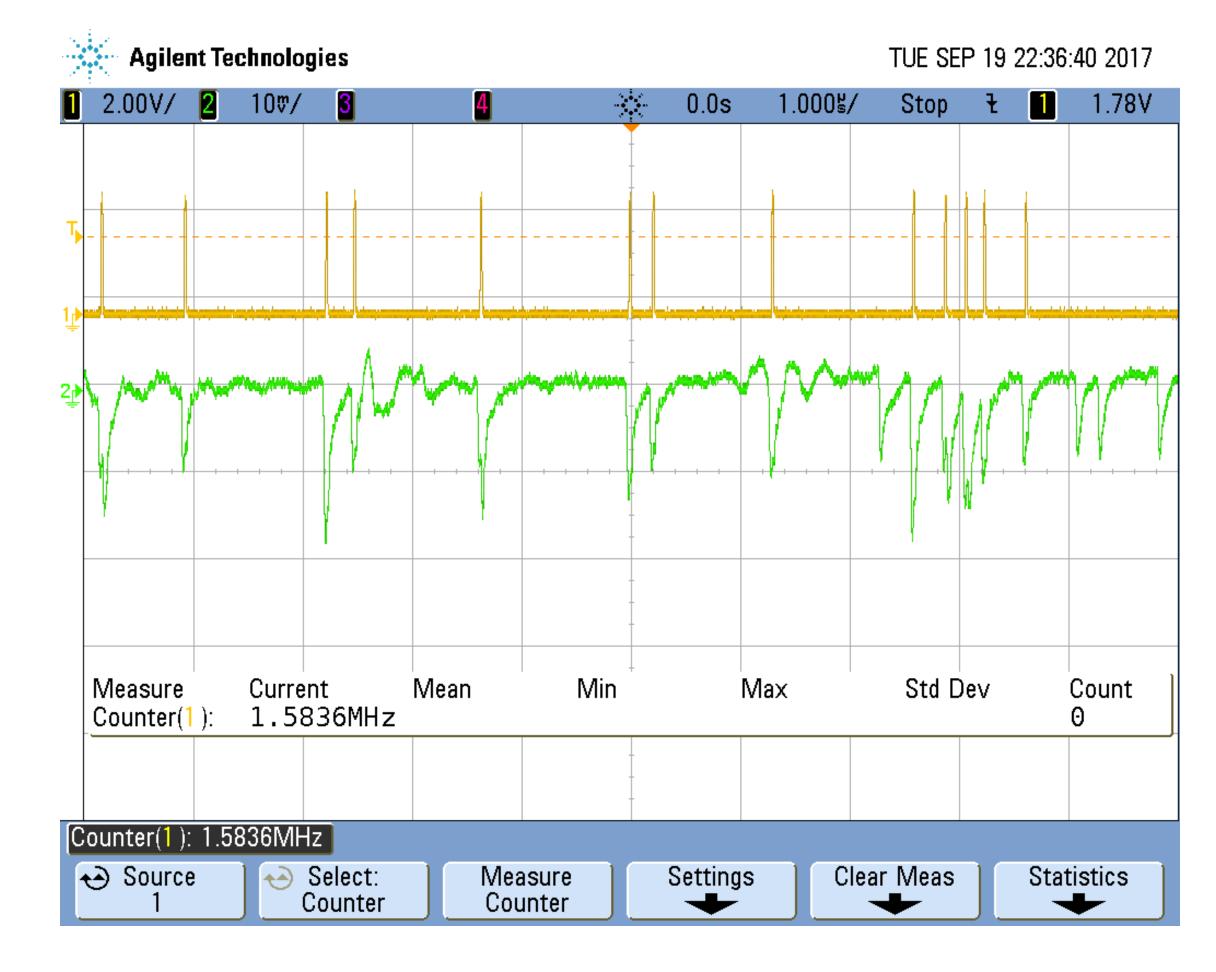
Inspired by Forrest Gump, we say:

RADIOACTIVE IS WHAT RADIOACTIVE DOES

- emission by a radioactive source is due to the quantum laws of Nature
- decays of unstable nuclei are unpredictable



- check the parity of the number of pulses in a time window
- pre-define the time window in a way that is equally like to have or not to have a single pulse



Sequence of pulses by the decay of a radioactive source in a nuclear physics detector

The idea behind

RINDOM POWER

What we do is essentially profiting from an effect well known since the early days of the Silicon technology development:

PHYSICAL REVIEW

VOLUME 94, NUMBER 4

MAY 15, 1954

Avalanche Breakdown in Silicon



K. G. McKay
Bell Telephone Laboratories, Murray Hill, New Jersey
(Received December 23, 1953)

IOURNAL OF APPLIED PHYSICS

VOLUME 35, NUMBER 5

MAY 1964



Model for the Electrical Behavior of a Microplasma*

ROLAND H. HAITZT

Shockley Laboratory, Clevite Corporation Semiconductor Division, Palo Alto, California (Received 5 November 1963)

The complex current fluctuations observed in connection with microplasma breakdown can be explained by a simple model containing two constants: extrapolated breakdown voltage V_b and series resistance R_s ; and two continuous probability functions: turnoff probability per unit time $p_{10}(I)$ as a function of pulse current I and turn-on probability per unit time p_{01} . Experimental methods allowing an accurate measurement of these four quantities are described. The new concept of an extrapolated breakdown voltage V_b is discussed based on two independent measurements: one of secondary multiplication and the other of instantaneous current, both as a function of voltage. Within the experimental accuracy of 20 mV both methods extrapolated to one and the same breakdown voltage. The turnoff probability $p_{10}(I)$ is determined by a new combination of experimental techniques to cover the current range from 5 to 70 μ A with a variation of 11 decades for $p_{10}(I)$. The observation of a narrow turnoff interval is explained quantitatively.

JOURNAL OF APPLIED PHYSICS

VOLUME 36, NUMBER 10

OCTOBER 1965

Mechanisms Contributing to the Noise Pulse Rate of Avalanche Diodes*



ROLAND H. HAITZ†

Shockley Research Laboratory, Semiconductor Division of Clevite Corporation,‡ Palo Alto, California (Received 16 November 1964)

1. INTRODUCTION

MOST reverse biased p-n junctions in silicon have their avalanche breakdown caused by microplasma effects. Microplasmas are small regions within the junction, where a local disturbance of the electrical field is believed to reduce the breakdown voltage to a value below the breakdown voltage of the surrounding uniform junction. As voltage is increased from low values microplasma breakdown is generally characterized by random "on-off" current fluctuations so long as currents remain below a critical value (40 to 120 μ A). 6-8

from paper



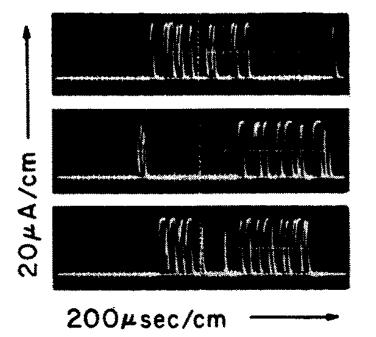


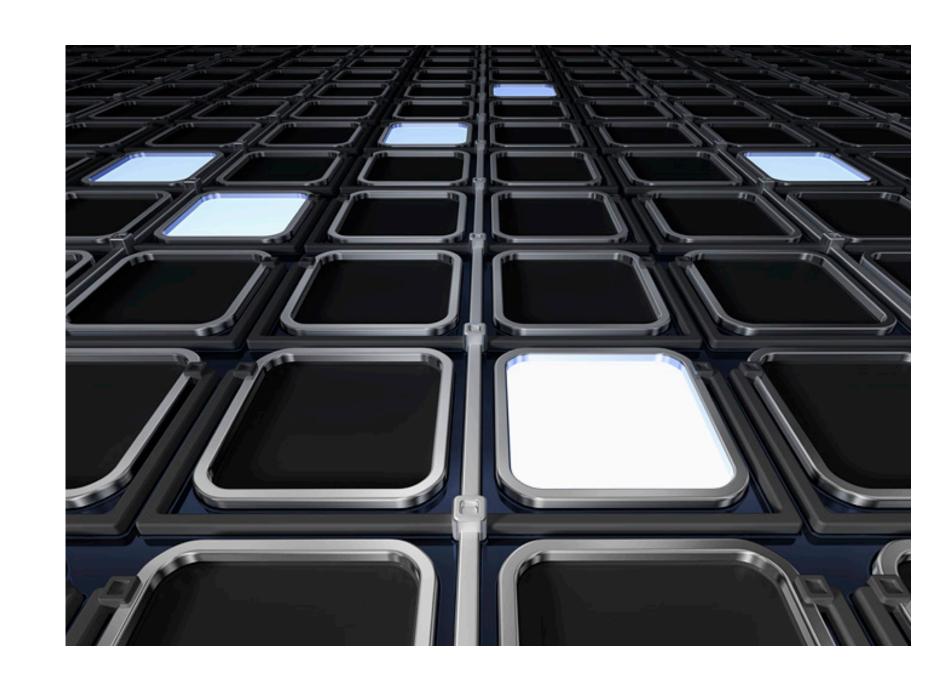
Fig. 5. Avalanche current as a function of time at low temperatures. The group character of the avalanche pulses is obvious.

from paper

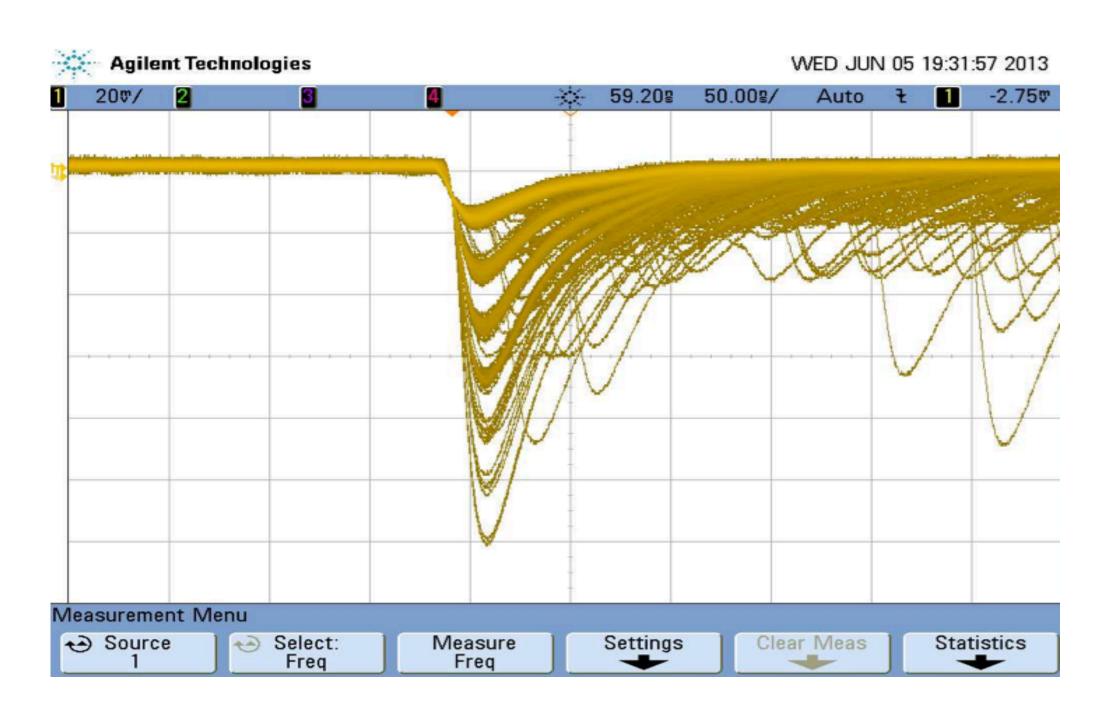
3



▶ Embodied in well known devices in their maturity stage, namely Silicon Photomultipliers, arrays of Single 11 Photon Avalanche Diodes, (SPAD), p-n junctions operated beyond the breakdown voltage



SiPM may be seen as a collection of binary cells, fired when a photon in absorbed



"counting" cells provides an information about the intensity of the incoming light:

Silicon photomultipliers represent today the state-of-the-art detectors with single photon sensitivity and

photon number resolving capability



12

▶ However, we know that charge carriers "seeding" the pulses can be generated "spontaneously", also when no light is illuminating the sensor and this is the name of the game

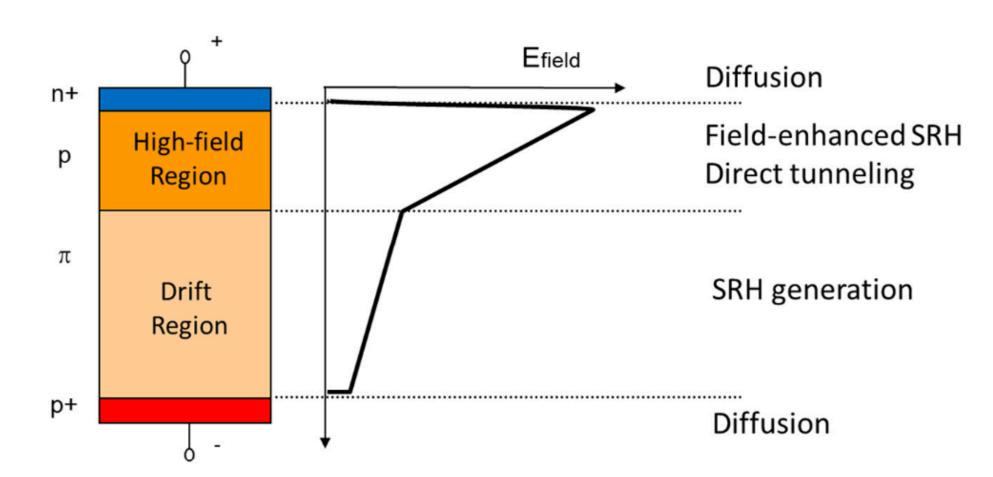


Fig. 8. Representation of the different sources of primary dark events and their location in the SPAD structure.

after A. Gola, C. Piemonte, NIM A926 (2019) 2-15

Key issues:

- * in SiPM, the Dark Count Rate is O(1 KHz)/cell, 50 µm pitch (it may be higher for SPAD arrays in CMOS technology)
- * provided the nature of the Dark Pulses, we have a significant dependence on Temperature

Thermal generation of carriers by states in the bang-gap

(Shockley-Read-Hall statistics), where trapping and detrapping is increased by the high electric field in the junction:

$$G = \frac{n_i}{2 \cdot \cosh\left(\frac{E_0 - E_t}{kT}\right)} N_t \sigma v_{th} = \frac{n_i}{\tau_{g0}}$$

 E_0 = Fermi level

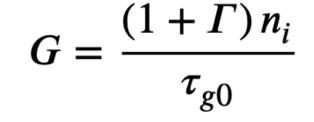
E_t = trapping level

 n_i = intrinsic carrier concentration

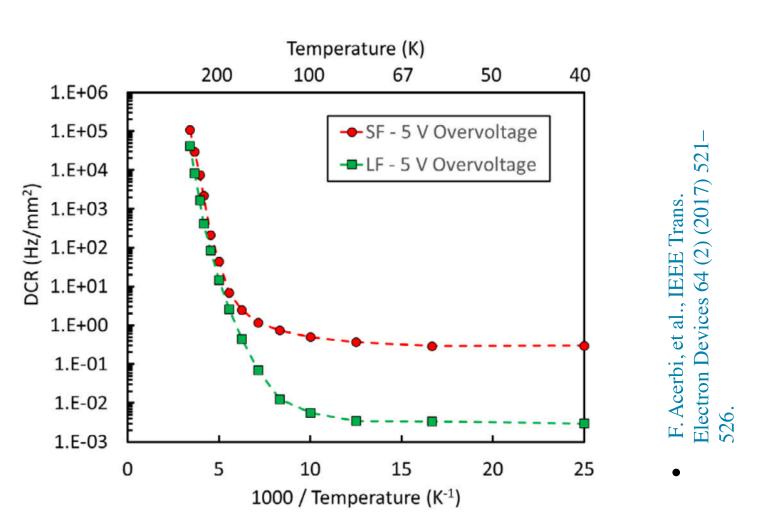
 N_t = trapping concentration

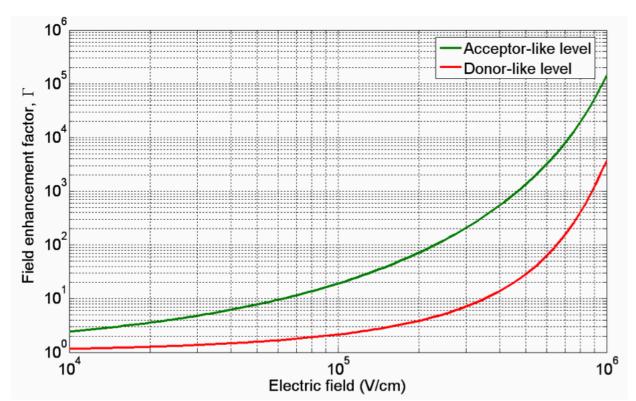
 σ = trapping cross section

 v_{th} = thermal velocity



Γ "boost" by the field





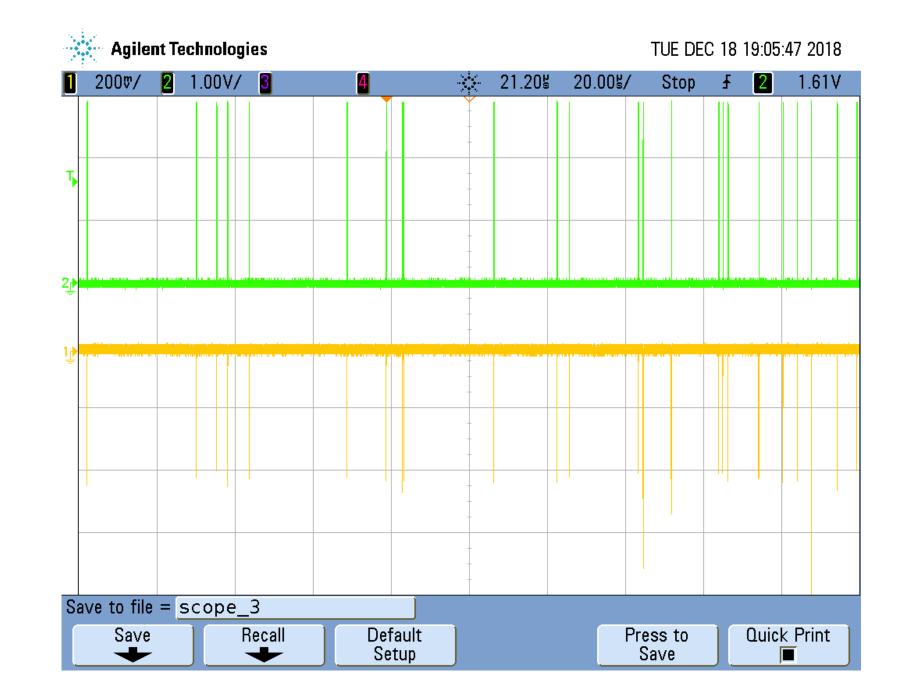


▶ The essence of:



turning unpredictable "Dark Pulses" into bits:

1. tagging & time stamping the occurrences of the random pulses



2. analysing the time series of the pulses:





granted in

WHERE ARE WE NOW

The MINIMUM VIABLE PRODUCT [MVP], the progenitor of a class of Quantum Random Bit Generators:



Developed thanks to the **seed capital [100 000 €]** granted by



which selected Random Power as one of 170 "breakthrough projects" out of 1211 submissions - Phase 1 (2019-2020)







3.5 cm

Upon request, bits can be routed to pins

FTDI chip for data routing on the USB (FT232HQ - Single Channel Hi-Speed USB to Multipurpose UART/FIFO IC

FPGA embedding a proprietary TDC and implementing the bit extraction + real-time sanity checks + SHA256 (Spartan-7 Family 23360 Cells 800MHz 28nm Technology 1V 225-Pin CSBGA Tray)

Amplification & discrimination (LTC6268HS6-10#TRMPBF amplifier by Linear technologies + ADCMP605BCPZ LVDS comparator by Analog Device)

Single generator (either 1x1 mm² or 3x3 mm² - **Bit rate for the smaller** area device: O(100 kbps) - operated with overvoltage stabilisation against Temperature variations



5. state-of-the-art:

finalAnalysisReport_PART2.txt												
RESULTS FOR THE UNIFORMITY OF P-VALUES AND THE PROPORTION OF PASSING SEQUENCES												
generator is TestFW8_4BitNoReshape_1GB_Part2.bin>												
C1	C2	С3	C4	C5	C6	С7	С8	С9	C10	P-VALUE	PROPORTION	STATISTICAL TEST
100	110	 95	93	 90	90	114	101	 98	109	0.682823	986/1000	 Frequency
	102		103				106		87	0.941144	993/1000	
95				113			100	89		0.842937	989/1000	
94	112			93		89		123	95	0.125927	987/1000	
100	93	91	112	93	112	99	110	101	89	0.647530	992/1000	Runs
105	91	96	80	121	99	85	100	107	116	0.092597	989/1000	LongestRun
100	104	89	110	97	88	126	84	99	103	0.148653	992/1000	Rank
95	109	103	113	85	94	90	100	106	105	0.630872	995/1000	FFT
104	98	91	89	104	90	110	104	115	95	0.632955	987/1000	NonOverlappingTemplate
111	93	112	88	96	95	100	101	106	98	0.798139	981/1000	NonOverlappingTemplate
111	100	93		101	109	93		117	95	0.514124	986/1000	NonOverlappingTemplate
86	94	119	101		98	93	103		101	0.626709	998/1000	NonOverlappingTemplate
	112				89	94		115	111	0.498313	989/1000	NonOverlappingTemplate
	106				119		96	94	94	0.249284	988/1000	NonOverlappingTemplate
114	92	98		105				83		0.682823	992/1000	
117	87			100		91	94	105	101	0.697257	991/1000	NonOverlappingTemplate
90	93		107	99	89	100		108		0.689019	994/1000	NonOverlappingTemplate
99	108	98		116		98	85	96	97	0.743915	991/1000	NonOverlappingTemplate
88		103				111	99		99	0.829047	988/1000	NonOverlappingTemplate
96				106			97		83	0.651693	987/1000	NonOverlappingTemplate
08	95	97	109	84	94	101	707	91	120	0.388990	988/1000	NonOverlappingTemplate

series of tests on non-overlapping templates

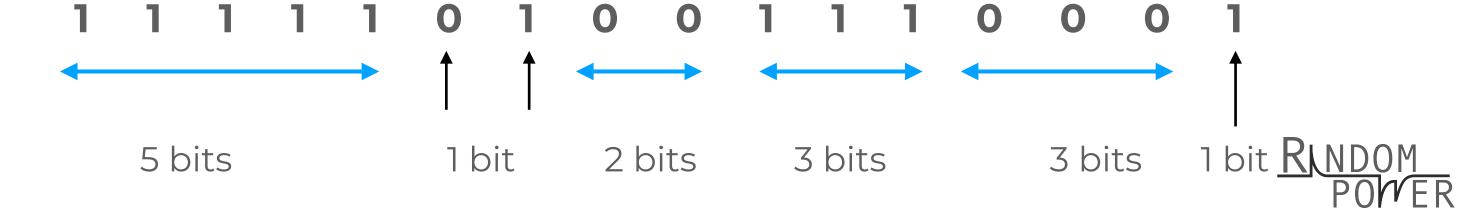
▶ A proto-randomness farm based on 10 boards have been collecting about 1.5 Tb, currently being qualified through the NIST and TESTU01 suites.

Preliminary results over 250 Gb show that the stream looks extremely "white", essentially with no failures on the raw data:

Two tests have been implemented in firmware to guarantee realtime sanity checks:

* MONOBIT: essentially testing the asymmetries between 0's and 1's in a bit string:

* RUNS: testing the statistics of the number of sequences of identical bits in a string



WHAT'S NEXT

≈10⁵€

[2021-2022]

≈n x 10⁵€

[2022-2024]

≈2-3 x 10⁶ €

[2022-2025]

■ GO TO THE MARKET and EXPLOIT THE MVP





- ▶ Get official certifications
- ▶ Enhance IP protection
- SEEK FOR FUNDING

▶ GO MACRO & SECURE:



development of "agnostics" applications



RNG replacement

→ GO MICRO & SECURE:



▶ High End applications [e.g. FULL HOMOMORPHIC ENCRYPTION] [prioritized after the end of the technology development]

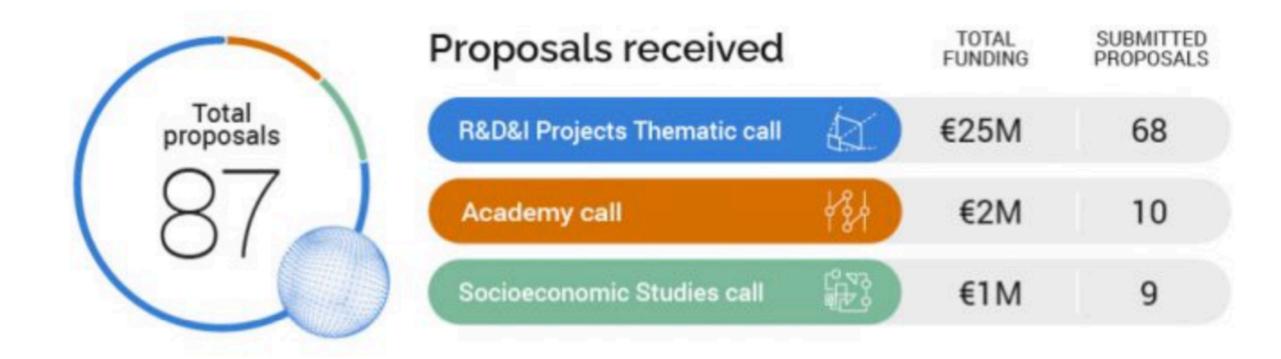


Phase II:

- submission Sept. 20th, 2021
- notification of approval Jan. 31st, 2022
- Duration: May 2022 to August 2024
- ▶ funding: 2 MEUR
- selection & competitiveness:



1211 submissions in Phase 1 → 170 approved → 87 submissions for phase II (68 R&D proposals) → 18 R&D approved



combined success rate: 18/1211 = 1.5%, so we did well!





Our consortium:



leading party













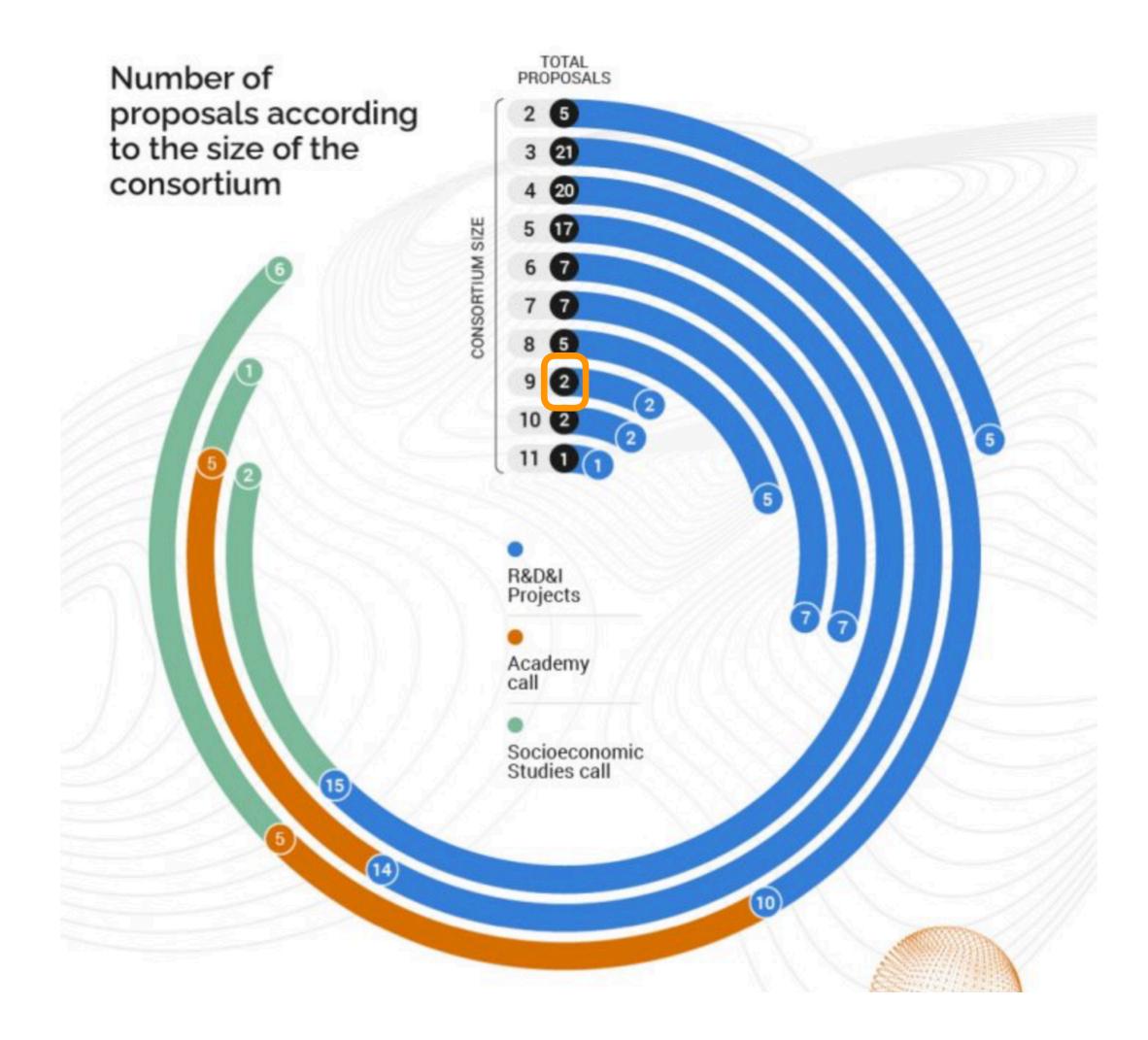


Contact

novaon



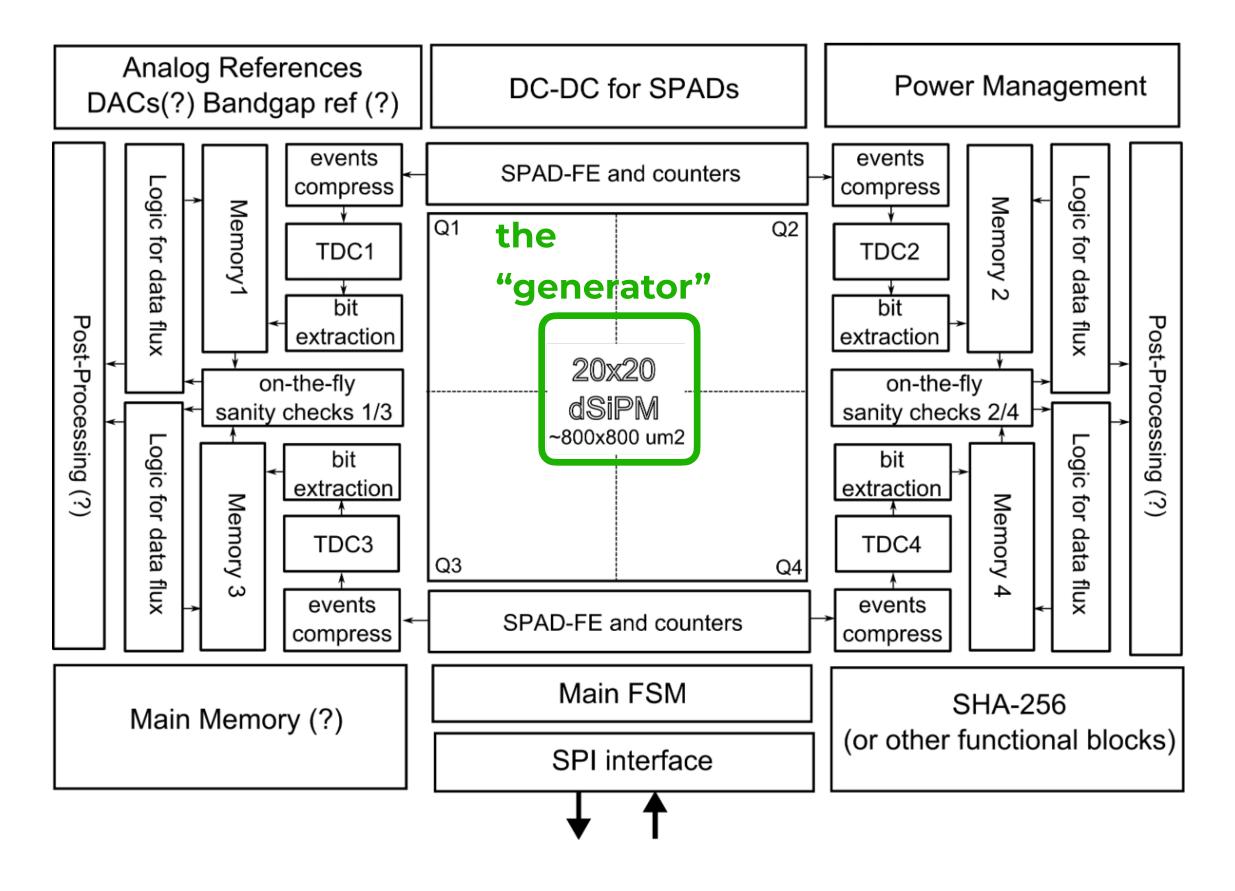




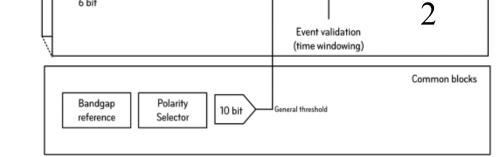


6. roadmap

Our main goals:



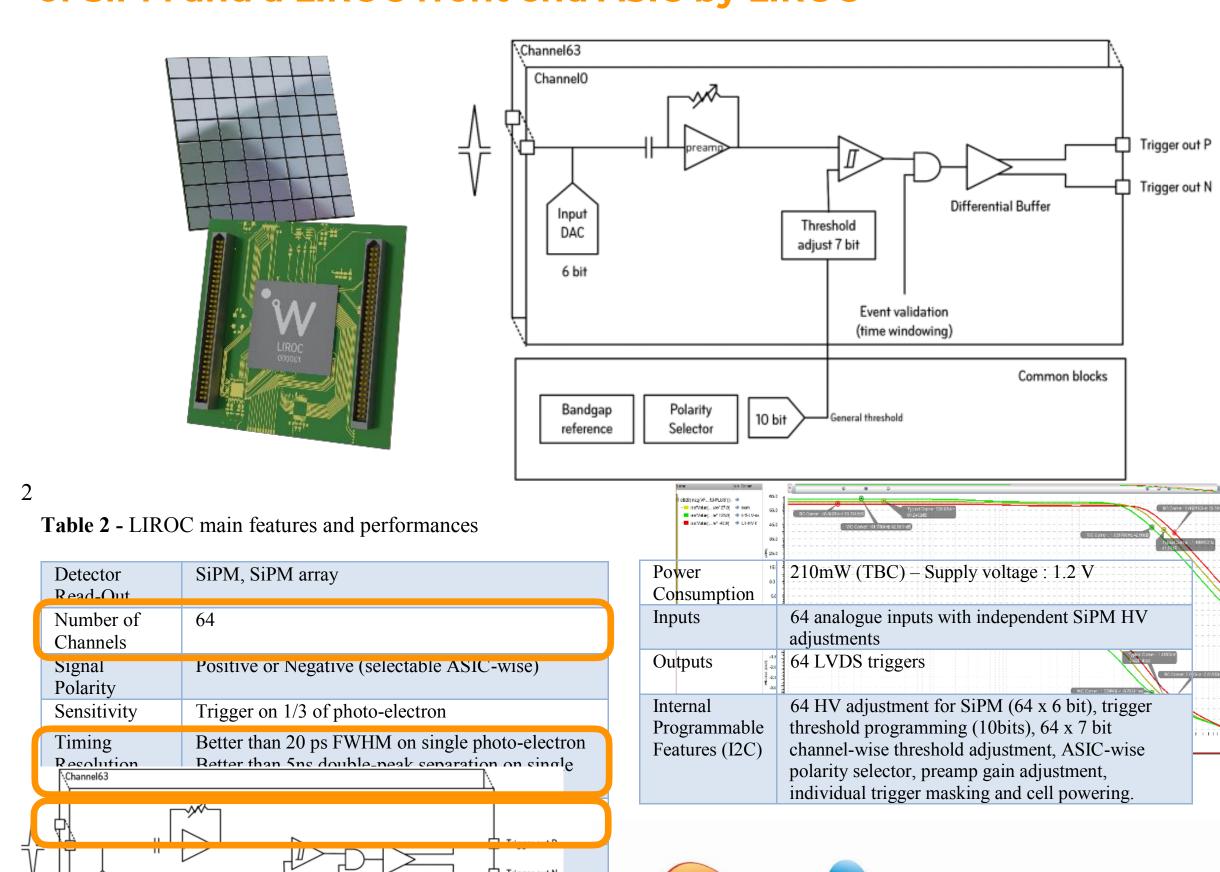
design a FIPS-compliant ASIC embedding a SPAD array in standard CMOS technology





ATTRACT

design a scalable multi-generator system based on an array of SiPM and a LIROC front end ASIC by LIROC



Differential Buffer

adjust 7 bit

Polarity Selector

10 bit

Event validation (time windowing)

RIDOM POYER

www.randompower.eu

Established in June 2021





This project has received funding from the ATTRACT project funded by the EC under Grant Agreement 777222

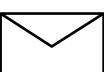


Join us, we will be happy to walk with you!





CONTACT US at:



- massimo.caccia@randompower.eu
- marcello.esposito@randompower.eu
- * lorenza.paolucci@randompower.eu