

Traveling-wave electron accelerators – leveraging exascale computing towards scalable laser-plasma accelerators

hZDR

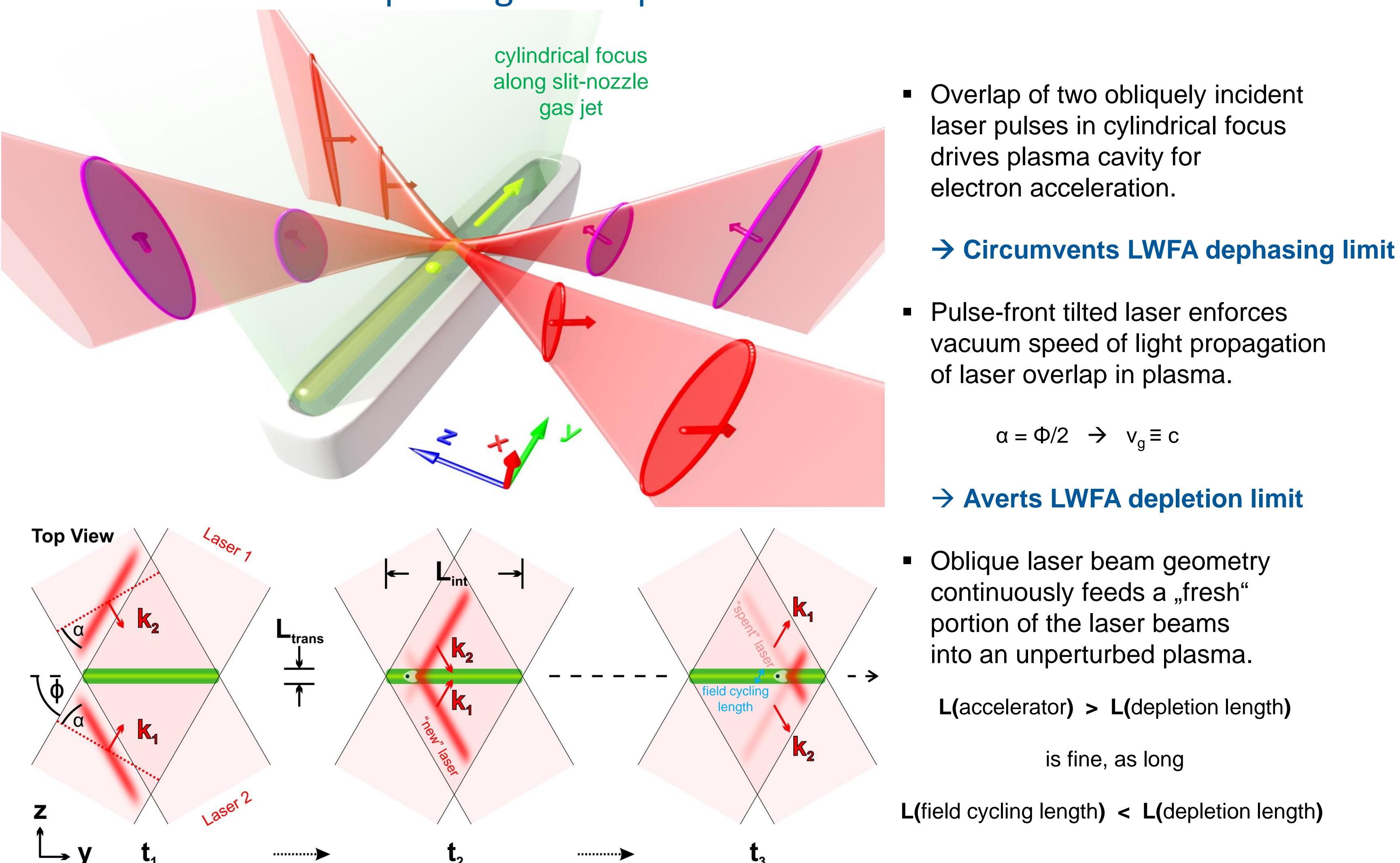
A. Debus¹, K. Steiniger¹, R. Widera¹, S. Bastrakov¹, F. Carstens^{1,2}, F. Meyer¹, R. Pausch¹, A. Lebedev^{1,2}, M. Garten⁵, T. Kluge¹, J. Kelling¹, B. Hernandez⁴, J. Young⁷, F. Poeschel^{1,3}, A. Huebl⁵, D. Rogers⁴, G. Juckeland¹, S. Chandrasekaran⁶, M. Bussmann^{1,3} and U. Schramm^{1,2}

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¹ Helmholtz-Zentrum Dresden-Rossendorf, ² Technische Universität Dresden, ³ Center for Advanced Systems Understanding (CASUS) ⁴ Oak Ridge National Laboratory (ORNL), ⁵ LBNL, ⁶ University of Delaware (UDEL), ⁷ Georgia Tech

Traveling-Wave Electron acceleration (TWEAC)

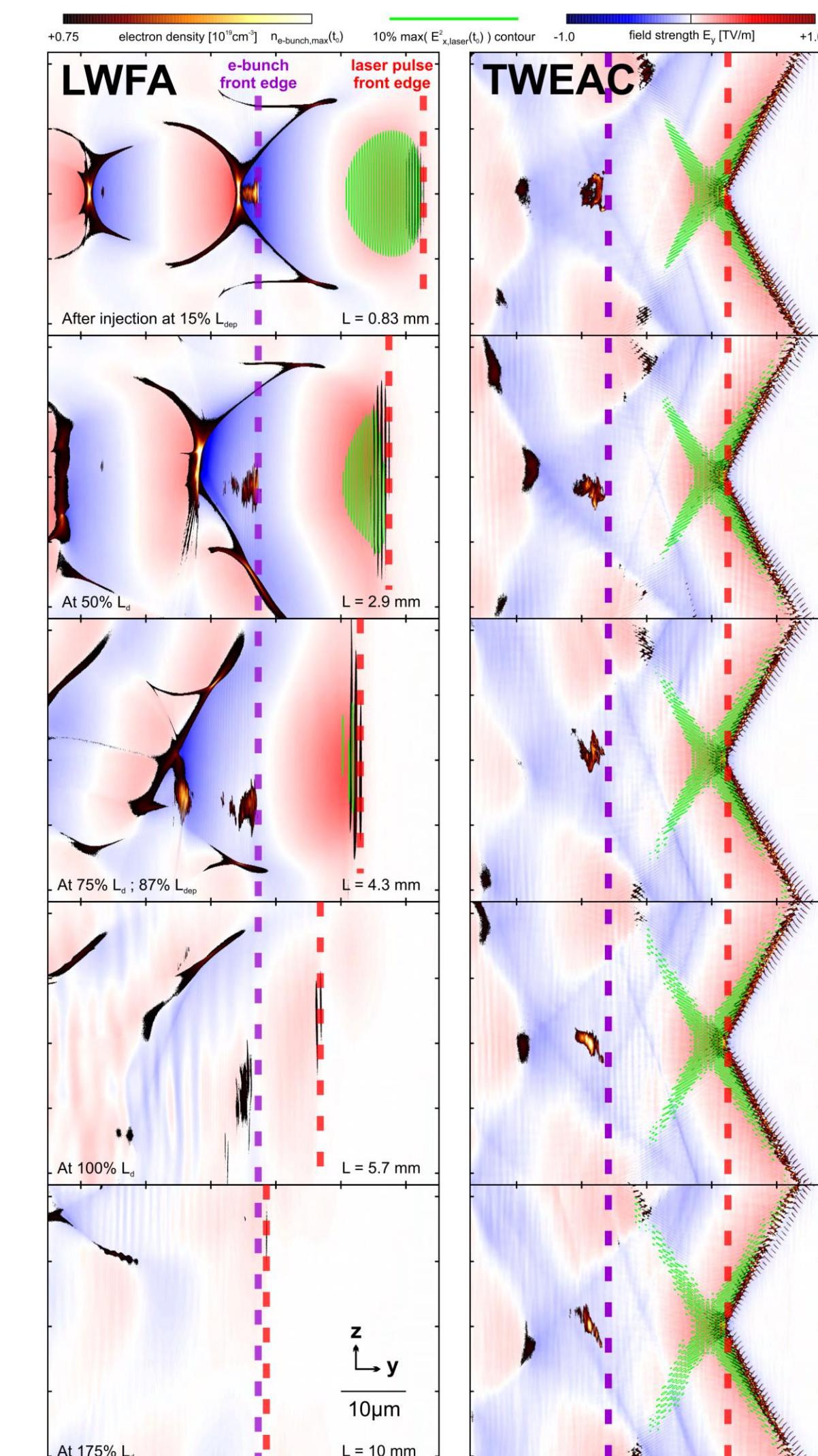
eliminates the dephasing and depletion limit of Laser-wakefield acceleration



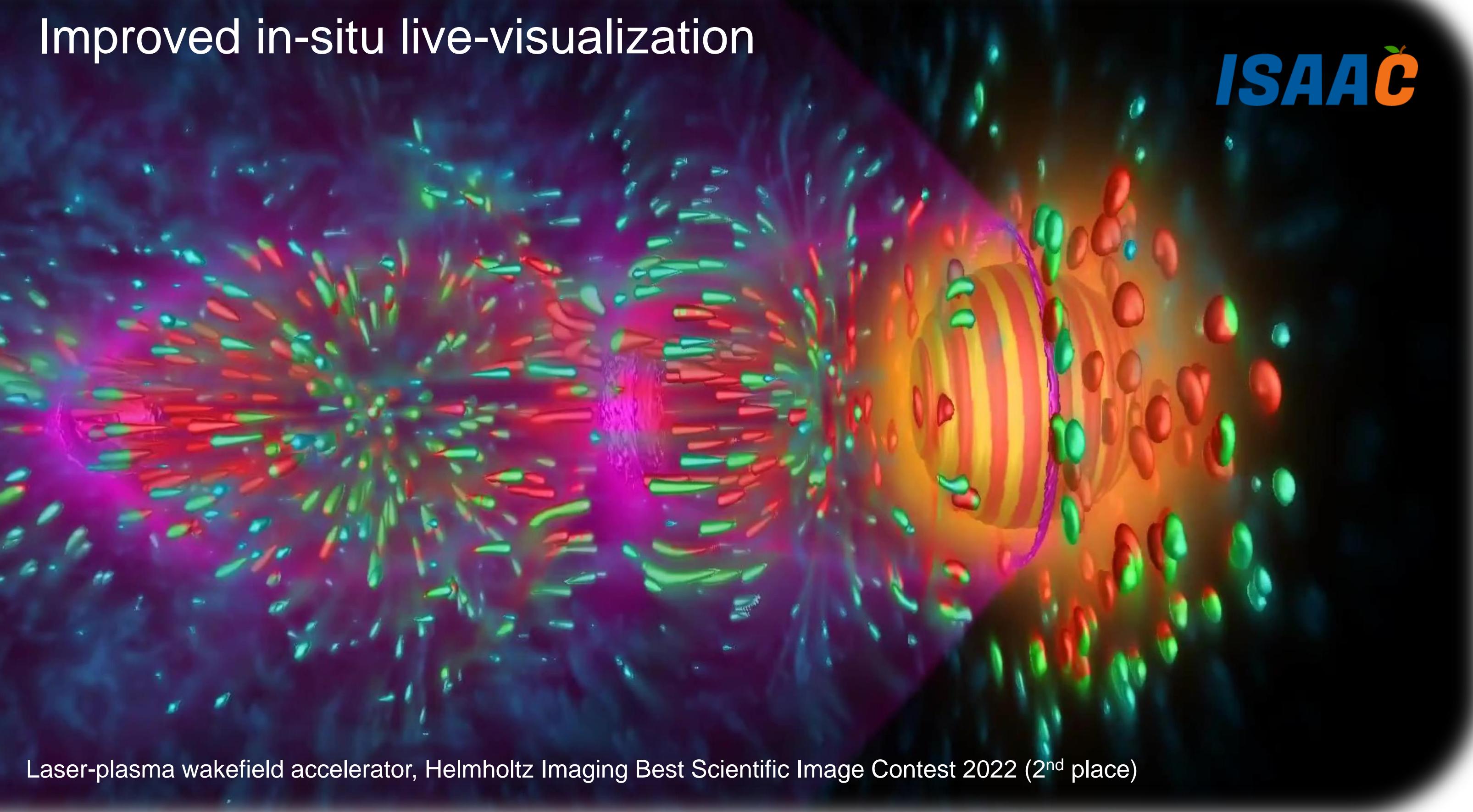
TWEAC maintains quasi-stationary plasma conditions

- TWEAC accelerator length can be made longer without dephasing or depletion.
- **No need for staging.**
- Quasi-stationary plasma conditions without (parasitic) self-injection.
- No laser self-phase modulation along direction of electron acceleration.
- Does not require guiding of laser pulses.

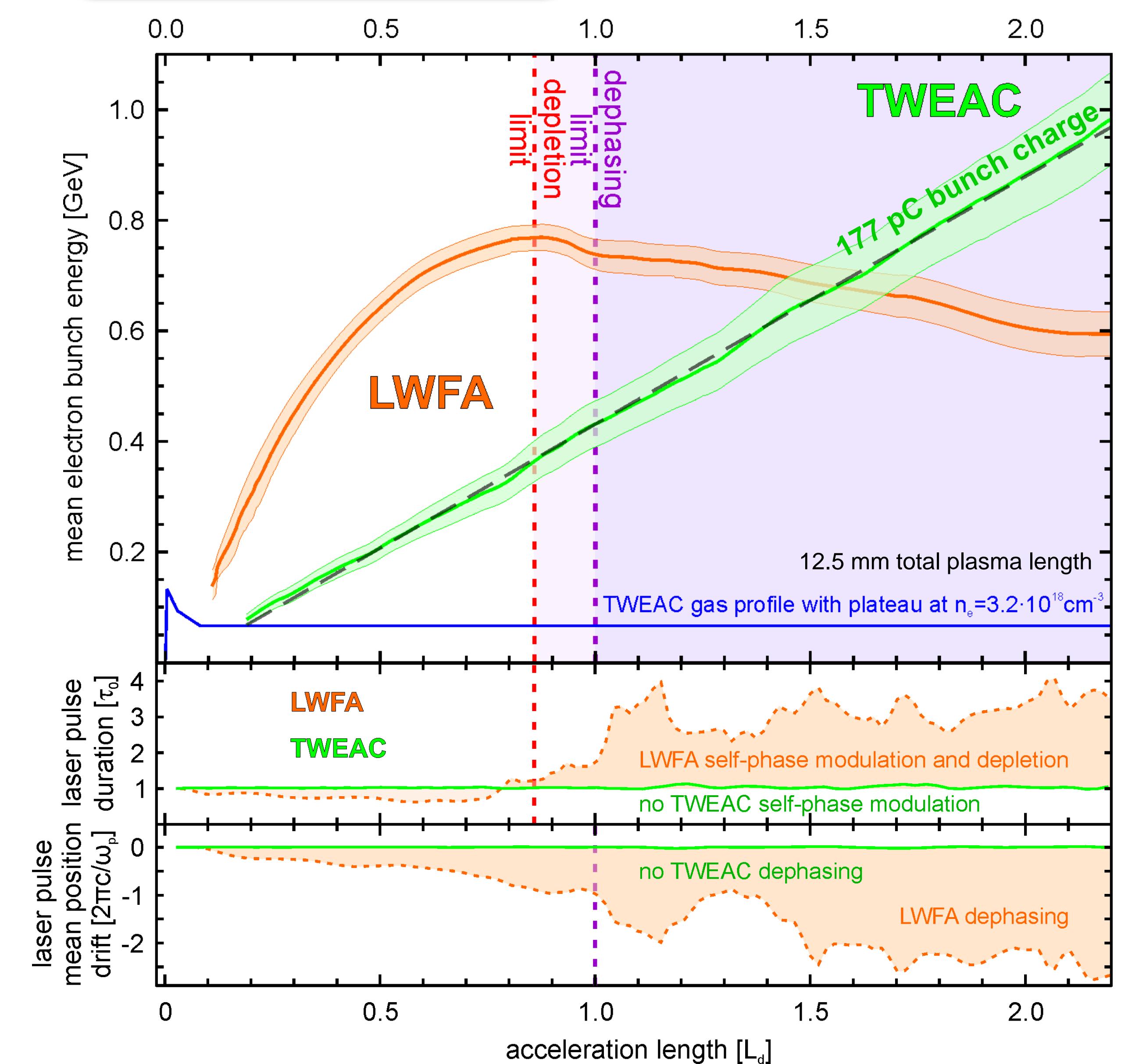
Simulated with
PIConGPU
picongpu.hzdr.de
Performance on GPUs and CPUs



Improved in-situ live-visualization

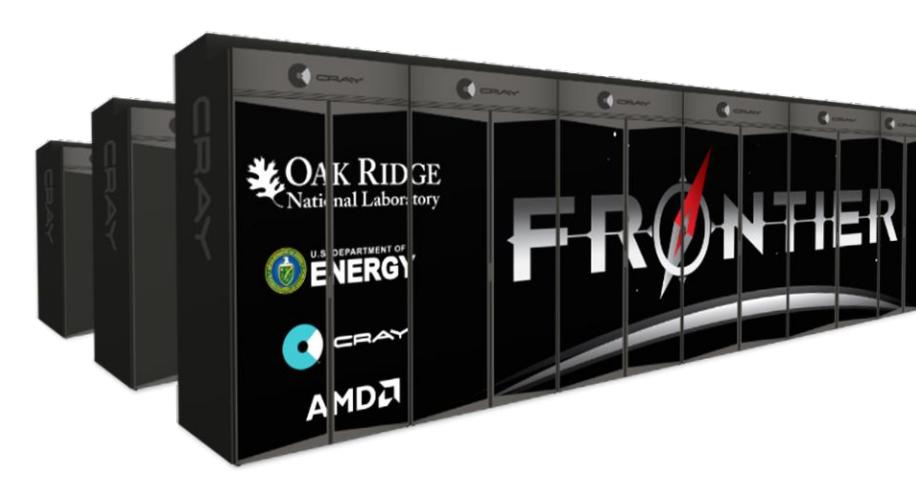


Laser-plasma wakefield accelerator, Helmholtz Imaging Best Scientific Image Contest 2022 (2nd place)



Road to exascale – PIConGPU scales to the largest machines

PIConGPU team is one of 8 teams in early-access project CAAR for Frontier (Center for Application Readiness) at ORNL



Juwels Booster (Nov 2020),

JSC at FZ Juelich

73 PetaFLOPS (7th Top 500, 3rd Green 500)

936 nodes, 3744 NVIDIA Volta A100s

AMD EPYC Rome CPU

Summit, ORNL (2018)

200 PetaFLOPS

4,608 nodes

27,648 NVIDIA Volta V100s

IBM POWER9 CPU

Frontier, ORNL (2022)

> 1.5 ExaFLOPS

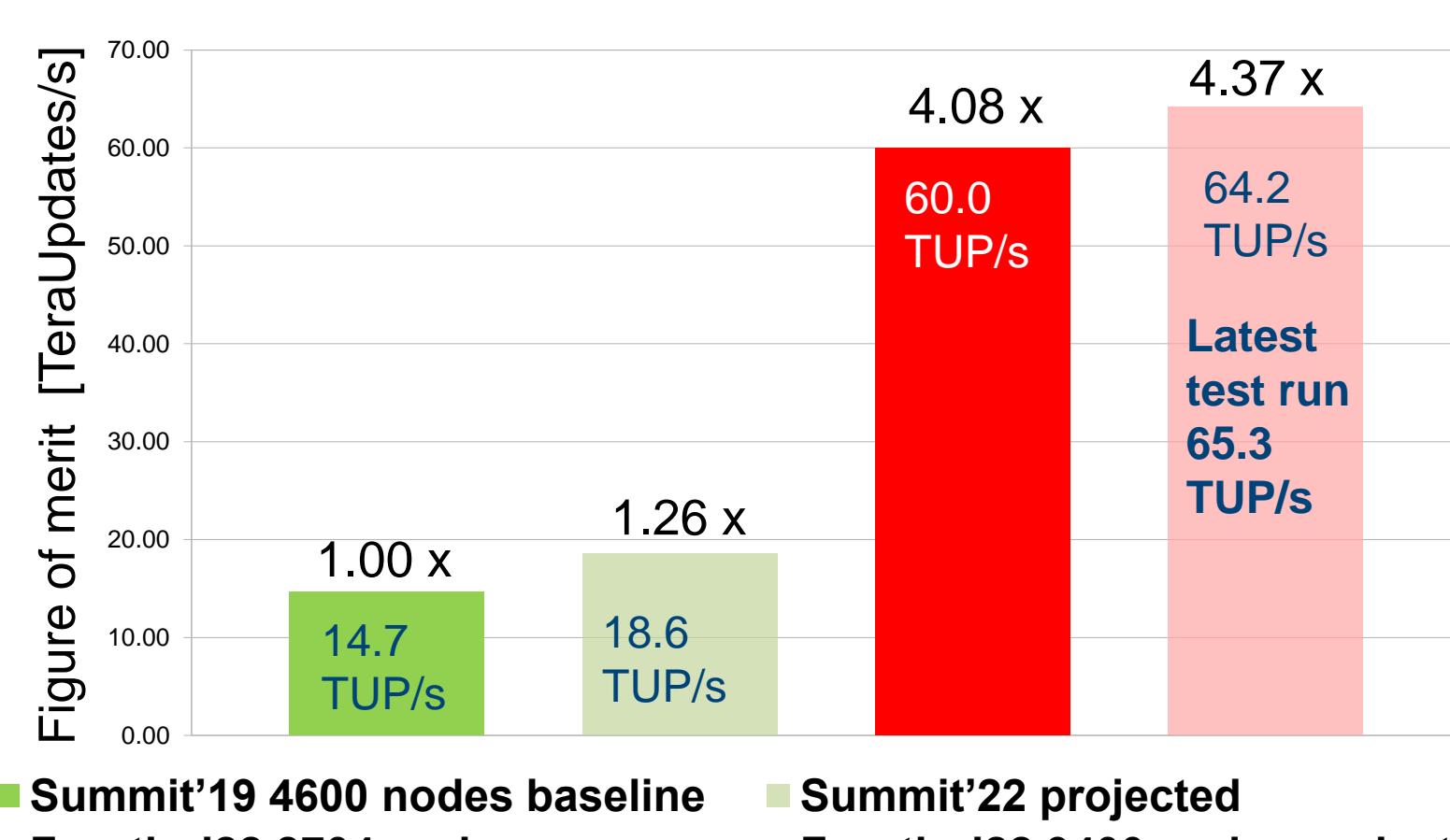
AMD GPU hardware

Cray architecture / compilers

PIConGPU runs on latest AMD GPUs

AMD CRAY a Hewlett Packard Enterprise company

OAK RIDGE National Laboratory



powered by
alpaka
github.com/alpaka-group/alpaka

OAK RIDGE National Laboratory

