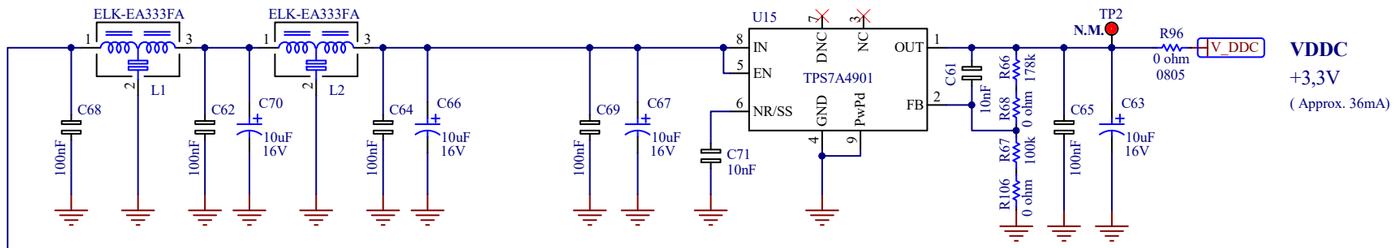
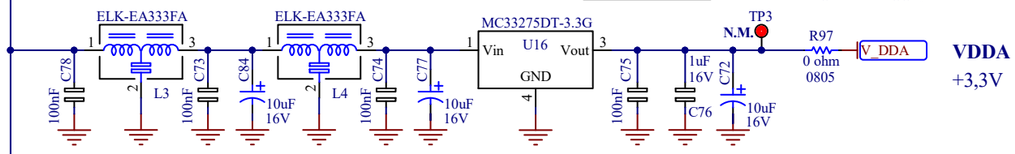
ASIC HiDRA2: CSA power supply



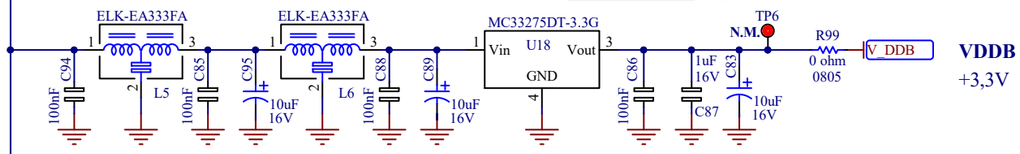
VDDC
+3,3V
(Approx. 36mA)

ASIC HiDRA2: CDS and ST power supply



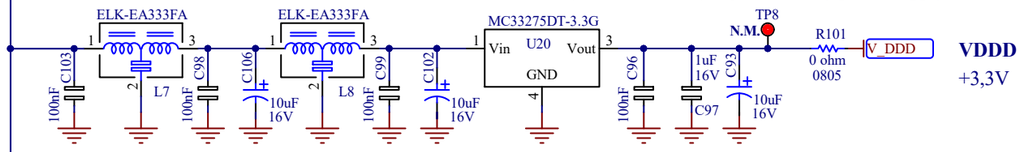
VDDA
+3,3V

ASIC HiDRA2: Digital circuitry power supply



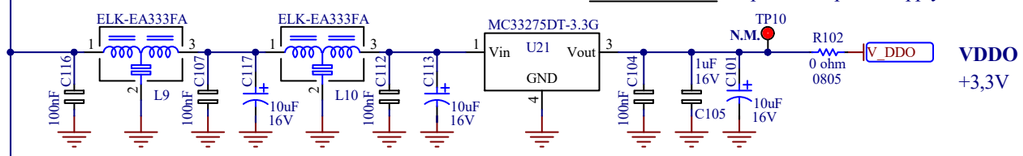
VDDB
+3,3V

ASIC HiDRA2: Digital circuitry power supply



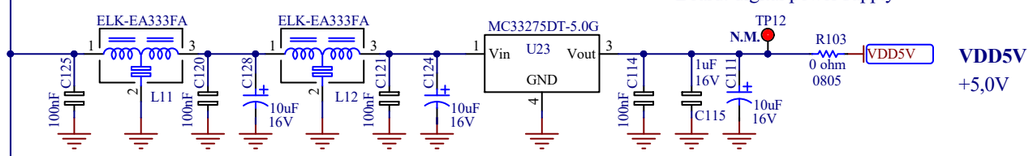
VDDD
+3,3V

ASIC HiDRA2: Output buffer power supply



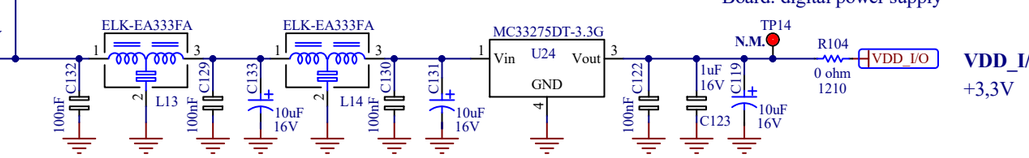
VDDO
+3,3V

Board: digital power supply



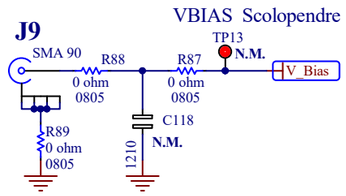
VDD5V
+5,0V

Board: digital power supply



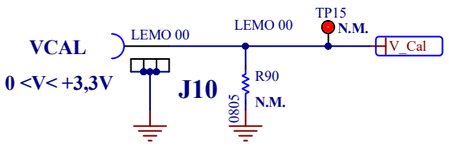
VDD_I/O
+3,3V

VBIAS Scolopendre

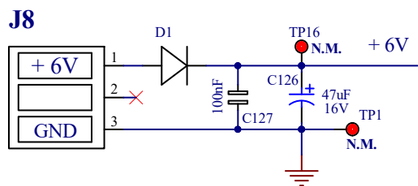


VBIAS
+50V max

ASIC HiDRA2: Calibration voltage input

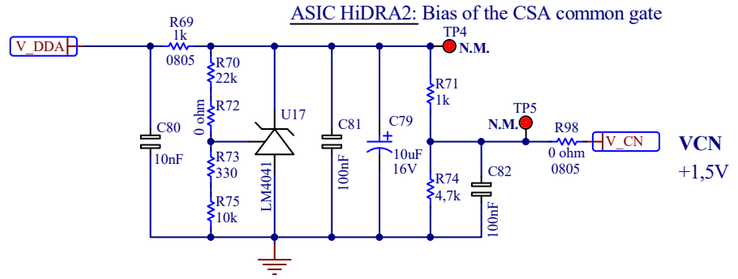


VCAL
0 <V< +3,3V



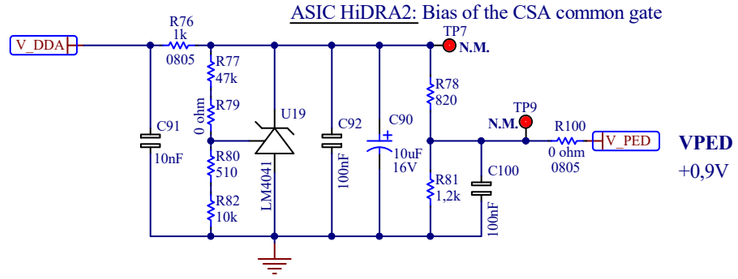
POWER
SUPPLY
+ 6,0 V

ASIC HiDRA2: Bias of the CSA common gate



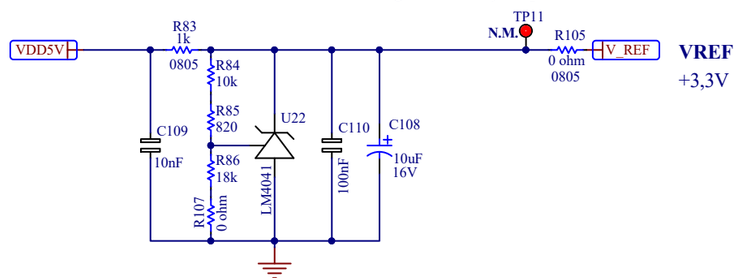
VCN
+1,5V

ASIC HiDRA2: Bias of the CSA common gate



VPED
+0,9V

Board digital circuitry: to pin 1 AD7686



VREF
+3,3V

Title: ***HiDRA 2 Power Supply_Vbias_Vref*** PRJ: HiDRA 2			INFN TRIESTE Lab. Elettronica
Size: A3	Number:	Revision:	
Date: 09-7-2018	Draw by: S. CIANO	Sheet 5 of 5	
File: U:\HIDRA\HIDRA2_MAIN\HIDRA2\HiDRA2_Power_Supply_Vbias_Vref_1.SchDoc			