

IDEA Dual-readout calorimeter

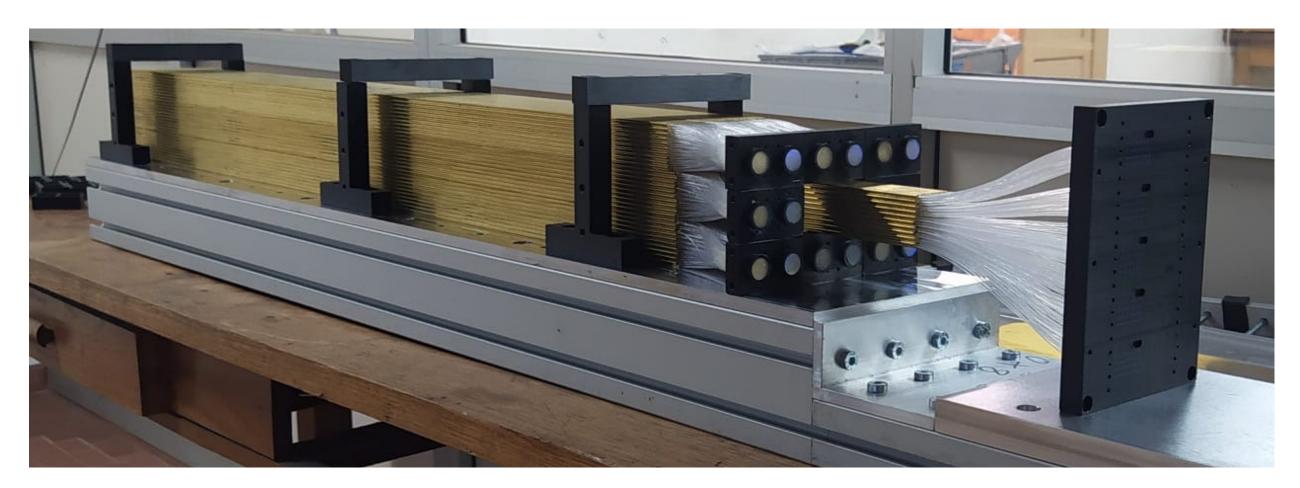
Gabriella Gaudio on behalf of the IDEA Dual-Readout Calorimeter Collaboration June, 3rd 2021

- Update on 2020-prototype •
- Development for Had-prototype
- **Synergies**



(a lot of details in the "additional material" section)

Capillary-tube based Prototype



Capillary:

- 2mm outer diameter, 1mm inner diameter ●
- Material: brass CuZn37 lacksquare
- Absorber structure assembled in RBI (Zagreb) lacksquare

lacksquare





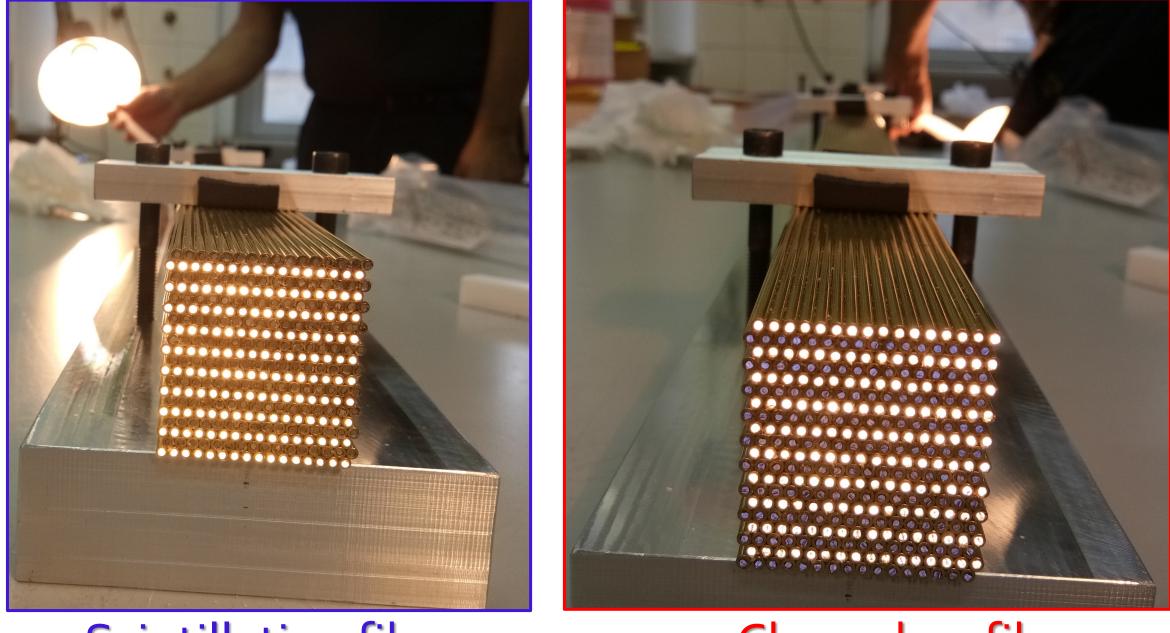
 10×10 cm² divided in 9 towers, Im long

16x20 capillary each (160 C + 160 S)

Readout: I central tower read out by SiPMs 8 surrounding towers read out by PMTs (à la RD_52)

Fibers loaded in a tower

Fibers illuminated from rear end



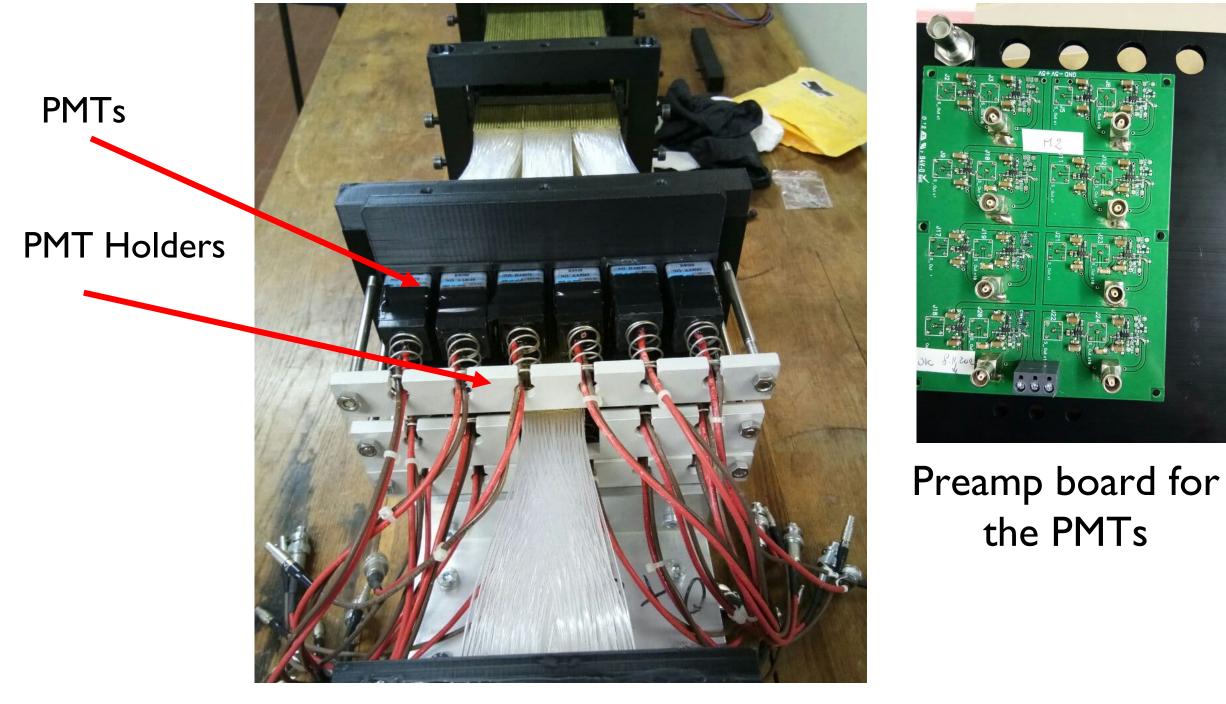
Scintillation fibers





Cherenkov fibers

PMTs readout of external towers



PMT: Hamamatsu R8900 (S), R8900-100 (C) Custom made tapered base

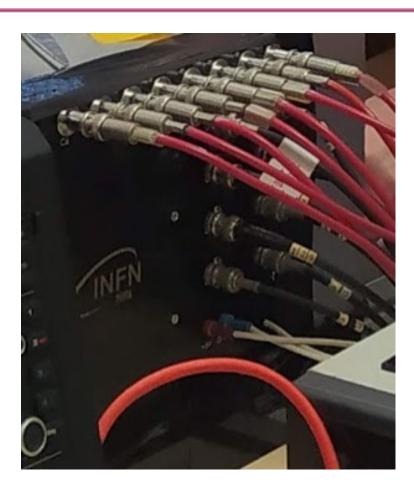
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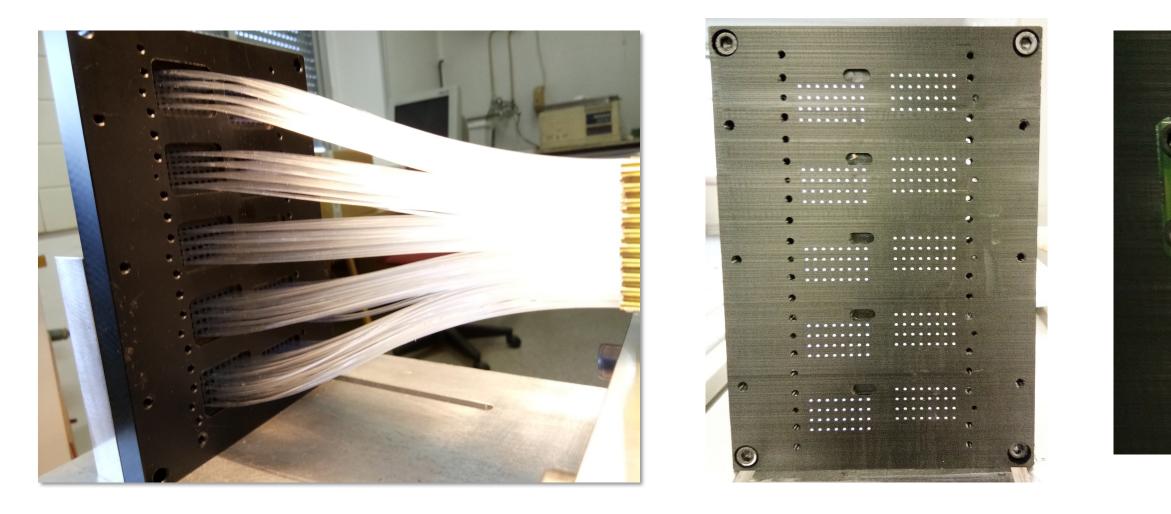
The readout of the PMTs is based on Caen QDC (V792AC)

SiPMs readout of central tower



SiPM: SI4160-1315PS from Hamamatsu Cell size: 15 µm

Sensor packaging not compatible with absorber structure: using a SiPM interface

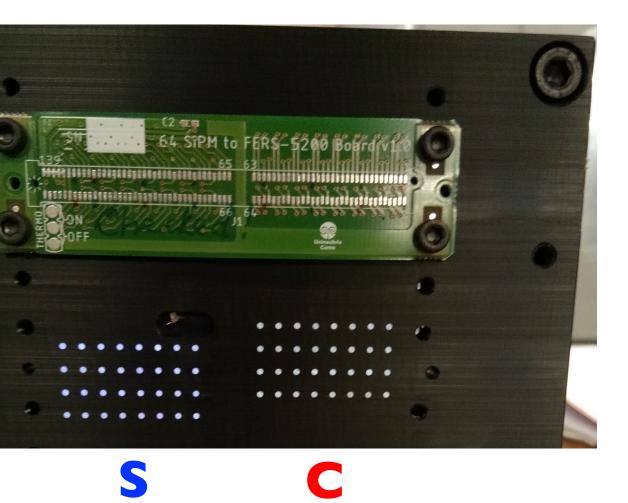




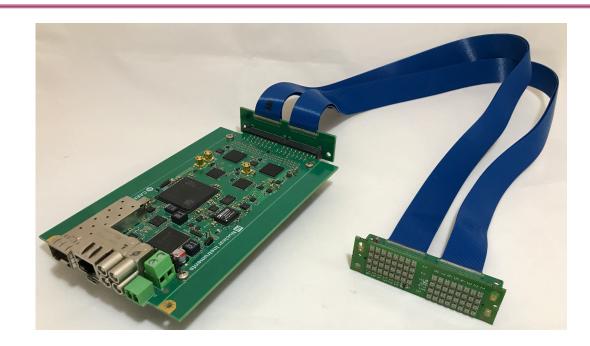


FEE – Boards 5 Boards (320 SiPMs)

Each board—> 64 SiPMs (32 S +32 C) to FERS board



SiPMs readout of central tower



Channel 31 Charge measurement Channel 0 8-bit input DAC Low gain Preamp low gain OUT IN from to High gain Preamp high gain **SiPM** DAQ Temperature sensor bandgap Dual DA 10-bit DA G.

is based on the Caen FERS system (A5202) 5 boards required

- compensation
- (LSB = 500 ps)



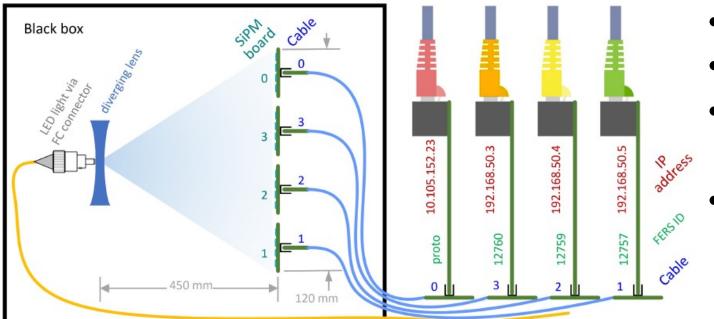
The readout of the highly granular module (320 SiPMs)

• Two CitiroclA for reading out up to 64 SiPMs • One (20 - 85V) HV power supply with temperature

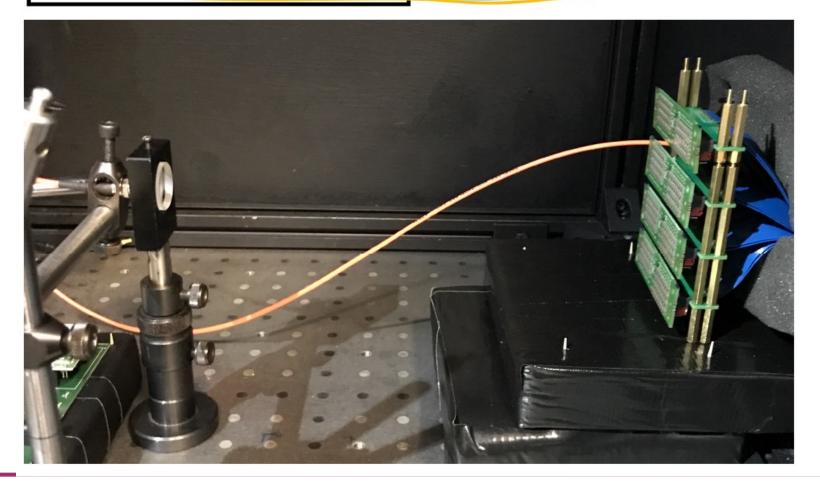
• Two 12-bit ADCs to measure the charge in all channels • Timing measured with 64 TDCs implemented on FPGA

• 2 High resolution TDCs (LSB = 50 ps) • Optical link interface for readout (6.25 Gbit/s)

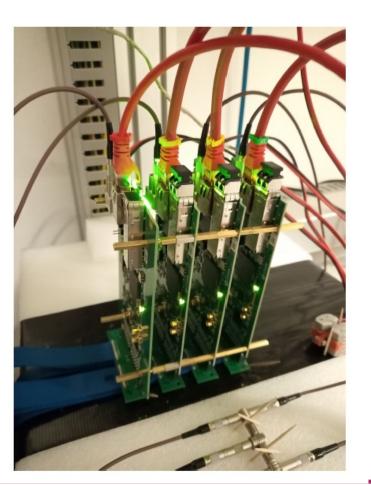
SiPM & FERS system under test



- FE-boards 5/5 qualified •
- FERS 5/5 qualified
- Data Concentrator: soon
- Channel equalization based on an automatic 3-steps procedure. Found set of optimal operational parameters



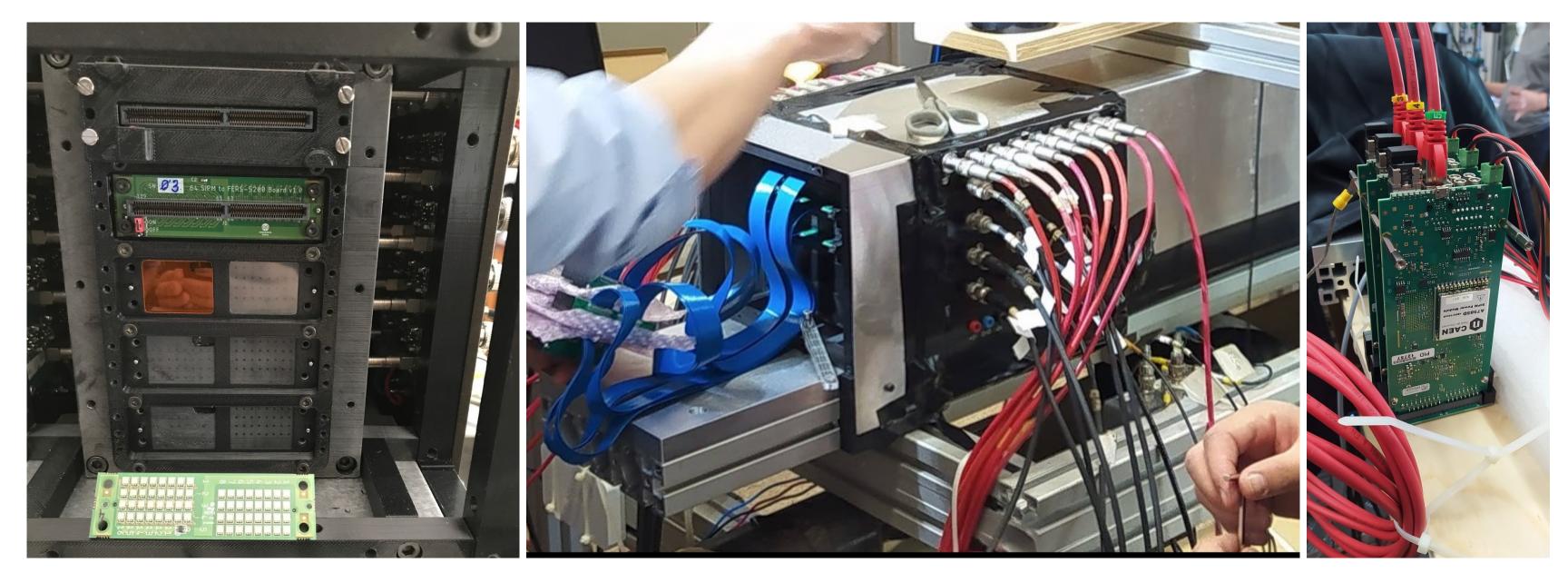








2020-prototype integration



- Integration of FEB boards, FEB + FERS system testing
- Check equalization by stimulation single fibre with a pulser
- Integration in the cosmic ray stand ongoing

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Testbeam Program

DESY: 14-27.6

- ✦ I-6 GeV electrons
- CERN: 18-25.8 (possibility to run parasitically in the previous week under evaluation)
 - ♦ 6-180 GeV electrons and muons

Measurement programs

- Main goal in DESY is system integration and working assessment + low energy measurements \blacklozenge
- CERN measurements will allow to focus on position, angular, energy scans \blacklozenge
- Timing measurements





Prototype 2021

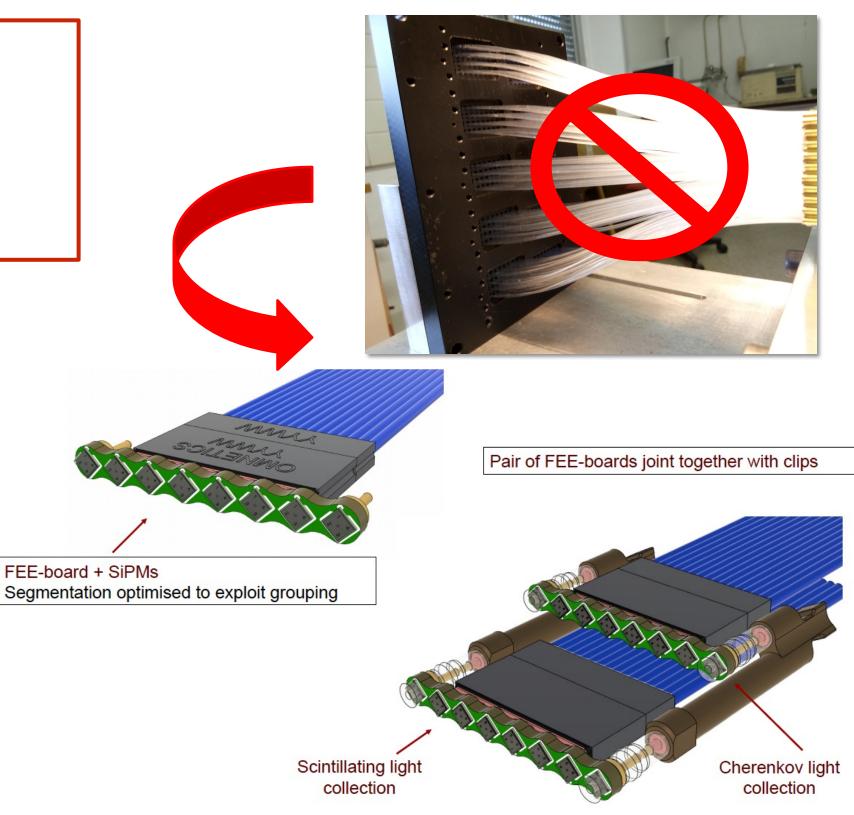
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Construction of a few MINIMODULEs to study:

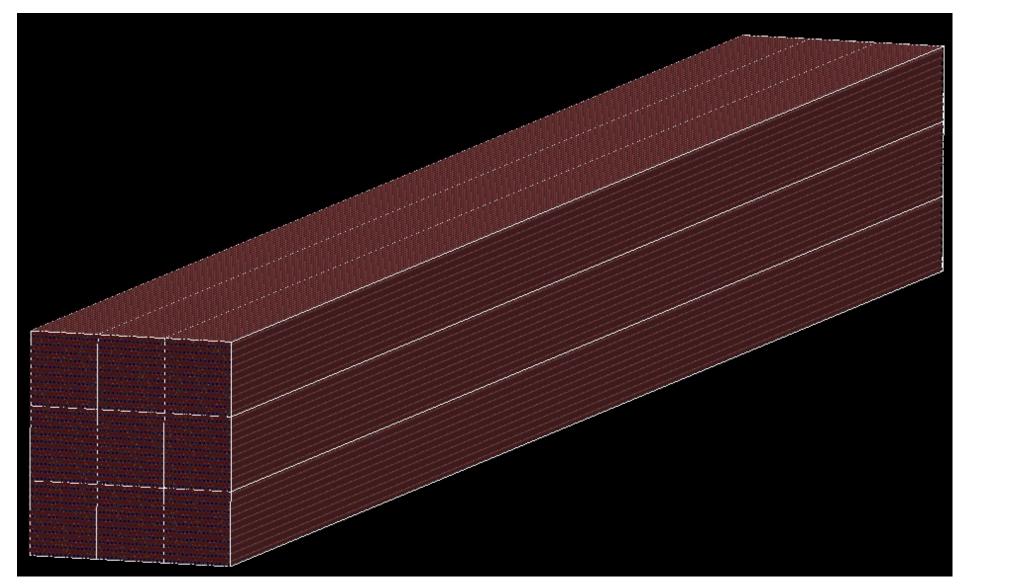
- Assembly procedure
- Reproducibility of assembled modules
- Mechanical supports
- + Material to assess material choice and baseline choice
 - Mini FEE Boards (8 ch) equipped with SiPMs and micro connectors.
 - Costs are dominated by PCB printing area
 - Qualification of single signals and signal grouping
 - Qualification of power supply for SiPM
 - After preliminary studies, signal caracterization with ASIC under evaluation

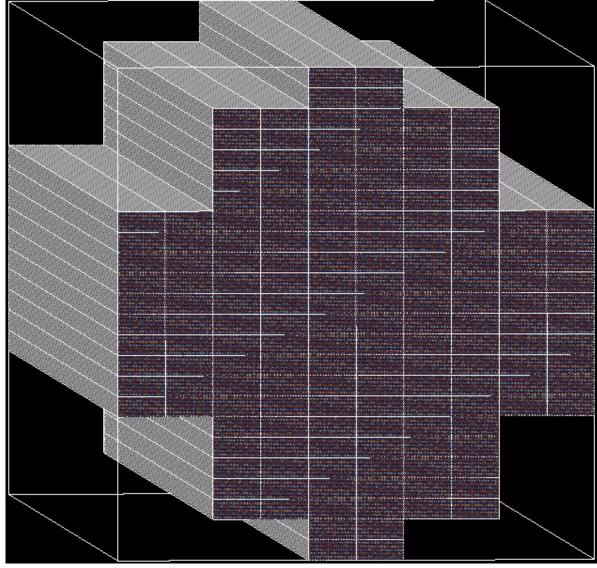




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Prototype G4 Simulation





"em-prototype":

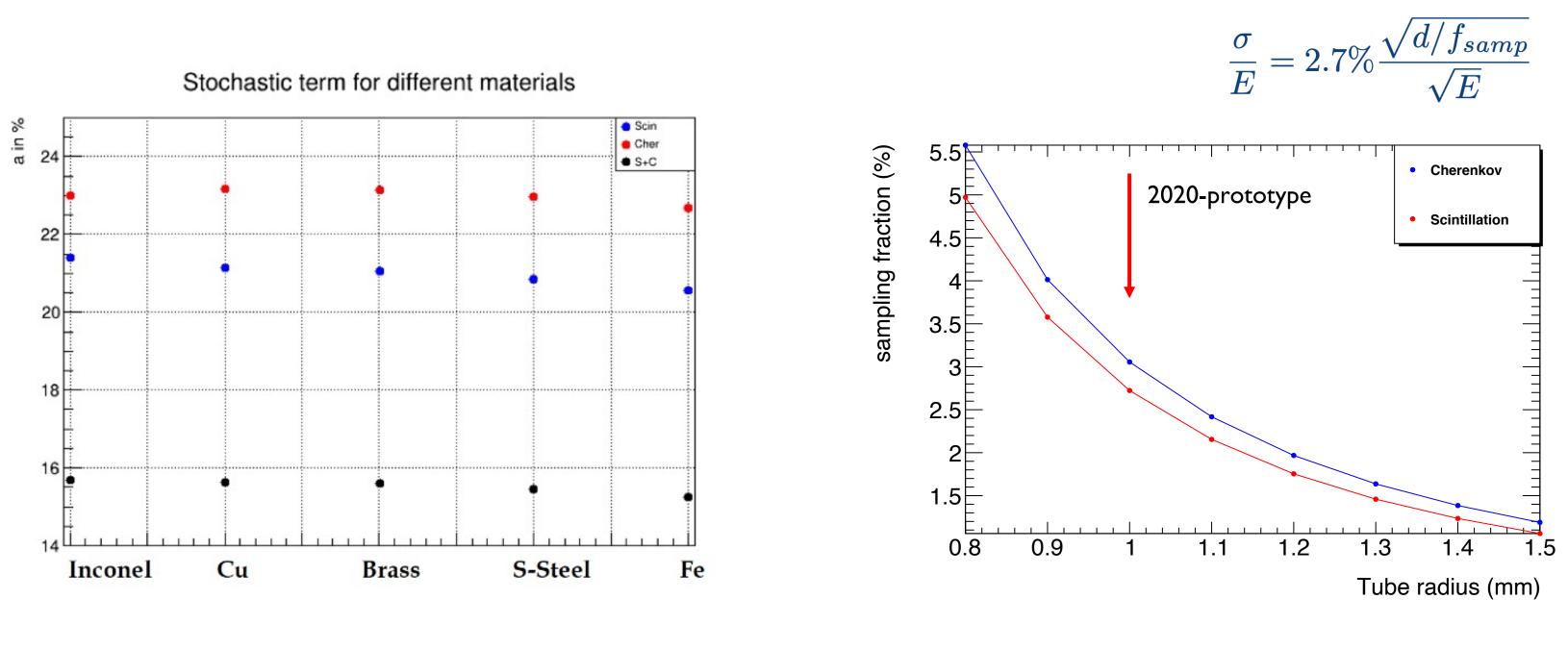
- realistic geometry and readout
- comparison with TB data lacksquare

"Hadronic-size prototype": study performance as support \bullet for technical choice





Choice of material and dimensions



GEANT4 simulation of 2020-prototype



Calculation

capillary for 2021-modules

Company	OD (mm)	ID (mm)	toll. length	Material	cost per tube	total	cost per tube	total
	(mm)	(mm)	(mm)		I 200x 2m		~ 90k x 2m	
	1.6	1.1		AISI 316L				
Rigamonti	+/- 0.020	-0 +0.1	0.1-0.2	EU	I.406	1687	I.334	120000
Albion Alloys	1.6	1.1						
	-0 +0.01	-0 +0.03		CuZn37	3.65	5474	3.09	278000
	2.0	1.1						
	-0 +0.03	-0 +0.04		CuZn37	3.73	5595	3.15	283000

- Other 3 companies contacted, but either non available or costs too high
- Albion Alloys (same for 2020-prototype) could send only sample of 2mm OD, 1m long
- Sampling from Rigamonti tested in Pavia:
 - 50 tubes 20-cm long
 - 10 tubes 2m-long





Stainless-steel tubes



AISI 316L EU (diamagnetic) electro-welded I.6 mm OD 1.6 to 2.0 mm OD also available

- 40/50 good

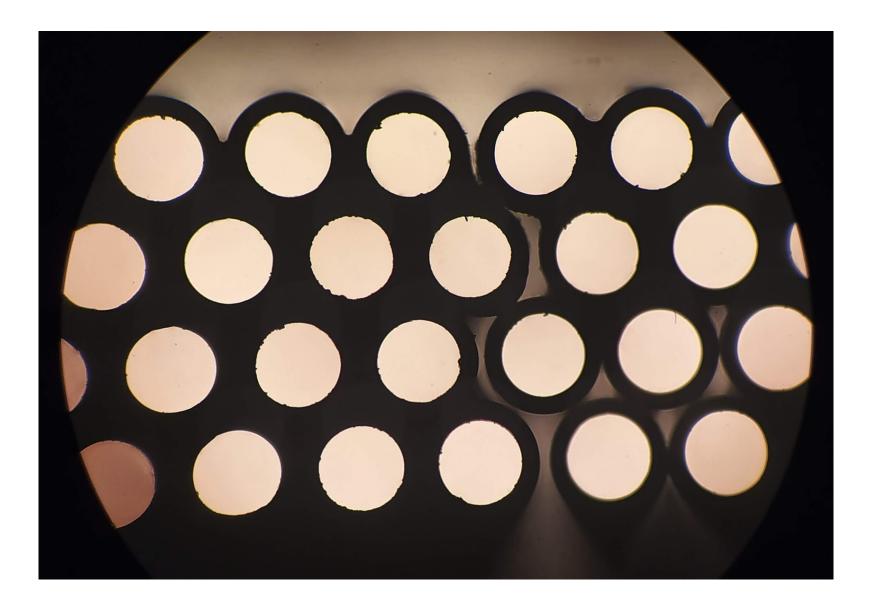




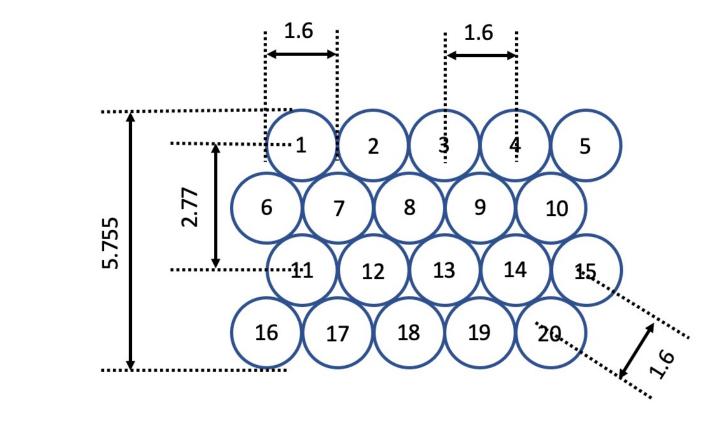
In all the tubes a scintillating fibre was passed

10/50 good after a quick cleaning with a drill-bit

Assembly tool for testing



produced sample tested for stiffness and measured with a microscope



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- Obtained distributions for center-to-center distances with RMS ~ 20 μ m
- Quality of the tube (OD: RMS 2 μ m; ID RMS 6 μ m) seems to be sufficient to guarantee good quality of the assembled array Great simplification on the assembly tools

Budget update

- Order for material to construct modules not yet done
 - finalization of dimension of tube and circuitry for mini-FEB ongoing
 - fibre chosen
 - acquire 2 new PMTs from Hamamatsu (new type)
 - SiPM: choice under finalization
- Design of mechanical assembly system
 - under development (finalization with tube dimensions)
- Test system for mechanical test and fiber test to be developed
- Board to qualify ASIC SiREAD
 - The evolution of the SiRead is the HDSOC, but the evaluation board is not yet available. For this reason we are considering to delay this activity at the next year





Left: R11265U series, Right:

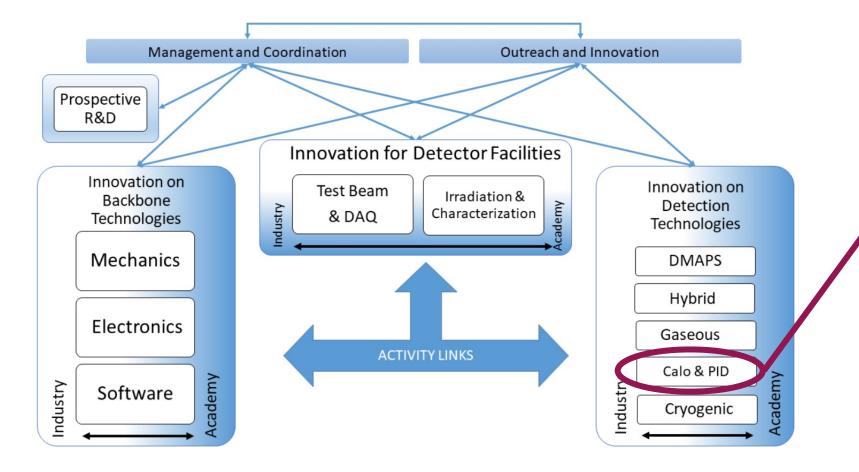
- borosilicate window, UBA (-200)
- UV glass window (extended to 185nm) UBA (-203)



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AIDAInnova



AIDAInnova Program Start: April 1st, 2021 Duration: 4 years

Deliverable: D8.4 : Construction and qualification with beam of 10×10 cm², 2 m long, prototypes [M46]

Total founds INFN: 120 k€

- equipment (20k€)

Other partner in the task:

- Univ. of Sussex (120k€)



WP 8.4.2: Development of highly-granular dual-readout fiber-sampling calorimeters

INFN-BO, INFN-MI, INFN-PI, INFN-PV, U. Sussex, RBI. CAEN + INFN-CT + INFN-RMI

• 40k€ from EU: 2y of PostDoc position (possibly 4 if we cofounded by INFN/Phys. Department • 80k€ cofounded by INFN: Personel time + Travel and

CAEN (Total 60 k \in : 30k \in from EU + 30 k \in cofounded by the company): aim at development of RO boards for DRC

ECFA R&D Detectors



24 March 2021 to 30 July 2021

Europe/Rome timezone

Rich program of symposia: <u>https://indico.cern.ch/event/957057/program</u>

ECFA Detector R&D Roadmap Symposium of Task Force 6 Calorimetry

■ Friday 7 May 2021, 08:59 → 18:05 Europe/Rome

Roberto Ferrari (Universita and INFN (IT)) , Roberto Ferrari (INFN Pavia (IT)) , Roman Poeschl (Université Paris-Saclay (FR))

<u>https://indico.cern.ch/event/999820/</u>

- A talk specific on "R&D for Dual-Readout fibre-sampling calorimetry"
- Dual-readout mentioned in other talks





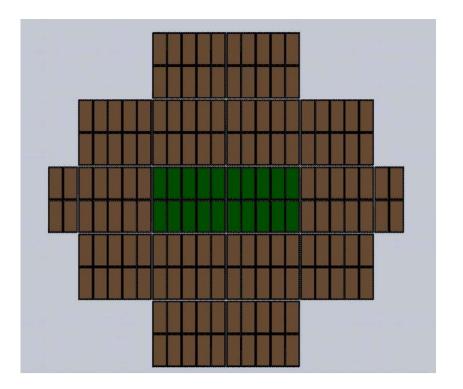
ECFA Detector R&D Roadmap



Call GR5 & PRIN

Re-submitted project to CSN5 call

- ✤ BO, CT, MI, PI, PV, RMI, TIPFA
- Updated wrt work done in 2021
- Large number of supportive letters



PRIN Project: MonSiPh

- R&D on dSiPMs
- target applications in biophysics and particle physics
- Uni. Insubria (PI Massimo Caccia), Uni.
 Piemonte Orientale, Uni. Padova, Uni.
 Pavia, INFN



Additional Material

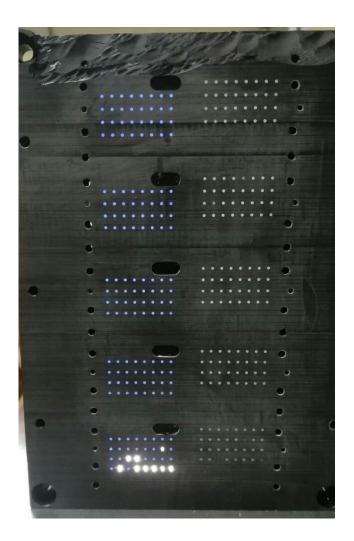
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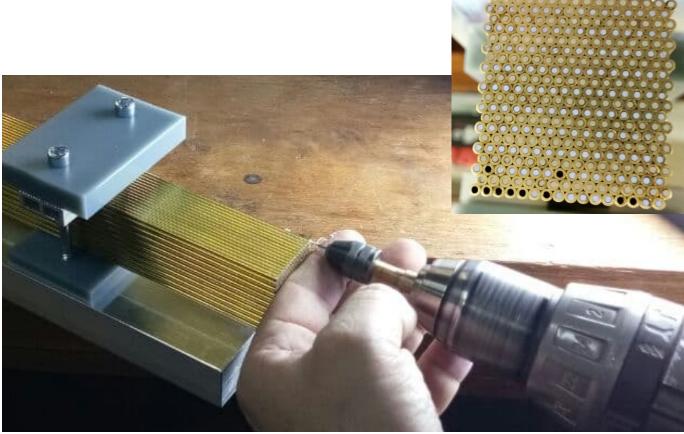


Reloading fibers

While milling the SiPM interface surface, the machine stopped, hitting the surface itself 10 S fibers broken between module and interface

➢Glued







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Fibers reloaded Damages Recovered



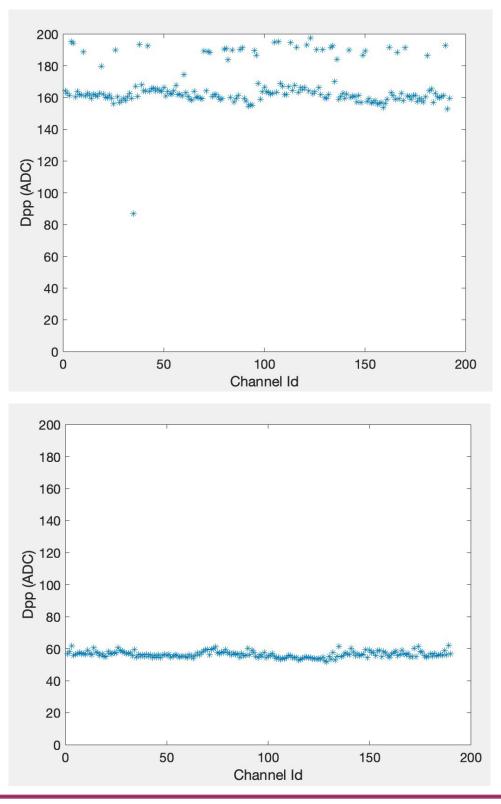


Precise Drilling

- Channel equalization is based on an automatic 3-steps procedure:
 - HV scan to measure the Breakdown voltage (all SiPMs will be operated at the same V_over)
 - ✦ HV-Adjust DAC scan:
 - This procedure will allow to calibrate the HV-Adjust DAC for all channels and the fits are used to get the proper settings
 - High Gain DAC scan
 - This procedure will allow to calibrate the High Gain DAC for all channels
- The threshold used for the trigger is defined at the end of the procedure (2.5 or 3.5 ph-e)

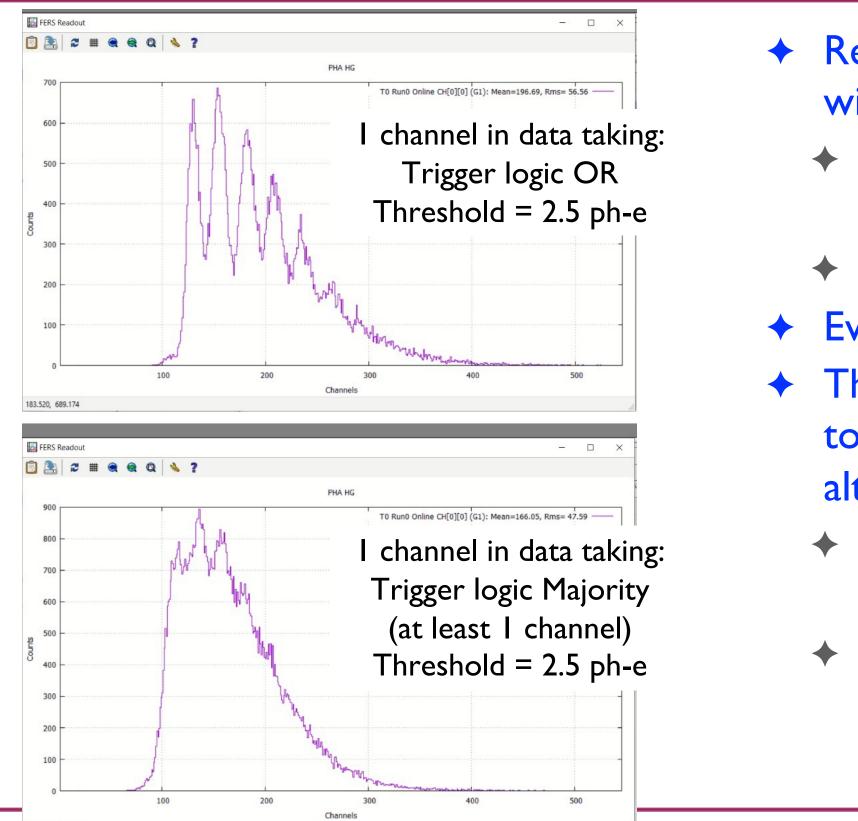
HG = 40 and LG = 0 : enough overlap for the LG calibration and we should cover up to the 60% of the SiPM occupancy





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System qualification



- Data concentrator is not yet available (Caen is finalizing the firmware)
- too risky to use it at the test beam in DESY
- Event accept works as expected (latency < 200 ns)</p> The majority must be improved (it probably comes) too late). In case of problems we can still use
- alternatives:
 - accept (Dark rate measured at the Hz level) phe + event accept (to be checked)
 - The OR logic with a threshold at 3.5phe + event ✦ The OR32 AND2 with a threshold at 2.5/3.5

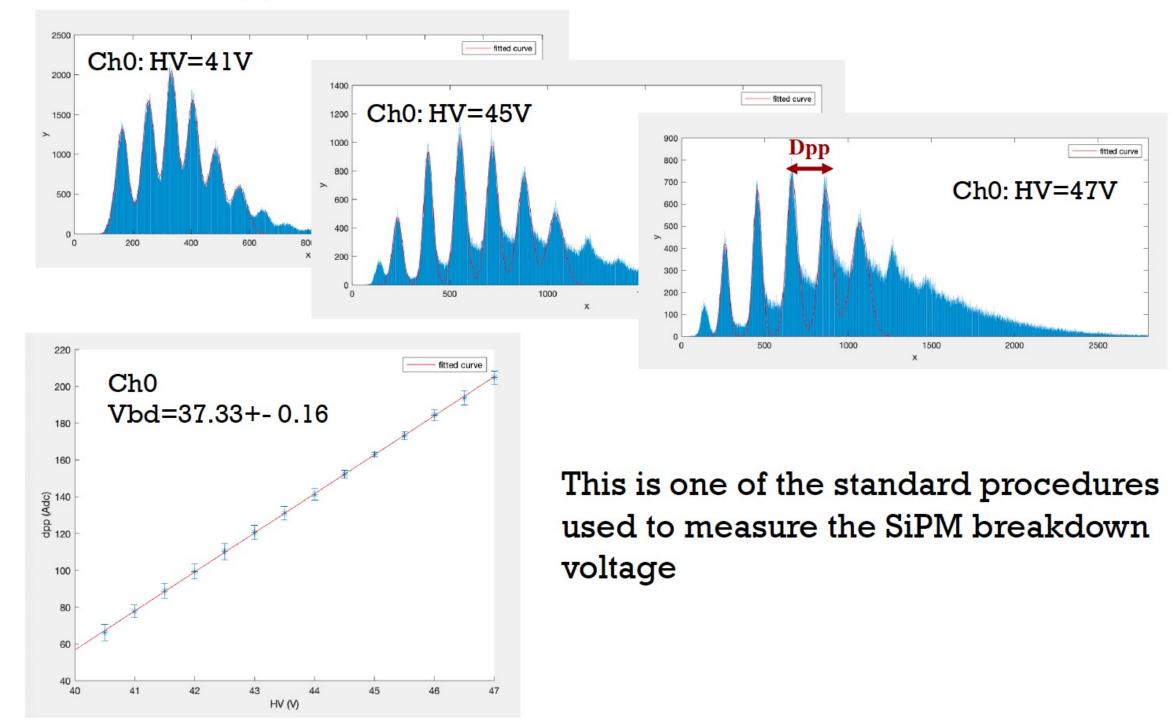
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 Readout system qualification (Multi-board schema without data concentrator): well advanced

Breakdown Voltage

FEE Board1_FERS12757



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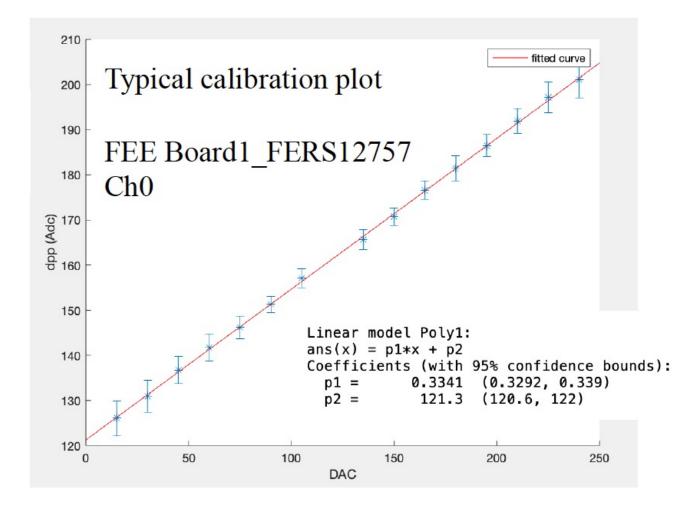


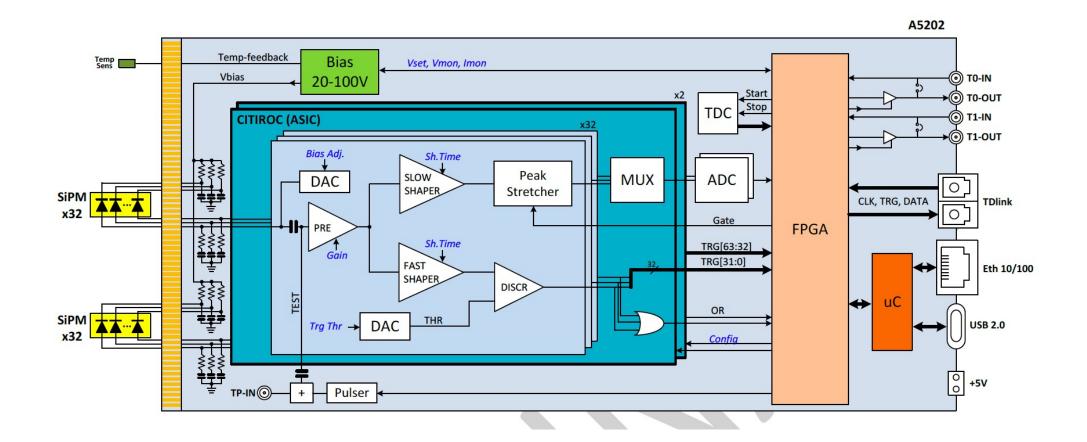


Setup	Vbd (V)
Board1_FERS12757	37.27+- 0.14
Board2_FERS12759	37.37+- 0.19
Board3_FERS12760	37.41+- 0.25

HV-DAC: calibration

The FERS provides the same HV to all channels, but it can be adjusted using 64 DACs (one per SiPM)



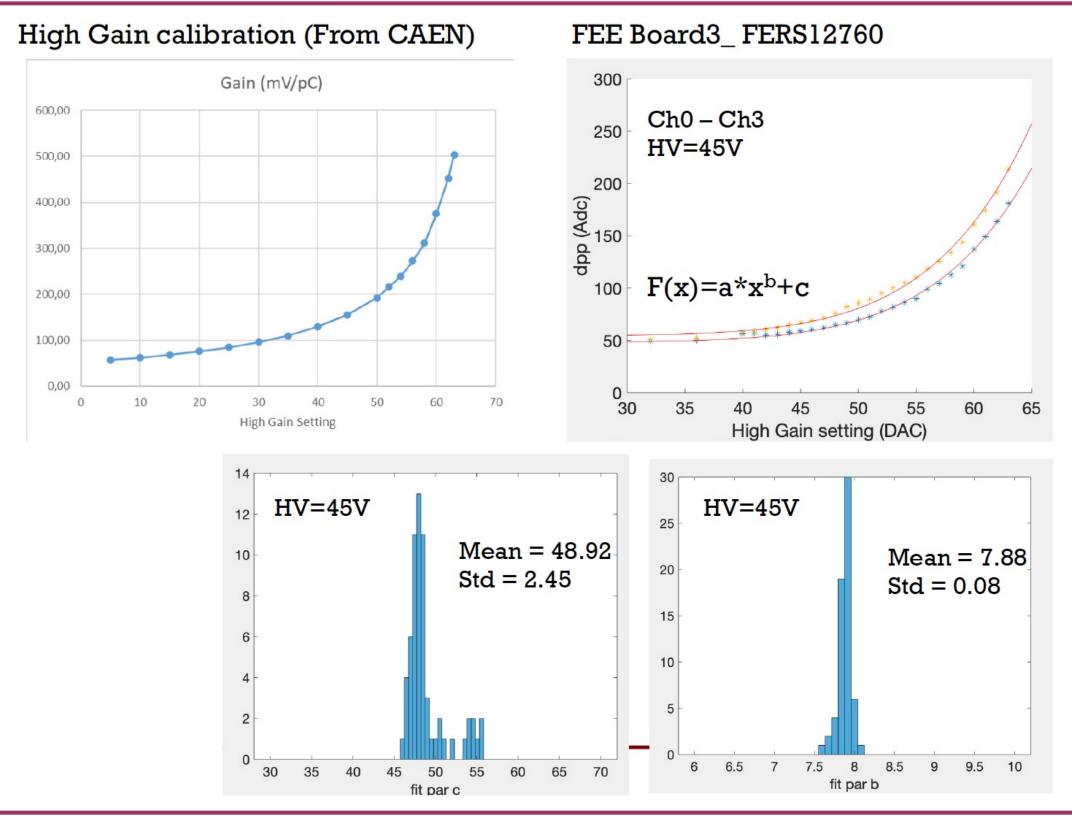


Setup	DAC – Dpp calibration
Board1_FERS12757	0.3353+- 0.0085
Board2_FERS12759	0.3312+- 0.0093
Board3_FERS12760	0.3301+- 0.0126





High Gain calibration



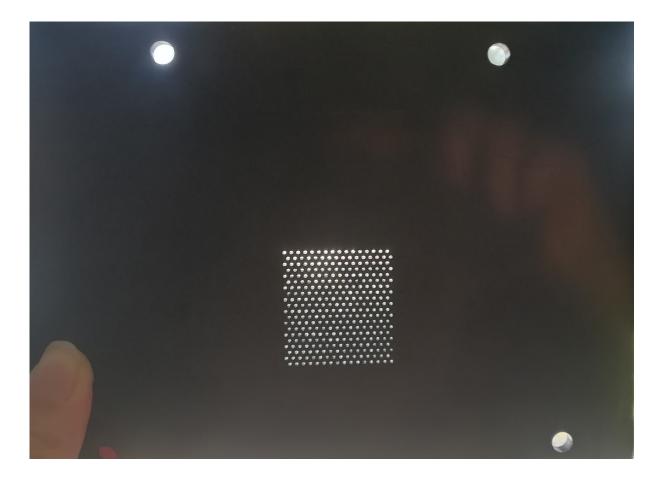
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Integrated system test

- Extra entrance window for single SiPM test ✦
- Single fiber stimulated with a pulser +
- Check equalization (work started, to be finished in next days) **+**

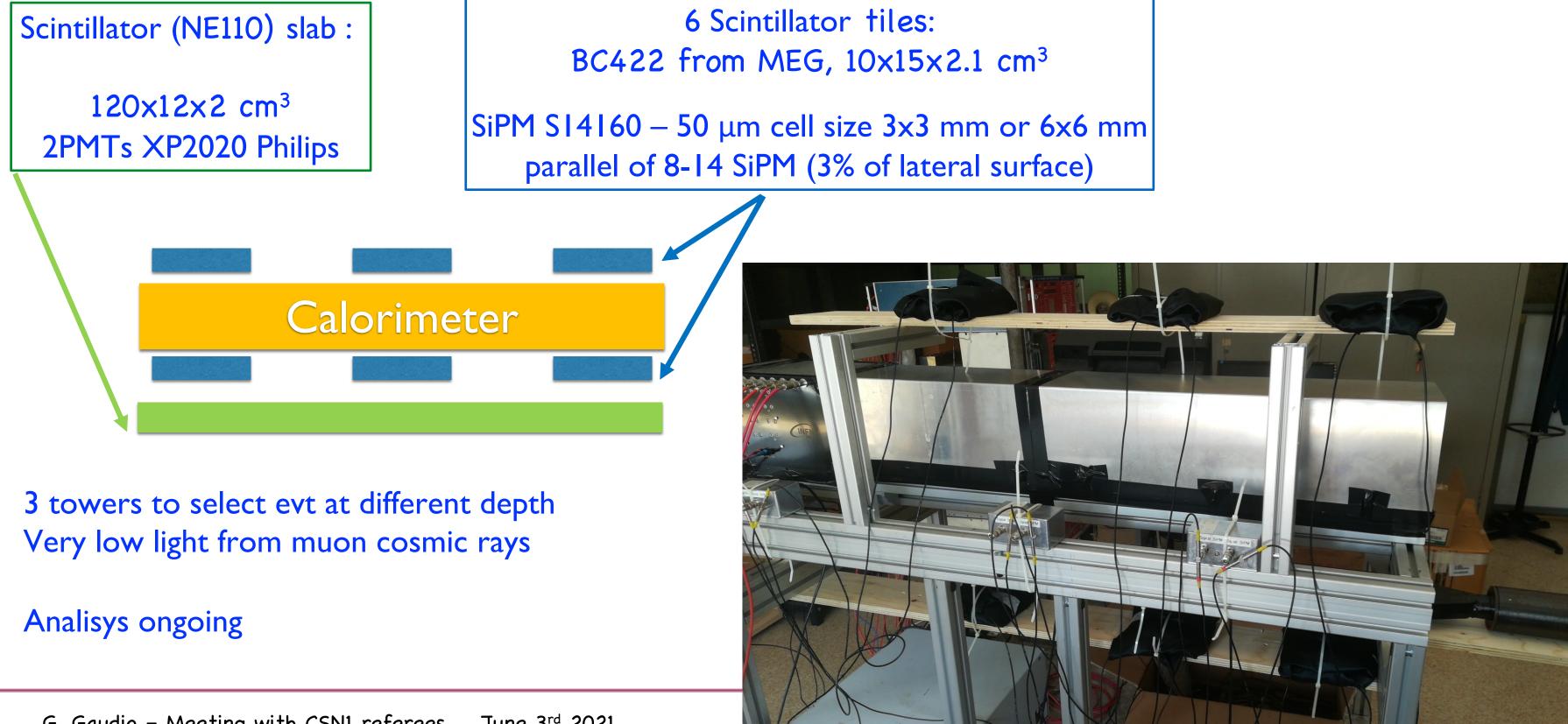








Cosmic-Ray test stand

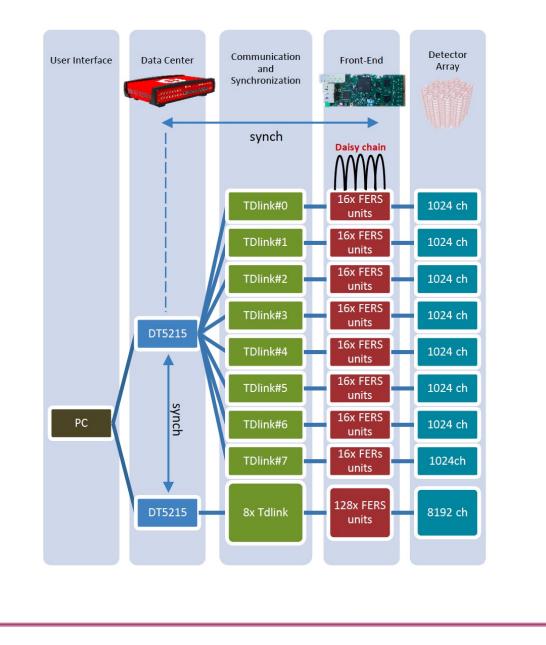


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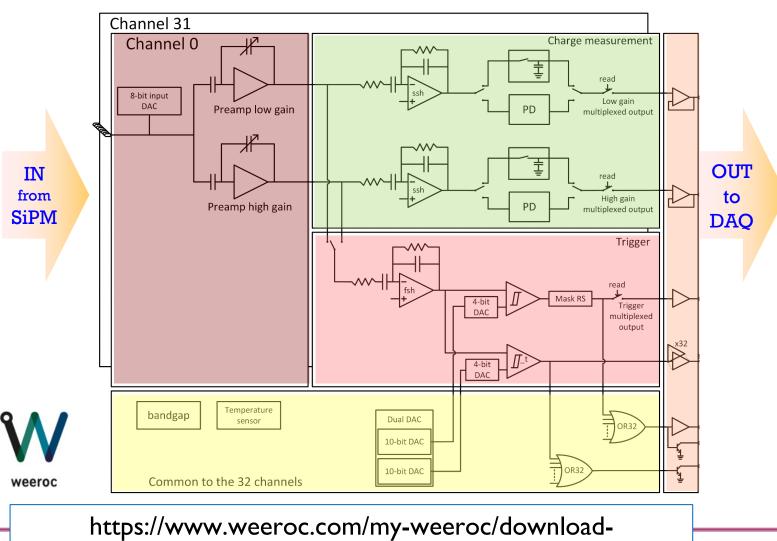


Test beam: readout scheme

- The readout of the PMTs will is on Caen QDC (V792AC) and TDC (V775N) modules \blacklozenge
- The readout of the highly granular module (320 SiPMs) is based on the Caen FERS system (5200) using 5 + readout boards (A5202)



CITIROC 1A: block diagram



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center/citiroc-la/l6-citirocla-datasheet-v2-5/file

Test beam: timing information

- Timing is an important feature for a longitudinally unsegmented fiber calorimeter
- Each FERS has 2 TDCs with high resolution (LSB=50 ps) and 64 TDCs (LowRes) coded on FPGA (LSB=500 ps)
- The LowRes TDCs can be used to measure:
 - ✦ The ToT for each SiPM
 - The ToA for each SiPM wrt the event-accept
- Two signals / FERS can be measured with HighRes TDCs: different options are possible:
 - ♦ Ist option: majority and OR wrt the event-accept signal
 - ◆ 2nd option: majority (cherekov) and majority (sc) wrt the event-accept signal
 - ◆ 3rd option: OR (cherekov) and OR (sc) wrt the event-accept signal



Material Properties

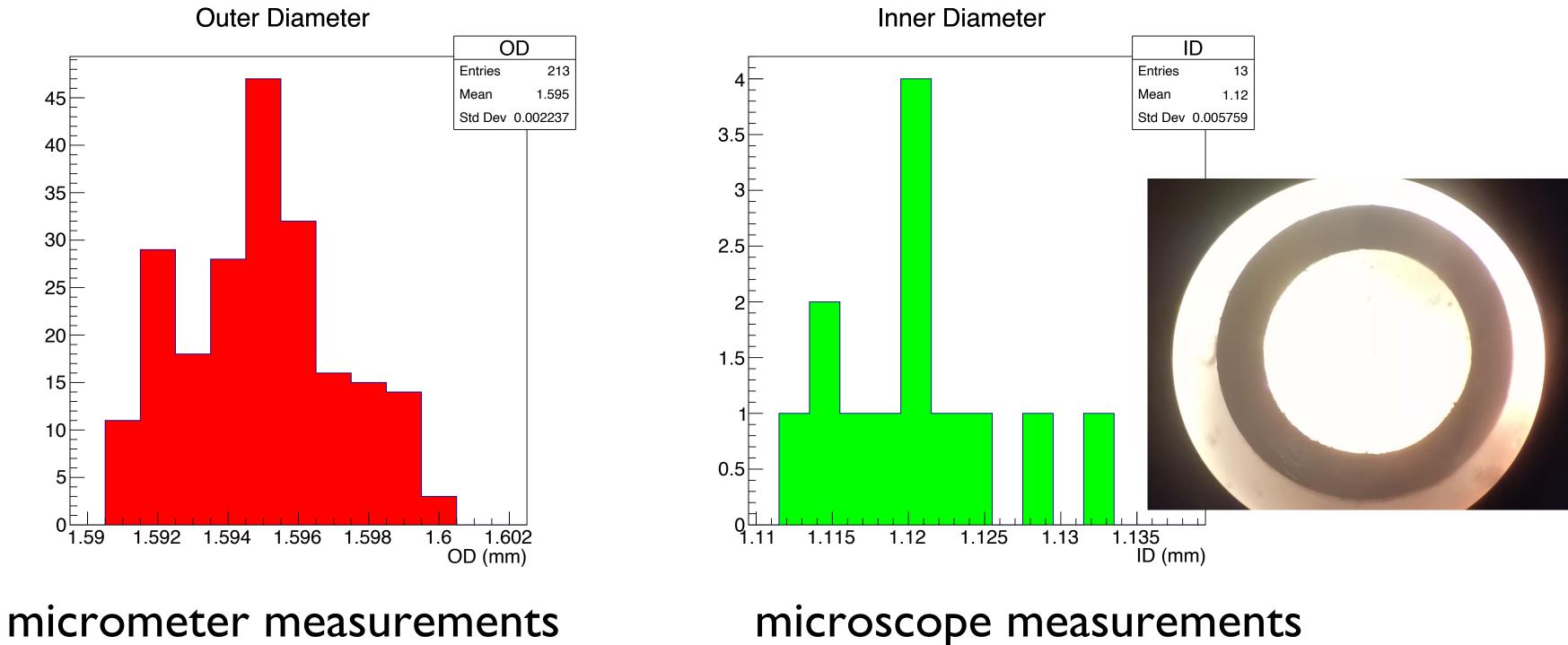
Properties	Copper	PMMA (Cherenkov)	Polystyrene (Scintillanting)	
% in Calo	65%	17,5%	17.5%	
Z	29	H,C,O 1,6,8	H,C 1,6	
Density (g/cm³)	8.96	1.19	1.06	
MIP(MeV g ⁻¹ cm ²) MIP (MeV cm ⁻¹)	1.403 12.57	1.929 2.296	1.936 2.052	
Radiation Length (cm)	1.436	34.07	41.31	
Moliere radius (cm)	1.568	8.422	9.409	

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Tube quality control



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Assembly tool for testing



I - Preparation of I0 tubes ~10 cm long



2 - glue in mixed and distributed on the tube with a small roller





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3 - positioning of the tube in the maquette of the assembly station

> 4 - glue left to set overnight weights apply on the top

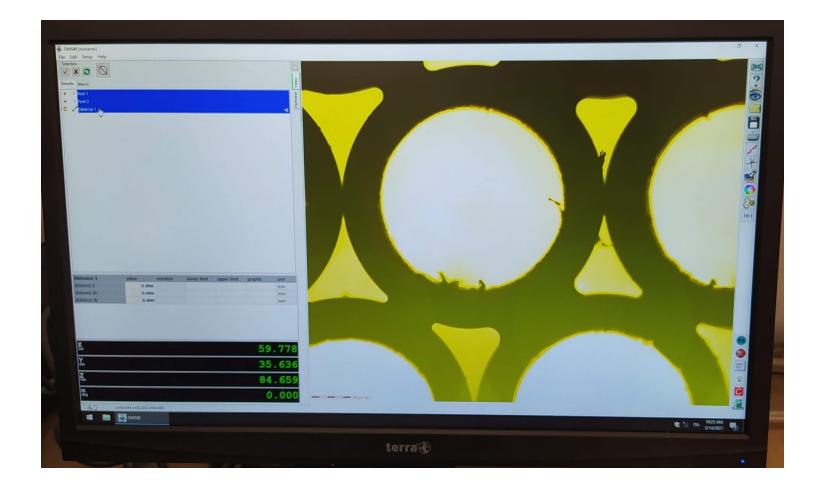
Assembly test - glue choice

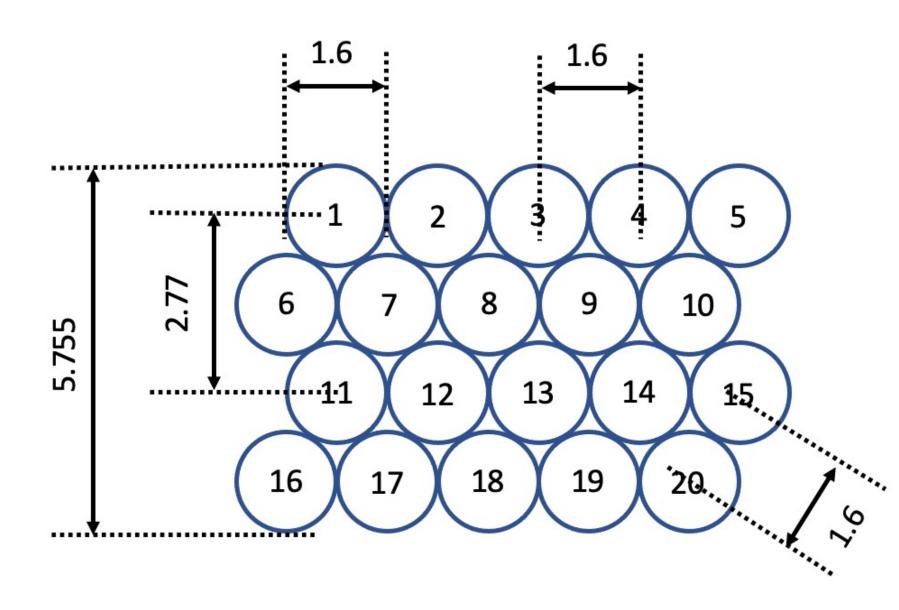
- Purpose was to check different glue types and how distribute them on the tube
 - ♦ DP270
 - ♦ DPI90
 - ♦ DP490
 - Araldite 2011
- We tested also ideas for assembly technique
 - ✦ Teflon tool (avoid gluing of pieces) true one will be rectified stainless steel
 - Small maquette to test how glue is distributed
 - Microscope measurements of the assembly





Microscope measurements





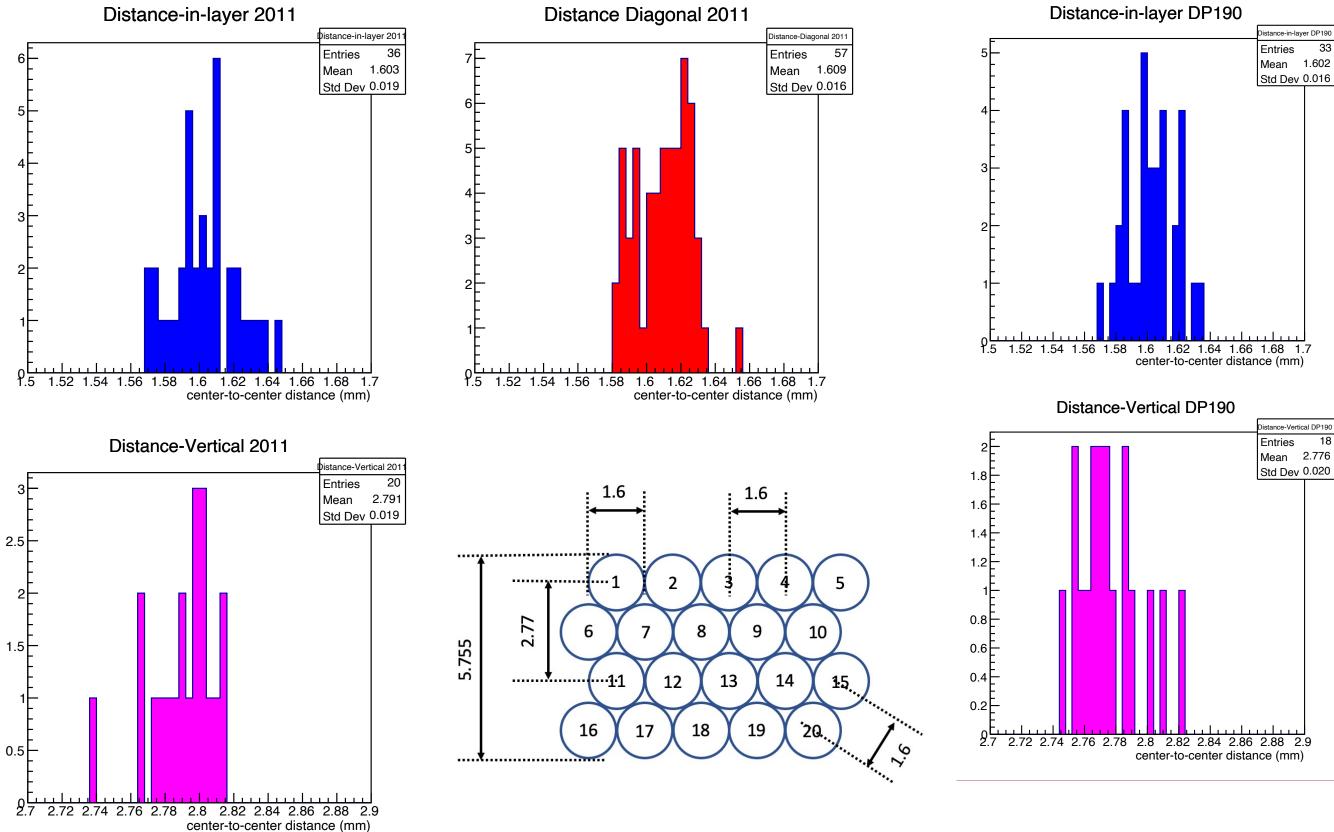
Identify the circle (inner diameter) with transmitted light measure center-to-center distance

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Araldite 2011 and DP190





stance-Diagonal DP190

Mean 1.607

Std Dev 0.022

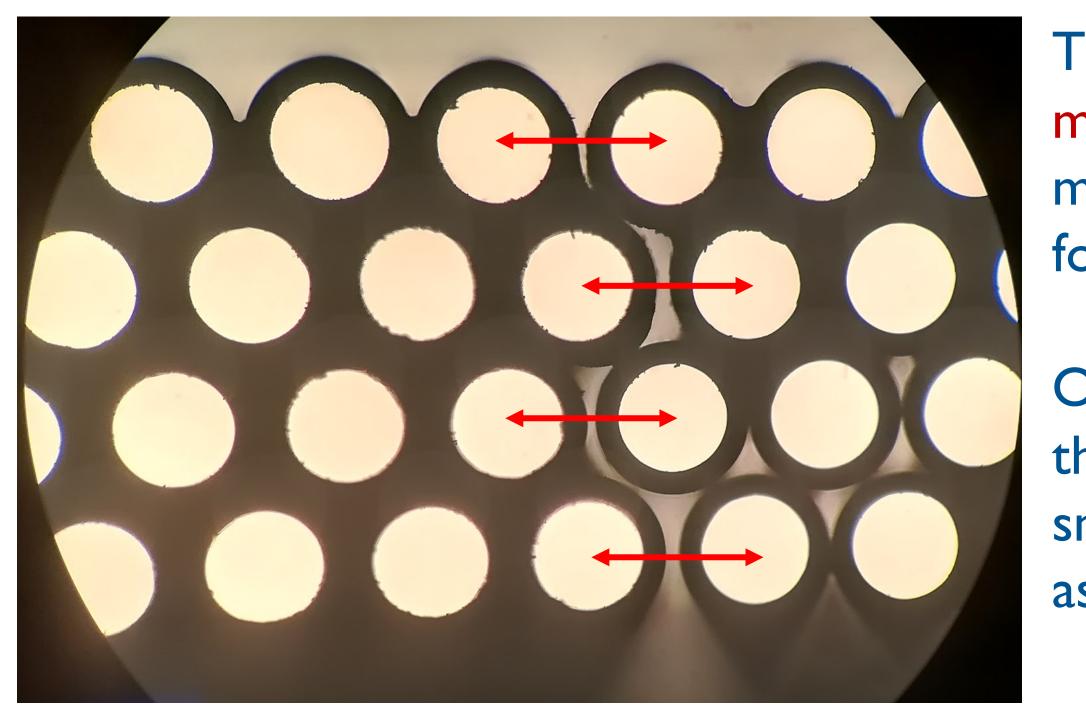
49

Entries

Distance Diagonal DP190 stance-in-layer DP190 33 Mean 1.602 Std Dev 0.016 1.5 1.52 1.54 1.56 1.58 1.6 1.62 1.64 1.66 1.68 1.7 center-to-center distance (mm)

Just for fun

1,719 - 1,723 - 1,701 - 1,758



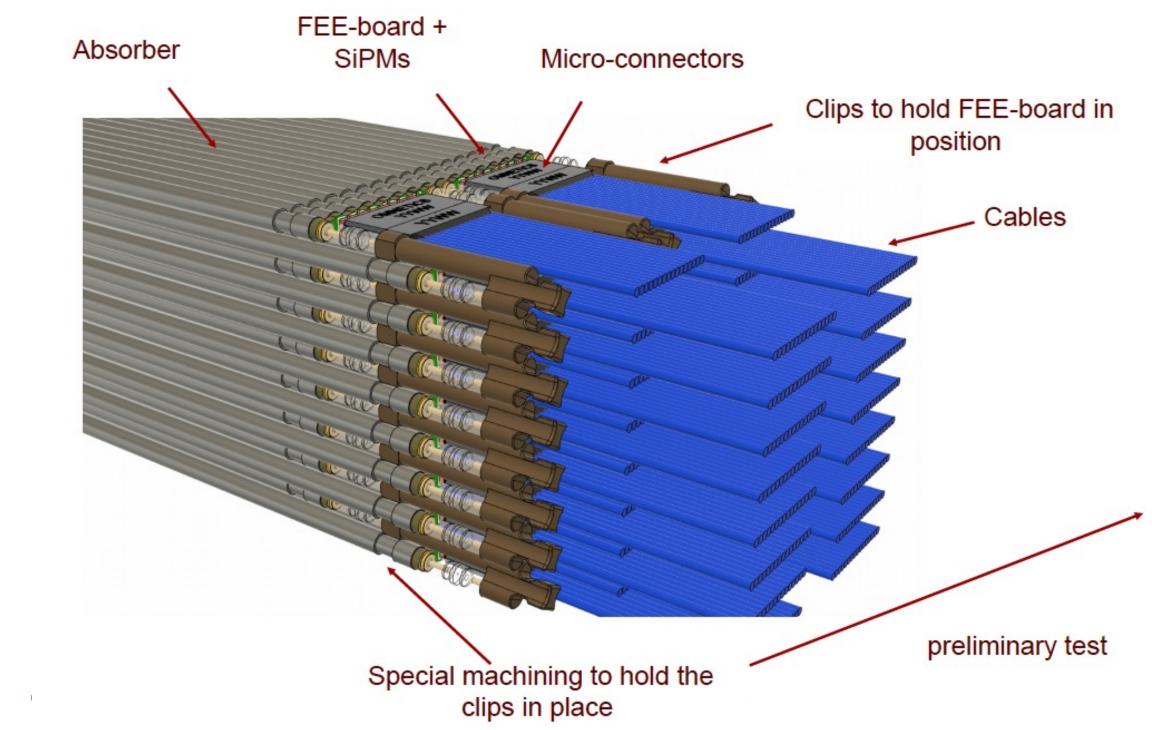
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The Teflon tool was not meant to guarantee mechanical precision, just for glue test

On the other hand, already this has allowed to produce small reproducible assembled arrays

New concept for a true scalable module



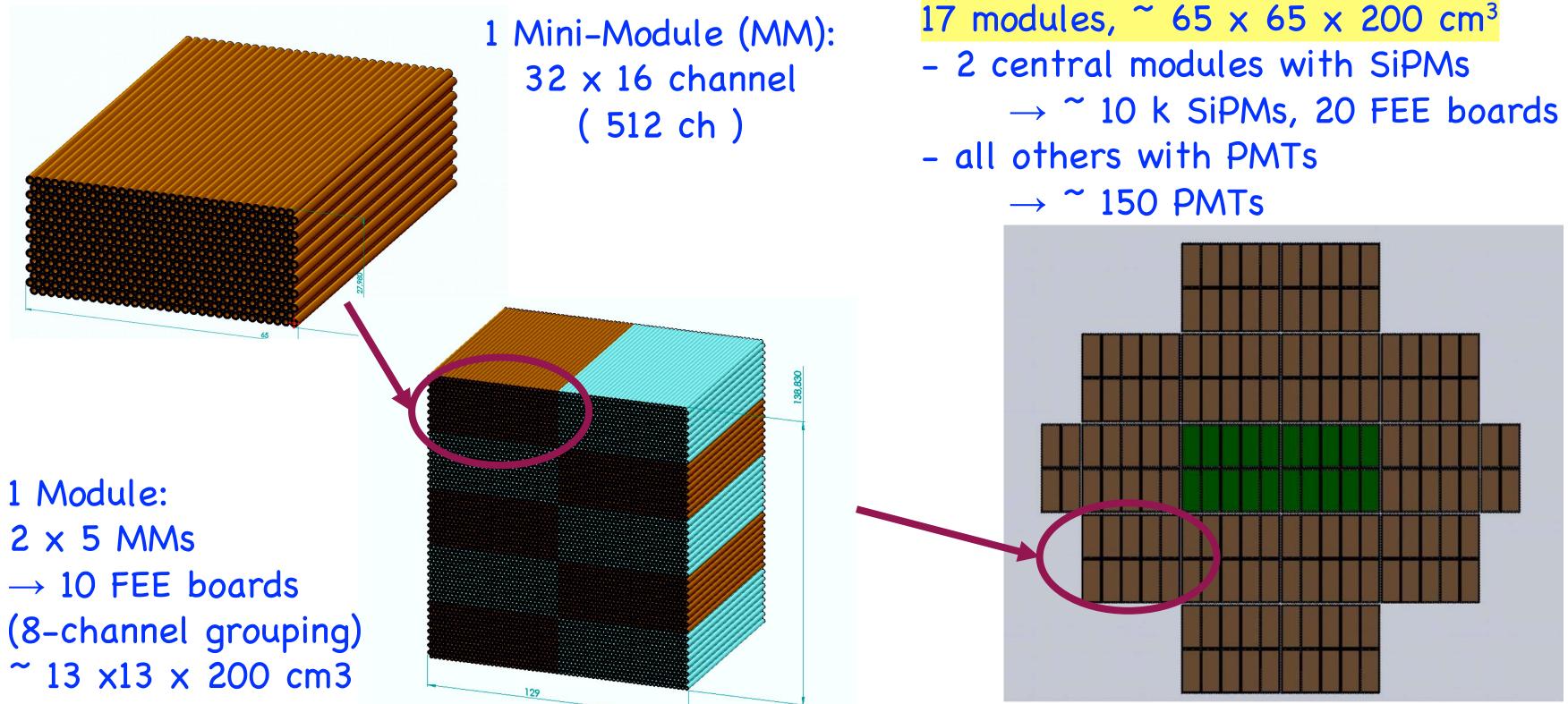
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rom HiDRa project for CSNV

Mid-term plans



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