

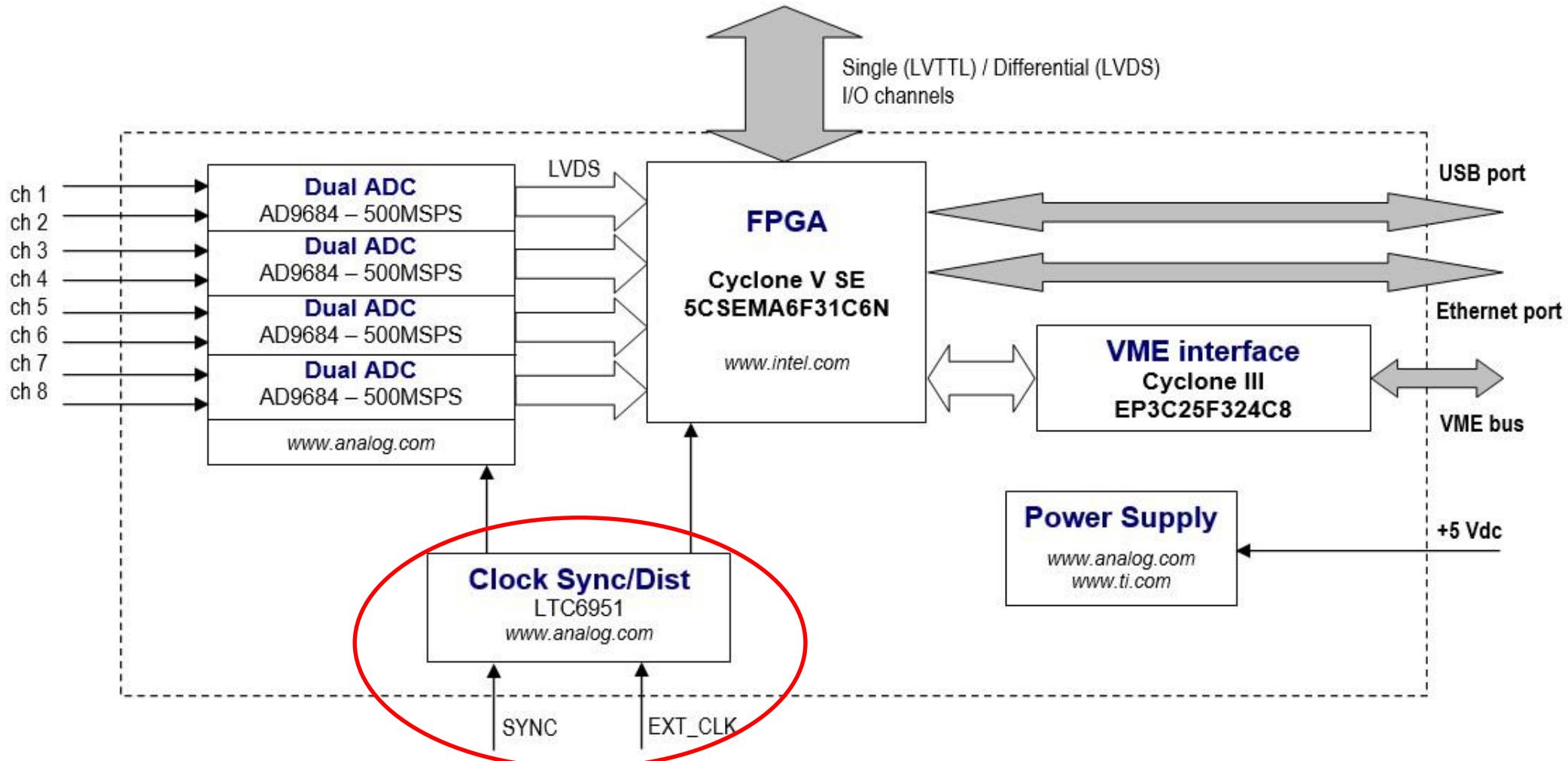
# CYGNO

# Photomultiplier DAQ: Clock Synchronization

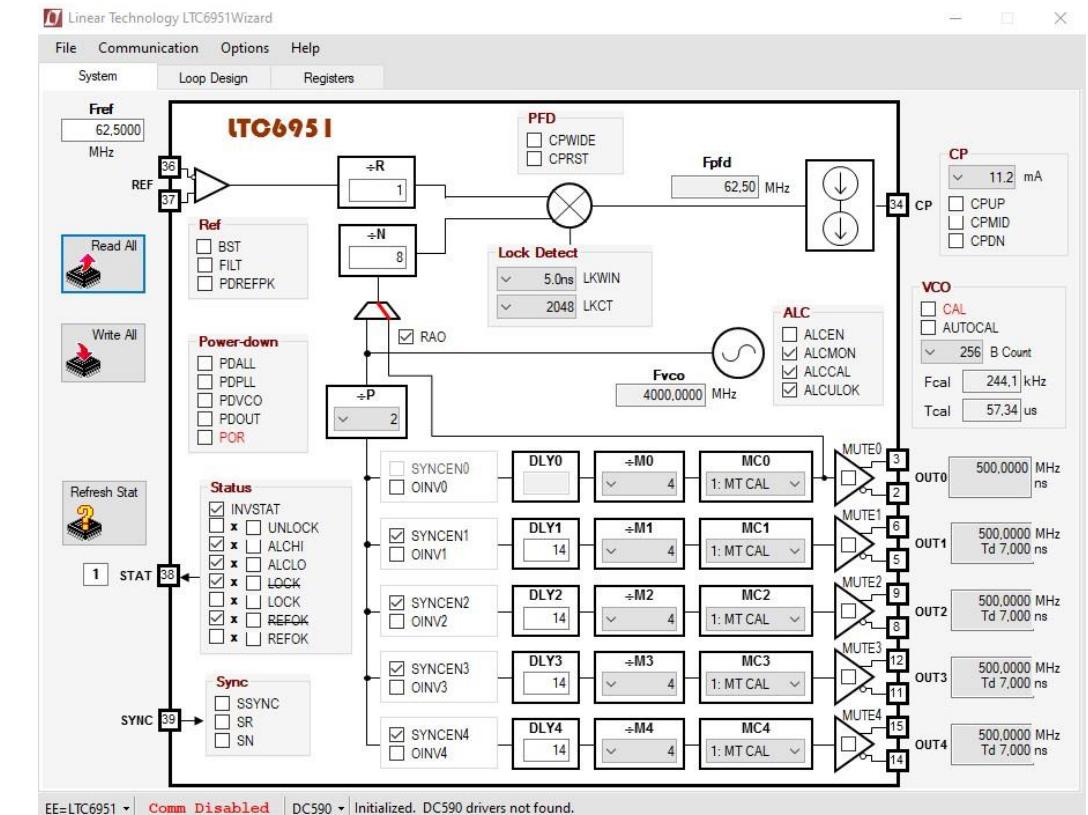
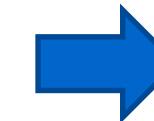
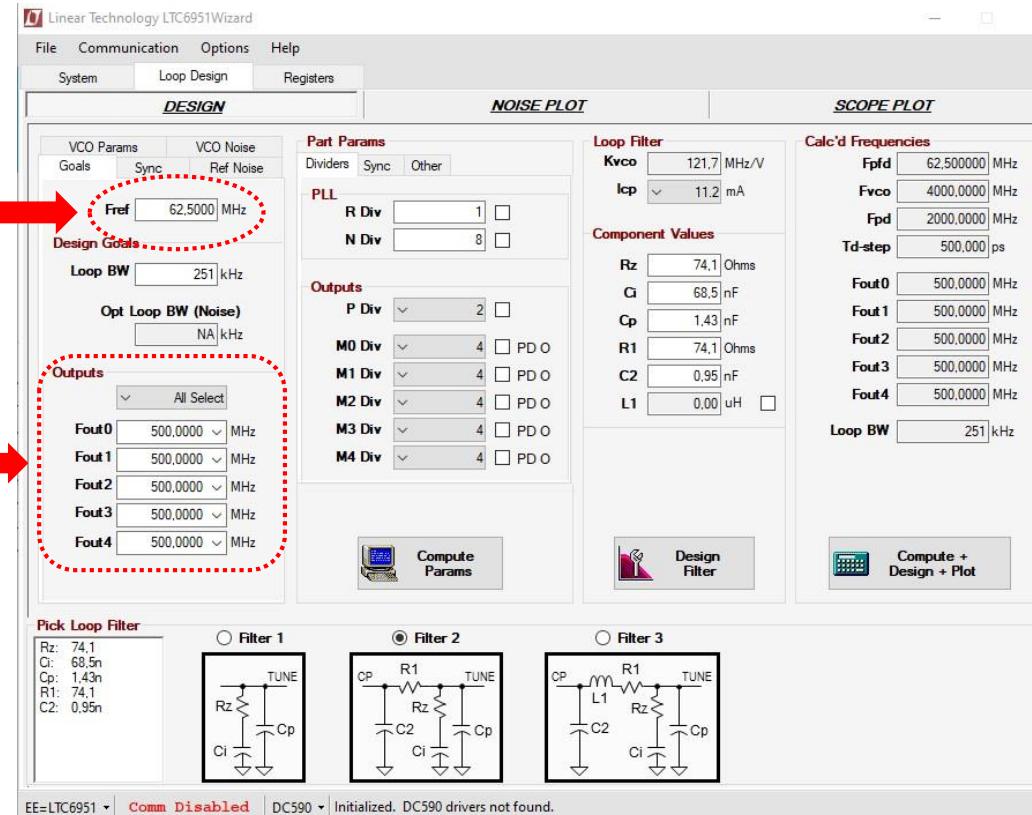
Herman Pessoa Lima Jr

13 Jan 2021

# Module overview



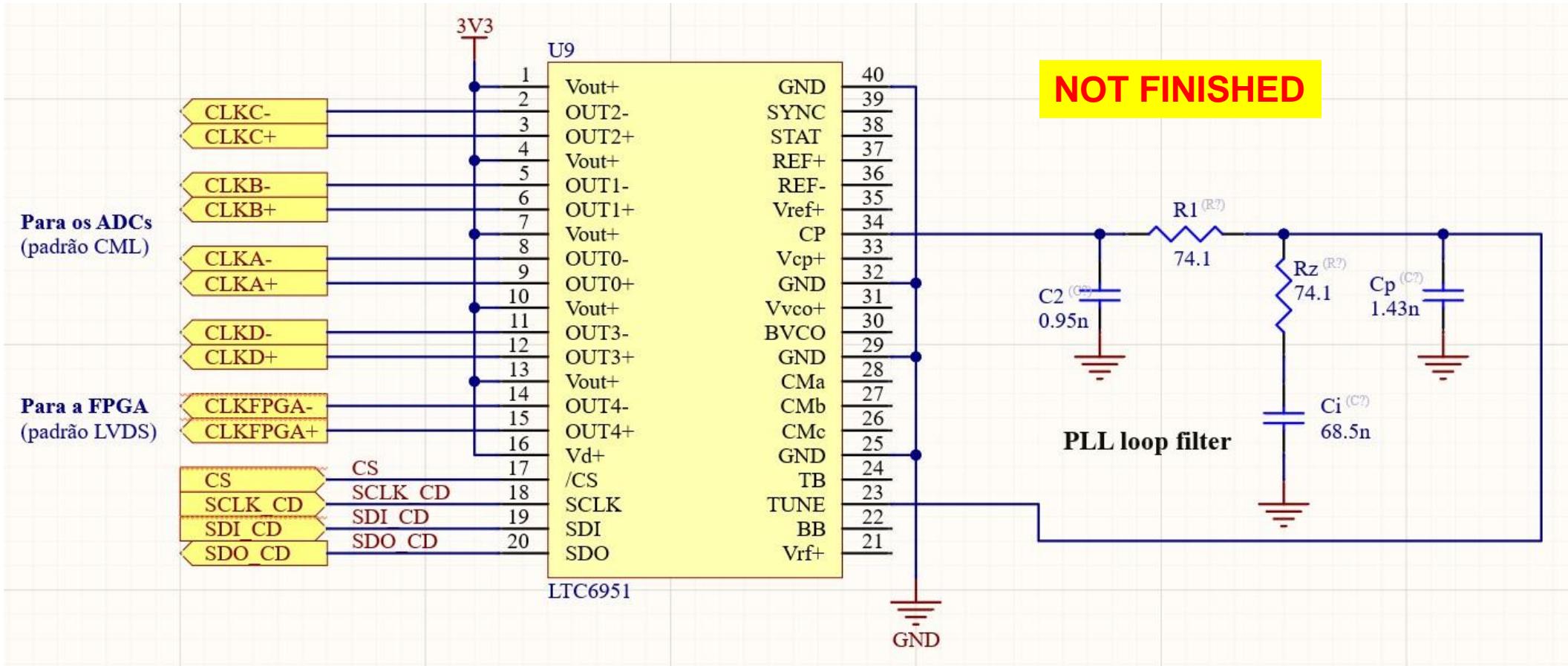
# Synchronization circuit design



Definition of parameters

Resulting configuration of LTC6951 IC

# Synchronization circuit design



# Synchronization overview

Example of a 32-channel system:

