Low Beam Intensity Monitoring at CNAO experimental room

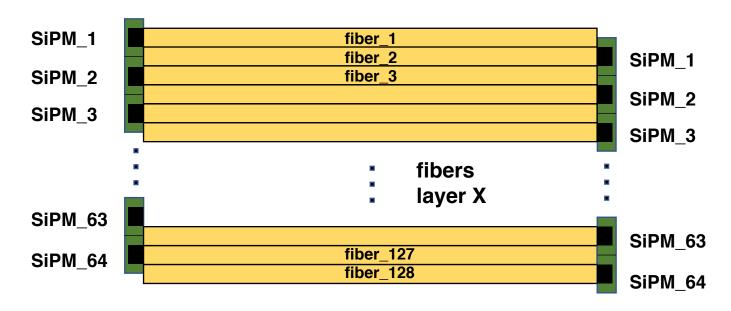
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Goals of the Beam Monitor for low intensities

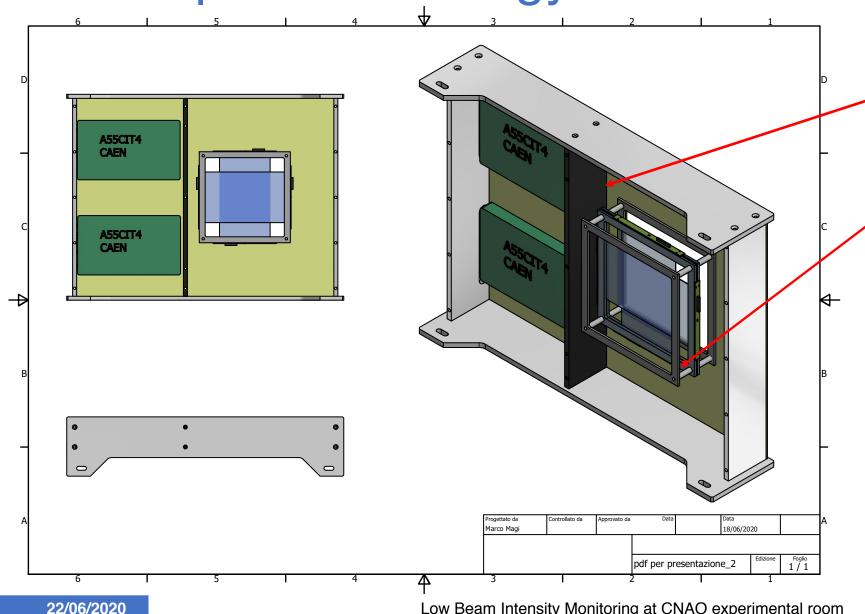
- Goal: Provide a feedback to the Dose Delivery system and to the research teams when working in the CNAO experimental room about the beam properties at low intensity, in terms of:
 - 1. primary ions counting
 - 2. beam position
- Motivations: when beam rate intensity is < 1 MHz, CNAO standard beam monitors (layers of parallel-plane ionization chambers closed in two BOX equipped with strips and pixels to measure the beam flux and x-y position) are almost 'blind' and so not able (very inefficient) to count the number of the impinging primaries ions
- The goal is to develop a monitor to measure rates up to 10 MHz covering an active area of ~ 13 x 13 cm² able to provide feedback about the beam position with an experimental resolution on both views (x,y) of ~1mm

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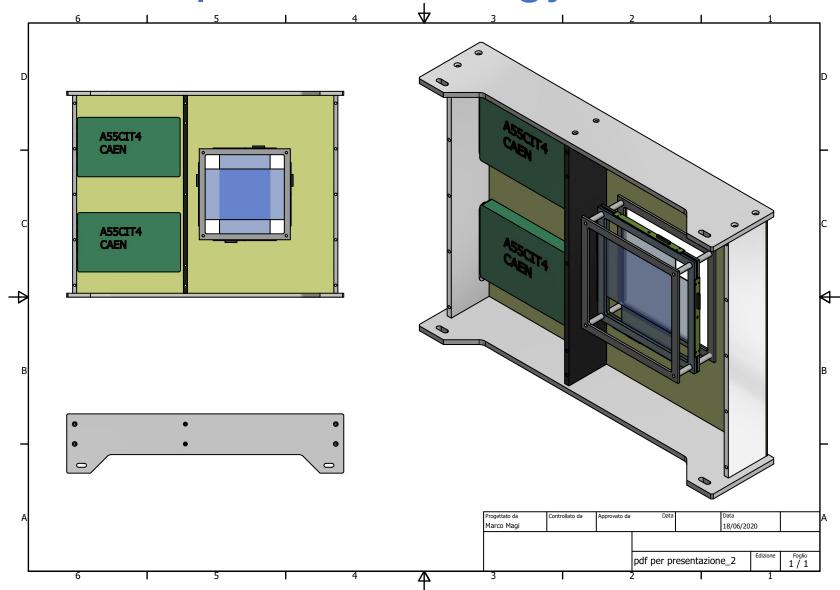
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- Aluminum frame centered with respect to the beam isocenter, holding the two layers of fibers and the SIPMs boards.
- A custom CAEN A55CIT4 / DT5550W is the proposed readout system (still some tests and investigations are necessary) handling128 channels: 2 of such boards can manage the readout of the whole detector, each board reading a BM view.



Bulkhead and beam monitor enter and exit windows for light tightness

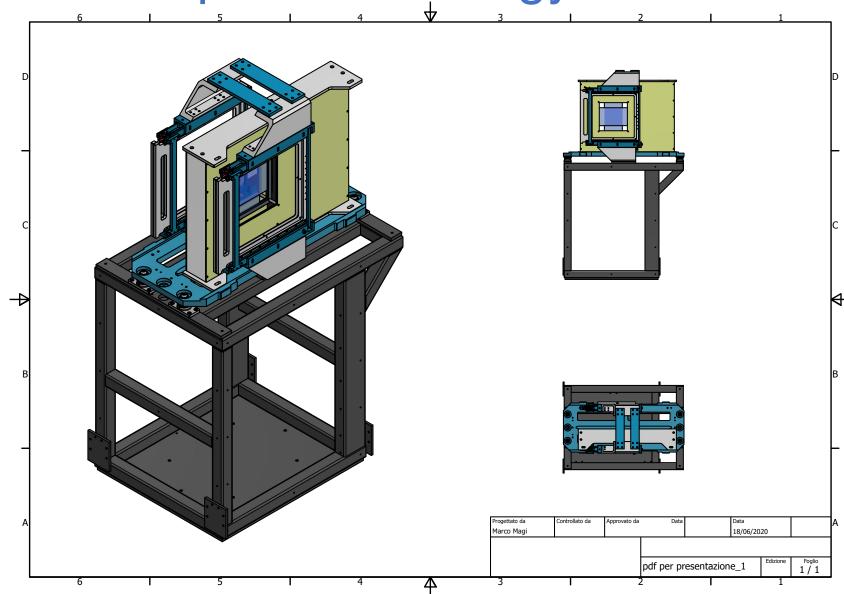
SiPMs boards have to be custom designed

Cable connecting SiPMs boards to the CAEN boards not shown



Further requests:

- in order to take care of aging effects the aluminum frame with the two layer of scintillators fibers have to be replaceable with new layers
- 2. The BM has to be removable from the beam axis for groups that don't need it



Low intensity beam monitor designed to be positioned in the same frame of the CNAO BOXes

With this design the beam monitor overall frame is in the same position of BOXes with beam impinging on the center of the monitor

The readout system

- The readout system is composed of:
 - ➤ A55CIT4 that host four CITIROC 1A WeeROC ASICs, managing 32 channels each, and providing a power supply for SiPM
 - DT5550W motherboard hosting a Xilinx XC7K160T (Kintex-7 family) FPGA
- Two readout modes:
 - Photon counting (providing the ID of the hit channels up to 10 MHz)
 - 2. Analog Charge Integration: measuring released charge in a dynamic range 0-400 pC up to 100 kHz
- The wanted operational mode is Photon Counting: first tests of the board have just started to check the expected characteristics and performances



A55CIT4

DT5550W

Conclusions

- A detector to be used as monitor in the CNAO experimental room for the case of low beam rate intensity is in the planning stage
- The monitor has to be able to measure rates up to 10 MHz, covering an active area of ~ 13 x 13 cm² of scintillating fibers read by SiPM, and to provide feedback about the beam position with an experimental resolution on both views (x,y) of ~1mm
- The adopted technology guarantee a low material budget minimizing fragmentation of the beam in the monitor itself (for carbons), and a dynamic range spanning from protons and carbons provided beam energies at CNAO
- The readout system has been just setup and tests to verify the expected performances are started at SBAI laboratory in Rome La Sapienza