

PID & TDR

TDR process time estimates

- **Finish of R&D tests in CRT:**
 - (a) test of FDIRC optics by the end of 2011 (will use BLAB3 electronics initially).
 - (b) test of final SuperB electronics on FDIRC by the middle of 2012.
- **Resolution of outstanding technical issues for CRT tests:**
 - Finished FBLOCK: Feb. 2011
 - QC of FBLOCK: Feb.-March 2011
 - Gluing tests: Jan.-Feb. 2011
 - N₂ gas flow design around FBLOCK and bar box: March 2011
 - FBLOCK housing installation in the clean room: March 2011
 - Mechanical support for CRT: March-June 2011
 - Laser calibration FDIRC design: March-June 2011
 - final SuperB electronics on FDIRC by the middle of 2012
- **Detailed design and engineering drawings for SuperB:**
 - Details of FBLOCK housing, gas sealing design: Fall 2011
 - FDIRC at SuperB: end of 2011
 - Electronics housing, motherboard, cooling: Fall 2011
 - Background shielding design: end of 2011
 - Method of shipment procedure of bar boxes to Italy: end of 2011
 - Magnetic shielding: end of 2011
 - Laser calibration in the final system: end of 2011

Forward PID

- **Three approaches currently being studied: low cost pixilated TOF counter, FTOF and FARICH**
- **Forward PID panel set up, chaired by H. Jawahery. This committee will review the projects in the coming months and then make recommendations before the collaboration takes a decision.**
- **The inclusion (or not) of the FPID in the TDR will depend on the outcome of this review.**