

SuperB Computing R&D Workshop

Wednesday, 10 March 2010

Plenary Session: Impact of new CPU architectures (II) (08:30 - 10:30)

time	[id] title	presenter
08:30	[10] Coding for the GPU: highlights and application examples	BOSI, Leone Battista
09:00	[11] Exploiting concurrency to speed-up ROOT data analysis: caching, PROOF and multi-threading	GERRY GANIS
09:30	[57] Multi-level Parallel fit algorithms using MPI and CUDA	TOMKO, Karen
09:50	[12] Discussion	