

A tile prototype of the Plastic Scintillator Detector for HERD based on long Printed Circuit Board: design and test with ion beams at CNAO

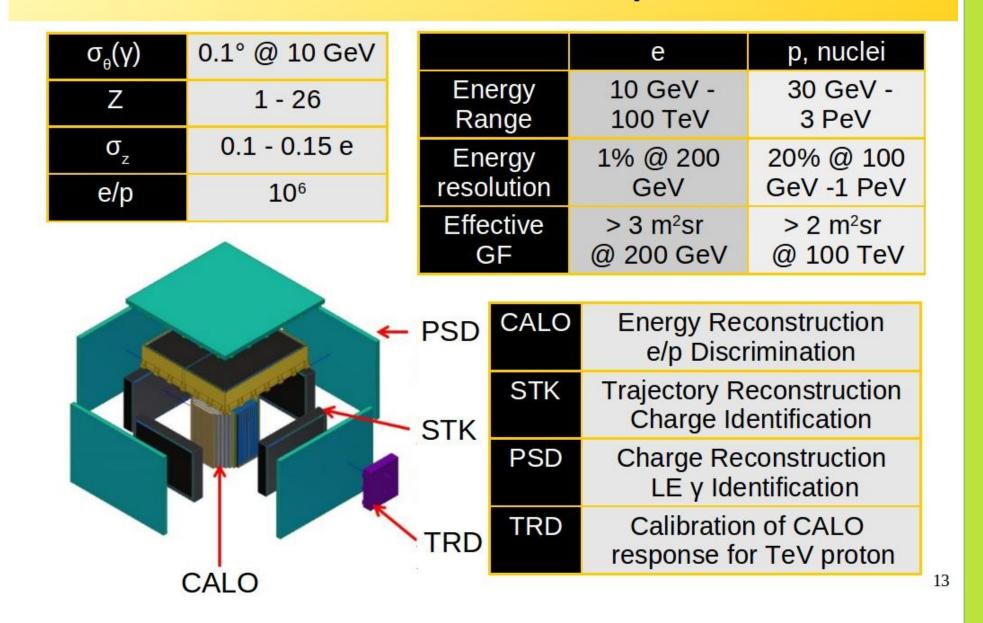
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The Herd detector ------

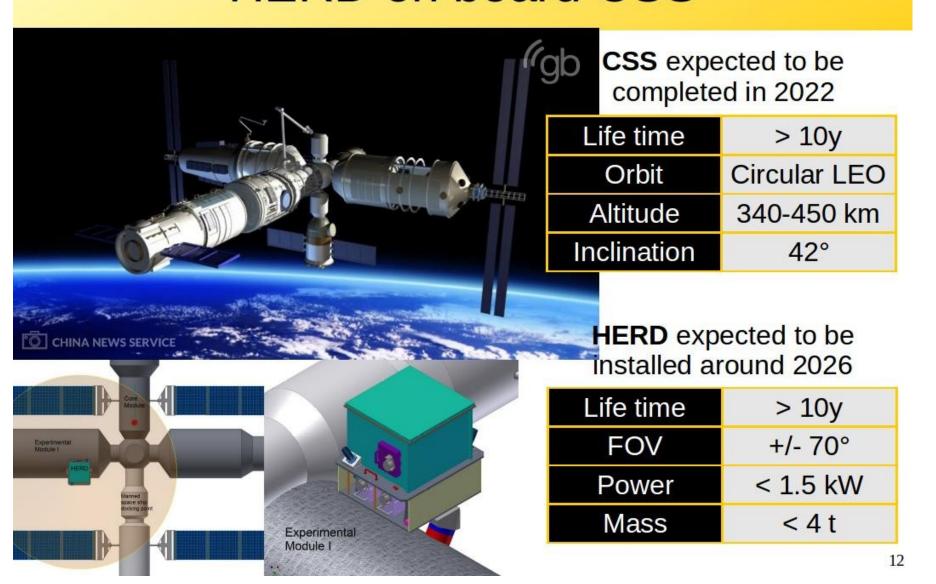
HERD detector and requirements



HERD will be located on the CSS.

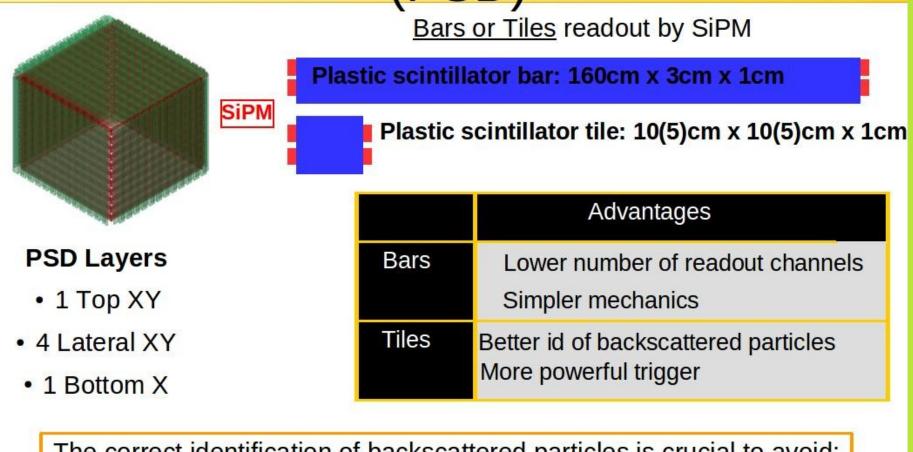
----- The Chinese Space Station -----

HERD on board CSS



-----The Plastic Scintillator Detector -----

HERD Plastic Scintillator Detector (PSD)



The correct identification of backscattered particles is crucial to avoid:

• charge misreconstruction in case of incident charged particles

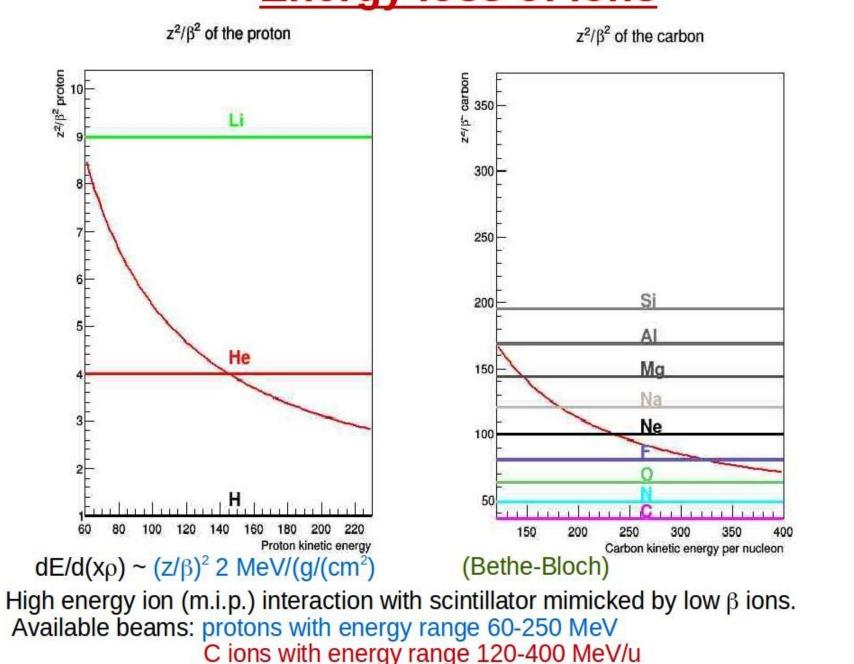
• self veto in case of incident y

CNAO facility for beam test



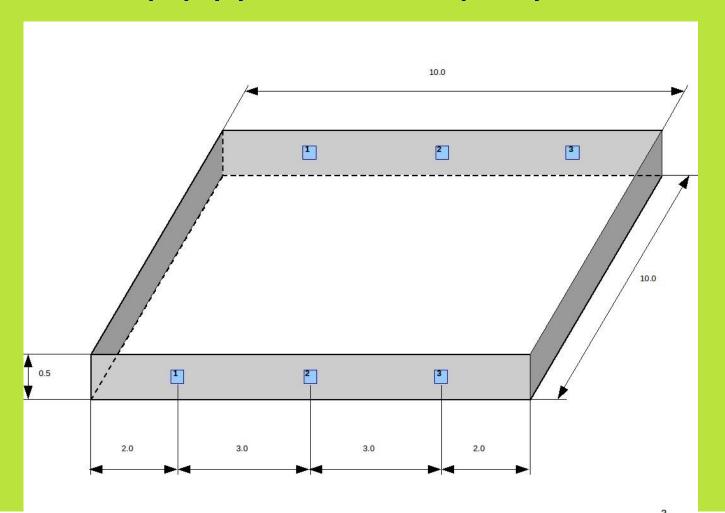
CNAO provides low energy ion beams (p,C) Energy loss of low β ions (p,C) compared to high-Z high energy ions

Energy loss of ions

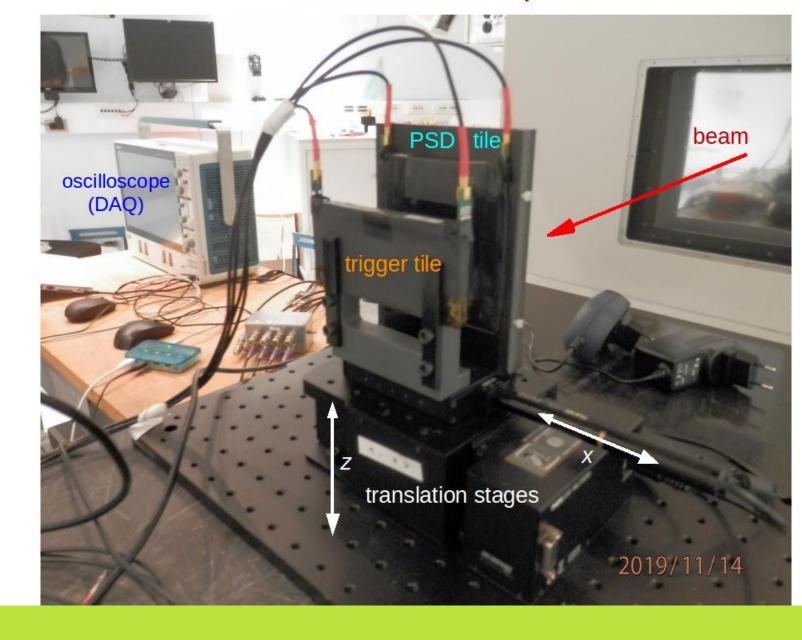


----- Beam Test 2019-2020 -----

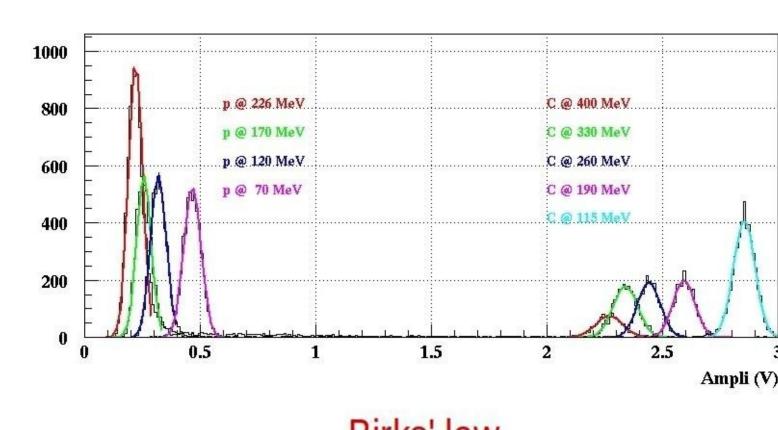
Test scintillator tile (EJ200) 10 cmx10cmx0.5cm Read out by 3+3 Hamamatsu S12572 50 μ m SiPM (3x3 mm²) (opposite sides) in parallel

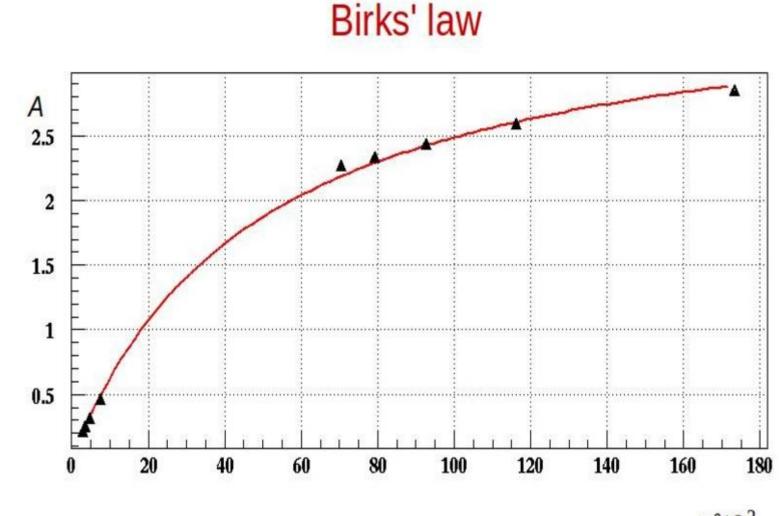


Beam test setup



Signal amplitude analysis





The correlation between the signal amplitude and the dE/dx (Z^2/β^2) is well fitted with a Birks' law

$$A = P_1 \frac{dE/dx}{1 + P_2 dE/dx}$$

 $P_1 = 0.0756$ $P_2 = 0.0204$

The 2019-2020 results proved the concept of using low energy (p,C) ions to provide energy losses as large as high-Z high energy ions. Saturation in light production following Birks' law was confirmed and its parameters measured.

Beam Test 2021: the long PCB

A new design of the tile is required to satisfy the requirements:

- full hermeticity (no dead space between tiles)
- mechanical support for the tile structure
- uniformity of response on tile surface
- two readout channels per tile with low-high gains to match the broad dynamic range in energy loss from proton to iron and beyond

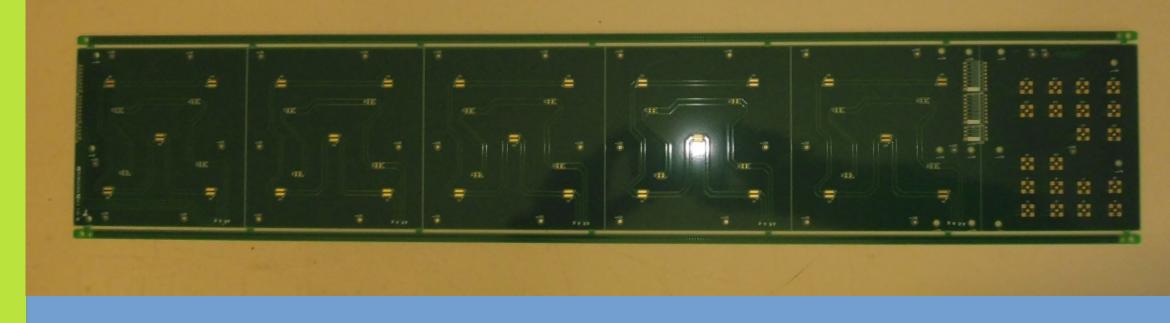
Possible solution: long Printed Circuit Board (PCB) 50 cm long (to be extended to 100 cm) hosting 5 tile (10) 10x10x0.5 cm³ each. Two sets of SiPMs located on the wide face to avoid dead space between tiles.

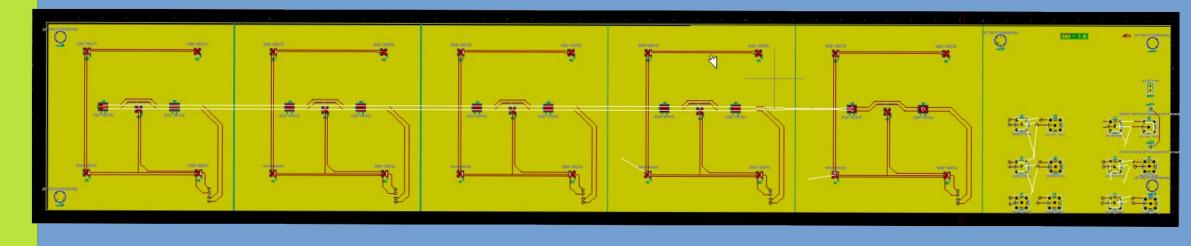
One set, 3x3 mm² with large gain, the other 1.3x1.3 mm² with low gain.
SiPM are located to guarantee a good light collection uniformity for both sets.

The 50 cm long PCB

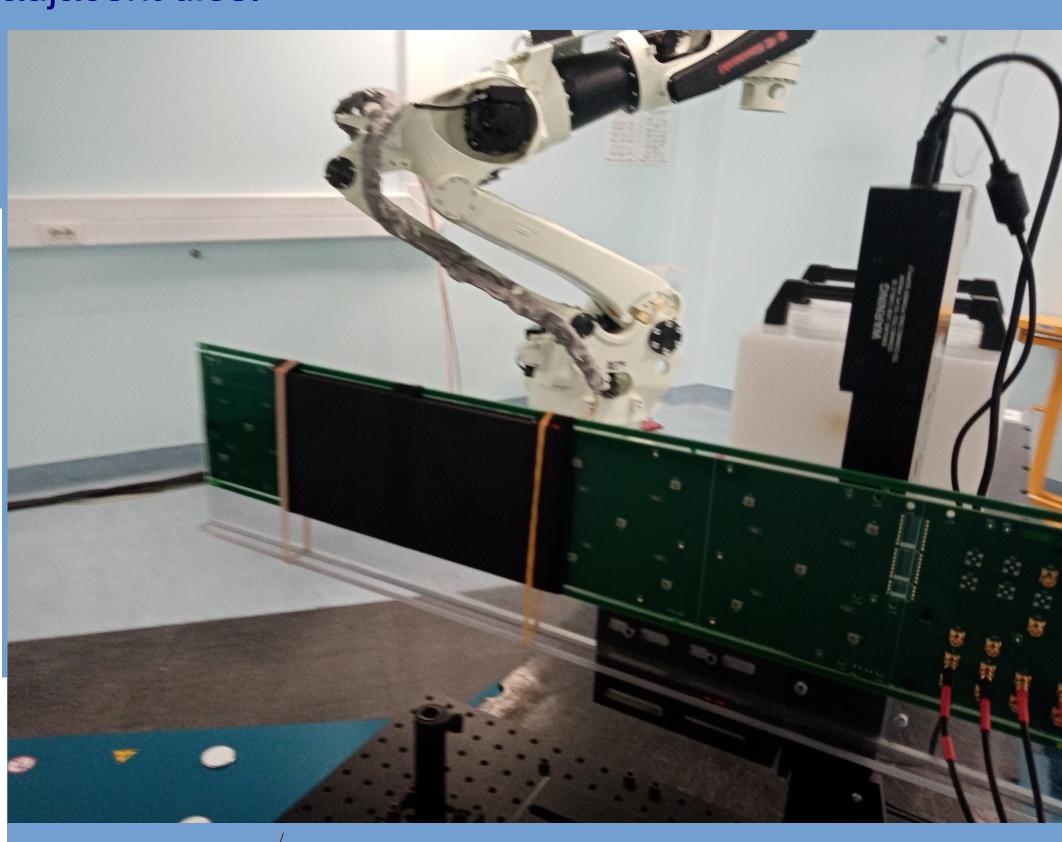
The PCB 50 cm long is designed to house 5 tile 10x10x0.5 cm³ each read by 5 SiPM 3x3 mm² connected in parallel (high gain) and by 4 1.3x1.3 mm² connected in parallel (low gain). Signals are routed to the connectors (left side below).

Two of these PCB can be ganged together to form a 1 m long PCB with 10 tiles.





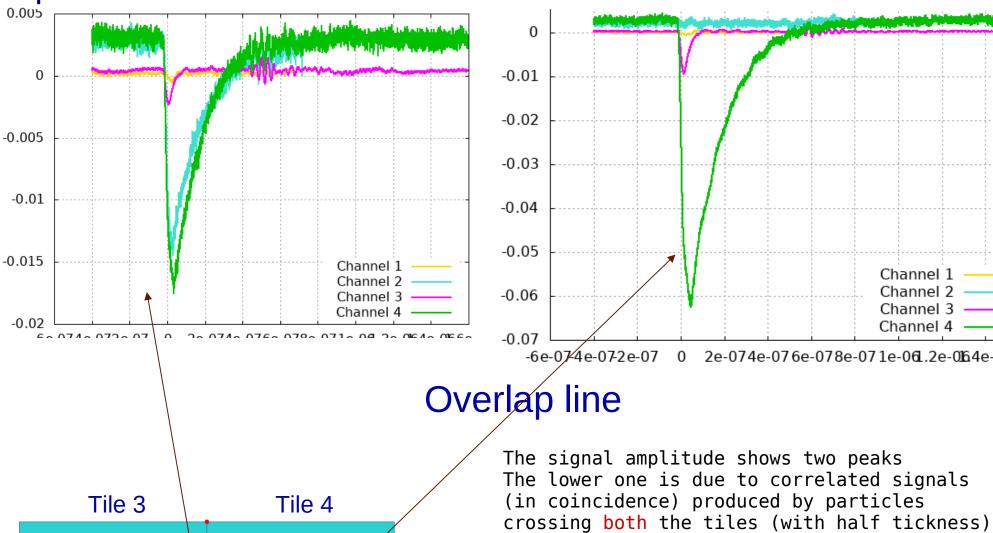
We put under test at CNAO a long PCB with 2 adjacent tiles.

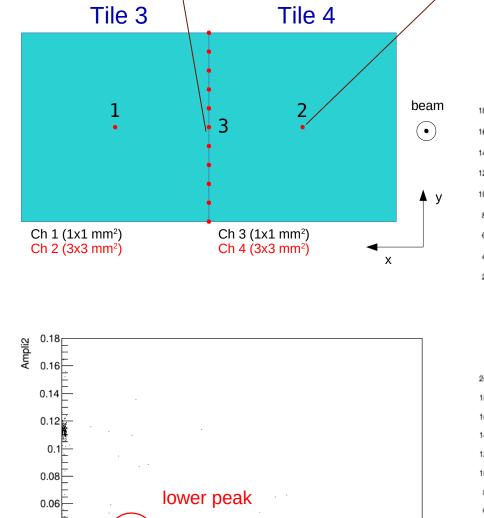


PCB edges are shaped as step to enhance hermeticity.

Some results

Pulse shapes of low-high gains channels for C beam in different position.





lower peak

Ch2 (tile 3)

Beam in pos 3

Ch2 (V)

lower peak

Ch4 (tile 4)

Beam in pos 3

Ch4 (tile 4)

Beam in pos 3

0.18 0. Ampli4 Time

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Prototypes of the HERD PSD have been tested with ion beams at CNAO. Saturation effects in scintillator are measured. The principle of long PCB read out is proved.

Conclusions

Amplitude measurements in low-high gain channels correspond to the expectations. The time resolution is adequate for the requirements of the experiment.