#### A 32-channels mixed-signal processor for the tracking system of the GAPS experiment

Valerio Re, Luca Ghislotti, Paolo Lazzaroni, Massimo Manghisoni, Elisa Riceputi

University of Bergamo and INFN Pavia

Lodovico Ratti

University of Pavia and INFN Pavia

Mirko Boezio, Gianluigi Zampa

**INFN** Trieste

Lorenzo Fabris

Oak Ridge National Laboratory

### The GAPS experiment

The General Antiparticle Spectrometer (GAPS) is an Antarctic balloon experiment designed to detect low-energy cosmic antinuclei as an indirect signature of dark matter



#### GAPS will deliver:

- a precision antiproton measurement in an unexplored energy range
  < 0.25 GeV/n</li>
- antideuteron sensitivity 2 orders of magnitude below the current best limits, probing a variety of DM models across a wide mass range
- leading sensitivity to low-energy cosmic antihelium nuclei

#### GAPS is under construction, preparing for first Antarctic Long Duration Balloon flight

The GAPS instrument consists of a large-area ( $\sim 50 \text{ m}^2$ ) scintillator time-of-flight system, and of a 10-planes tracker with custom Si(Li) detectors with dedicated ASIC readout

## The GAPS detection principle

- ToF measures energy deposition, velocity, timing of incoming low-energy cosmic antinuclei
- Antinuclei slow down in the Si(Li) tracker material, until captured by the target material forming an exotic atom, with the antinucleus orbiting the nucleus of target material.
- Exotic atoms de-excite emitting X-rays with characteristic energies that depend on the reduced mass of the nucleusantinucleus system. Finally, the antinucleus annihilates with the target material, producing pions and protons.



- Characteristic X-rays (20-100 keV) are detected by the Si(Li) detectors. Tracks from the charged annihilation products can be reconstructed using both the tracker and ToF, providing measurements of energy depositions and velocities.
- The GAPS detector uniquely identifies an antinucleus species while providing rejection power against protons and other non-antimatter cosmic-ray particles, which produce neither characteristic X-rays nor hadronic annihilation products.

## The GAPS Si(Li) tracker

The Si(Li) Tracker, with active regions that can be made both large in area and deep in thickness, functions as

- target to slow an incoming antiparticle and capture it into an exotic atom in an excited state
- spectrometer for de-excitation X-rays (~4 keV (FWHM) energy resolution to distinguish X-rays from antiproton or antideuteron exotic atoms)
- tracker to measure antinucleus dE/dx and stopping depth, and annihilation products from nuclear decay
- Large area Si(Li) detectors developed by Columbia, MIT, ISAS/JAXA, produced by Shimadzu Corp.
- ~10 cm circular detectors, segmented in 8 strips with equal area and 2.5 mm thick
- module: 2x2 detectors, 6x6 module array in each plane, 10 planes vertically spaced by 10 cm
- Operation at relatively high temperature of - 35 °C to - 45 °C
- Read out by a custom-designed front-end chip



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## The GAPS Tracker readout chip

 $3.14 \,\mathrm{mm}$ 

#### **Front-end electronics requirements**

- Channels per ASIC: 32
- Nominal operating temperature: -43 °C
- Power dissipation: ≤ 10 mW/ch
- Signal polarity: electrons
- Dynamic range: 10 keV-100 MeV
- Analog Resolution: 4 keV (FWHM) at detector capacitance 40 pF
- Threshold: 20 keV
- Detector leakage current: 5 nA
- Event rate: 100 Hz

#### **ASIC** features

- 32 analog readout channels
- 11-bits ADC
- Digital Back End (registers control, SPI, ...)
- BGR with 3-bit DAC regulation
- 8-bit DAC for global threshold setting
- 3-bit DAC for threshold fine trimming
- Detector leakage current readout
- Temperature sensor readout



 $3.14\,\mathrm{mm}$ 

#### Developing the GAPS Tracker readout chip

#### SLIDER: SiLI DEtector Readout



#### SLIDER4 (2018)

- 4 analog channels
- No digital back end
- 2 channels with analog output

#### **SLIDER8** (2018)

- 8 analog channels
- digital back end
- 11 bit ADC
- $\cdot\,$  No access to analog blocks



#### pSLIDER32 (2019)

- 32 analog channels
- digital back end
- 11 bit ADC
- 2 channels with access to analog blocks



#### **SLIDER32** (2021)

- 32 analog channels
- Digital back end
- 11 bit ADC
- 1 channel with analog outputs
- Additional tests points

## The analog readout channel



180 nm CMOS technology, 7.2 mW total power dissipation

**Charge sensitive amplifier** 

with dynamic signal compression CR-(RC)<sup>2</sup> filter

Vini

- with 8 selectable peaking times (from 300 ns to 1.8 μs)
- **SOT comparator** Signal Over Threshold identification
- Active CR and ZC comparator Shaper signal peak detection
- Single-ended to differential S&H Shaper signal peak storage
- Injection capacitance C<sub>inj</sub> Calibration
- **11-bit hybrid SAR ADC** One per ASIC with 32:1 MUX

## The charge-sensitive preamplifier



Normalized Filter Gain (dB)

## Preamplifier charge restoration network



- Continuous-time Krummenacher feedback for sensor leakage current compensation (up to 50 nA for testing at temperatures higher than nominal), including a leakage current monitor
- Non-linear feedback capacitance for a dynamic compression of detector signals

## Dynamic signal compression

It is based on the nonlinear behavior of a MOSFET capacitor operating in the inversion mode, with a suitable choice of W and L to set the gain in the low and high energy regime and dynamically change the gain of the preamplifier with the input signal amplitude

Drain and source shorted to form one capacitor terminal, the gate forms the other



A voltage shift (in the Krummenacher feedback) to the gate-to-source/drain terminals of the feedback MOS enables a precise variation of the voltage at which the kink in the compression occurs

## Dynamic signal compression

The analog channel features a dynamic compression to fit the wide dynamic range (10 keV - 100 MeV) into the input range of the 11-bit ADC, while measure X-ray energies with 1 keV resolution in high-gain region



At the preamplifier output: kink at output voltage 200 mV, corresponding to an input particle energy of about 1 MeV (44 fC).

### Preamplifier charge sensitivity:

High gain region: 250 µV/keV (5.7 mV/fC), equivalent feedback capacitance 175 fF

Low gain region:  $3.0 \mu V/keV$ ( $68 \mu V/fC$ ), equivalent feedback capacitance 14.7 pF (compression factor of about 80)

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## Dynamic signal compression and return to baseline at the preamplifier output



nonlinear discharge of the nonlinear integrating MOS capacitance

Slow discharge region: large output levels, feedback capacitance is large

Fast discharge region: small output levels, feedback capacitance is small

Undershoot at the end of the restoration phase: complete unbalance of the Krummenacher stage for large voltage steps

complete recovery in < 10 ms (compatible with the low event rate per strip, < 1 count per second including background)

## Shaper



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## Differential Sample&Hold

Provides a differential output signal suitable for the subsequent ADC, ranging from -1.8 V to +1.8 V with an almost rail-to-rail output dynamic range.



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#### ADC



#### Tests using the temperature sensor input

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#### Noise

Noise is measured at the output of the ADC (ENC extracted from the variance of the distribution of output values)



## Threshold setting



- 3-bit DAC in each channel for threshold fine trimming
- Threshold dispersion reduced by a factor of 2

#### After fine trimming :

- 0.4% Hot Channels for 15 keV Threshold
- 0.1% Hot Channels for 18 keV Threshold

#### Conclusions

- The final version of the readout chip for the GAPS Si(Li) tracker is fully functional according to the detector specifications
- The analog front-end implements a dynamic signal compression technique to handle a wide dynamic range while keeping a high resolution at low signal charges
- The chip production in 2021 was successful, and front-end boards with ASICs are currently being tested and selected for the flight instrument
- The integration of the GAPS instrument is beginning now, preparing for the first flight of a series of long-duration balloon flights at high altitude in Antarctica

#### The GAPS collaboration



### Backup slides

# Peaking time and noise optimization (from simulations)



## Mitigation of temperature effects on input-output characteristics

The input-output characteristic is affected by temperature variations (effect on transistor threshold voltages). The effect has been strongly mitigated by generating  $V_{ref}$  in a way which follows the temperature variations



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#### Error due to temperature variations around - 40 °C



ADC nominal resolution: 1 keV

ADC nominal resolution: 80 keV

### Front-end board

#### **Front-end Board**

- One ASIC connected to 4 Si-Li detectors
- voltage regulators and filtering for
  - Si(Li) detector High Voltage Power Supply
  - ASIC Low Voltage Power Supplies (AVDD, DVDD) 6 Layers
- ASIC SPI control signals
- ADC clock
- Temperature sensor
- ASIC calibration system (16 bit DAC)

#### **Flex-rigid Board**

- Connects Front-end boards in series
- Propagates

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- ASIC Low Voltage Power Supplies
- SPI control signals and ADC clock



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