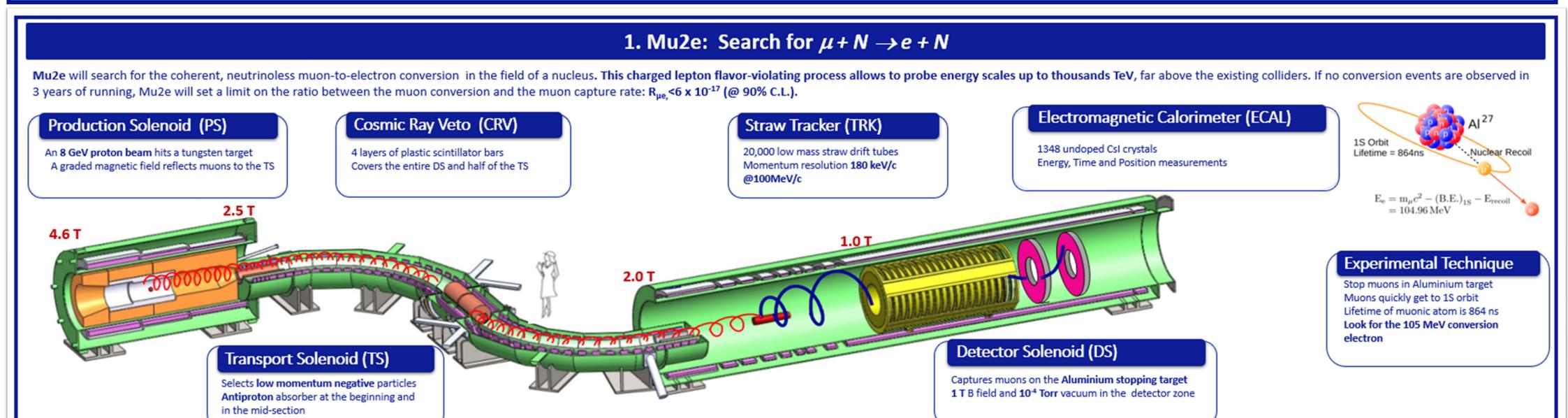


Mu2e calorimeter readout electronics

F. Spinella for the Mu2e Calorimeter group



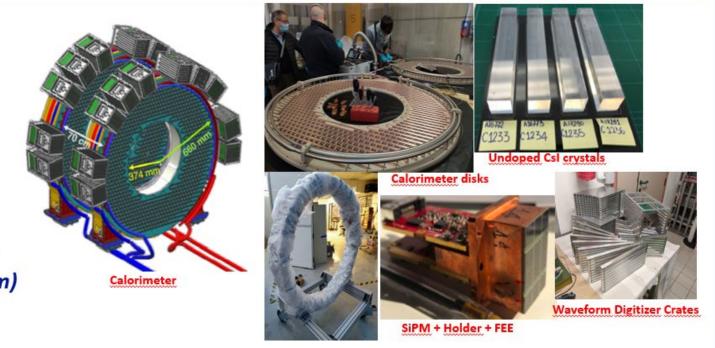




2. The Electromagnetic Calorimeter

Calorimeter Provides:

- \triangleright Particle identification u/e
- Seed for track pattern recognition
- Independent trigger
- \Rightarrow $\triangle E/E < 10\%$ and $\triangle t < 500$ ps ⇒ Position resolution of O(1 cm)



- → High granularity → made of 1348 undoped Csl crystals (3.4x3.4x20 cm³)
- > Crystals arranged in two disks (inner/outer radius 37.4 cm / 66 cm, separation between disks 75 cm)
- ➤ 1 crystal coupled to 2 large (14x20 mm²) area UV-extended SiPM → 2696 electronic channels
- > SiPM packed in a parallel arrangement of 2 groups of 3 cells biased in series
- > DAQ crates located **inside the cryostat** to limit the number of pass-through connectors
- ➤ 10 crates/disk with 6/8 boards / crate

3. Why a digitizer? What requirements?

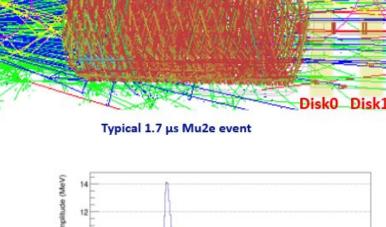
Requirements:

- Very intense particle flux expected in the calorimeter → High Sampling Rate digitizer crucial to resolve pile-up
- Sample SiPM signal at the frequency of 200 Msamples with 12 bits ADC

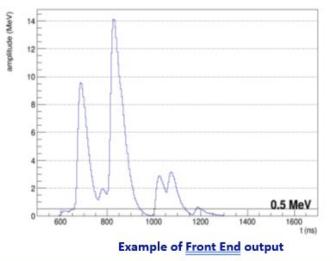


- Magnetic field of 1 T and 10⁻⁴ Torr vacuum
- Total Ionizing Dose (TID):
 - > 0.5 krad/yr X 12 SF X 5 years
 - > TID requirements of 30 krad
- Neutron flux 5x10¹¹ 1 MeV (Si)/yr (from simulation) Mechanical constraints:
- - ➤ Limited space → 20 ADC channels/board
 - ➤ Limited access for maintenance → Highly Reliable Design mandatory

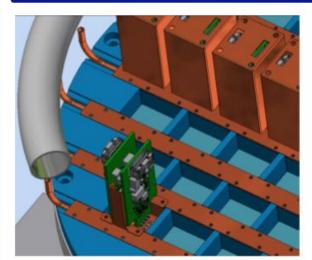
6 LDO



calorimeter

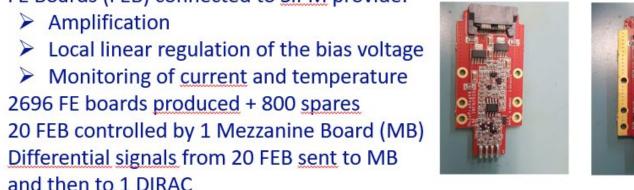


4. Front End Electronics and readout flow



- Differential signals from 20 FEB sent to MB and then to 1 DIRAC
- DIRAC → sampling, processing and transmission to the Mu2e DAQ

FE Boards (FEB) connected to <u>SiPM</u> provide:







DDR ADC **ADC FPGA** ADC litter Cleaner 3 DC/DC ADC

After an intense campaign of tests:

- ADC: ADS4229 (Texas Instruments®)
- FPGA: Polarfire MPF300 (Microsemi®) DC-DC: LMZM33606

5. Digitizer architecture and design

- LDO: MIC69502 (Micrel®)
- Jitter Cleaner: LMK04828 (Texas Instruments®)
- Optical Transceiver: CERN VTRX

DIRAC PCB specs:

- Material: FR408-HR
- Layers: 16
- Dimensions: 233x165 mm
- Thickness: 2.127 mm
- **Differential lines**: 100Ω
- Single ended lines: 50 Ω

FE requirements:

- Magnetic field of 1 T and 10⁻⁴ Torr vacuum Total Ionizing Dose (TID):
 - 1,8 krad/yr x 12 SF x 5 years
 - TID requirement of 100 krad
- Neutron flux 5x10¹¹ 1 MeV (Si)





6. Digitizer architecture and design Packaging VTRX Developed by TDAQ group

7. DIRAC Qualification Campaign γ from Bremsstrahlung (0<E<14MeV) Co60 source Estimated dose ≈ 20 krad/h @ 600µA Dose in function of distance: Max 2krad/h, requested 1krad/h Total neutron flux of 1.2 x10^12 n 1 MeV (Si) / cm^2 1 T magnetic field Total neutron flux of 6x10^11 n 1 MeV (Si) / cm^2 Different orientations

TDAQ sends an Heartbeat packet that contains EVENT TAG and **EVENT WINDOWS** DIRAC builds the calo hit applaying a zero suppression and pre-

Data stored in DDR

processing data

Tdaq sends a specific EVENT TAG, and DIRAC retrieve requested Data Packet from DDR and sends it out to DTC

8. Vertical Slice test



•Large scale EMC prototype:

- > 51 Csl crystals
- ➤ 102 Mu2e SiPMs
- > 102 FEE boards
- > 1 DIRAC board handles 20 channels
- •Mechanics and cooling system are similar to the final ones
- Energy distribution deposited by cosmic rays
- $\sigma_{\rm T} \sim 390 \, \rm ps^{-1}$
- 1. Comparison with commercial digitizer showed no differences in performances
- 2. Obtained time resolution in accordance with expectations
- 3. Noise level and dynamic scale as expected