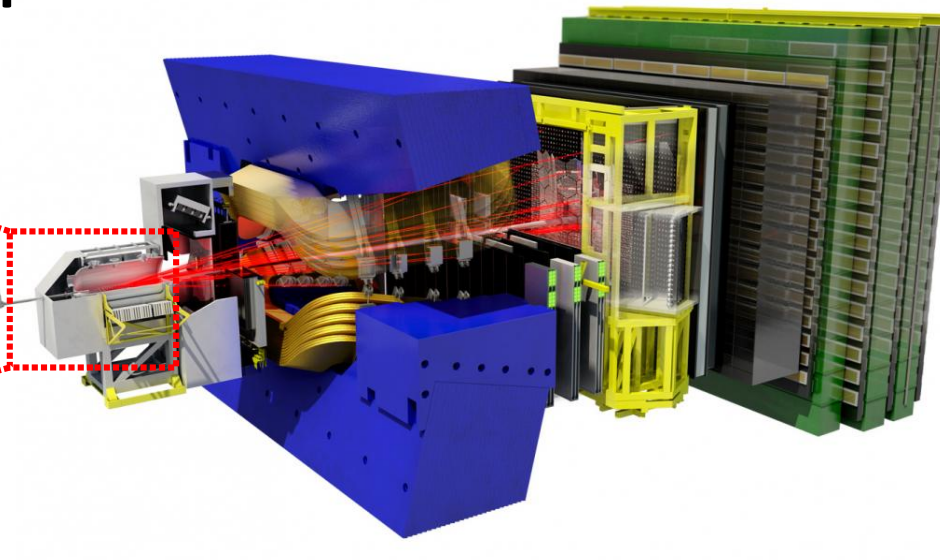
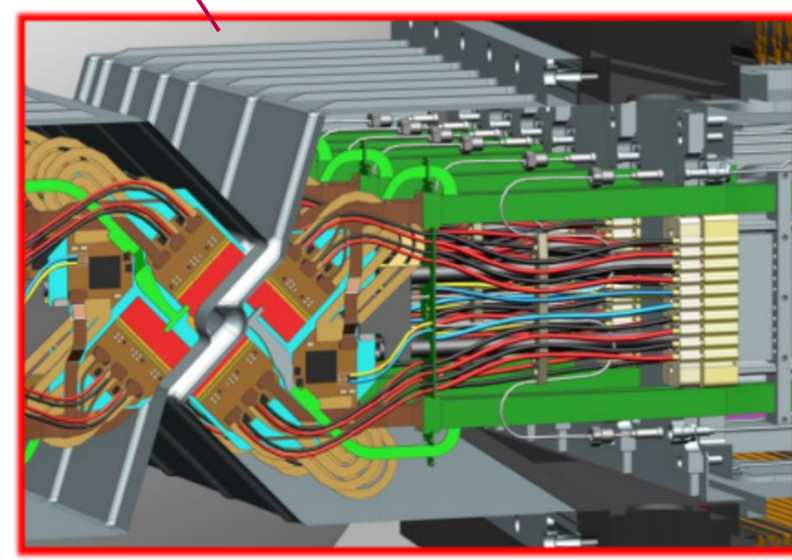


- RF box enclosure
- Single aluminium alloy part
 - Separates beam and module vacua

LHCb Detector

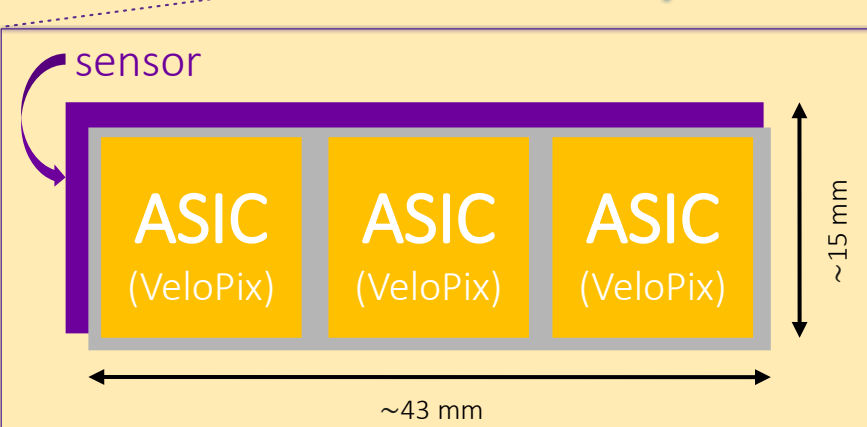
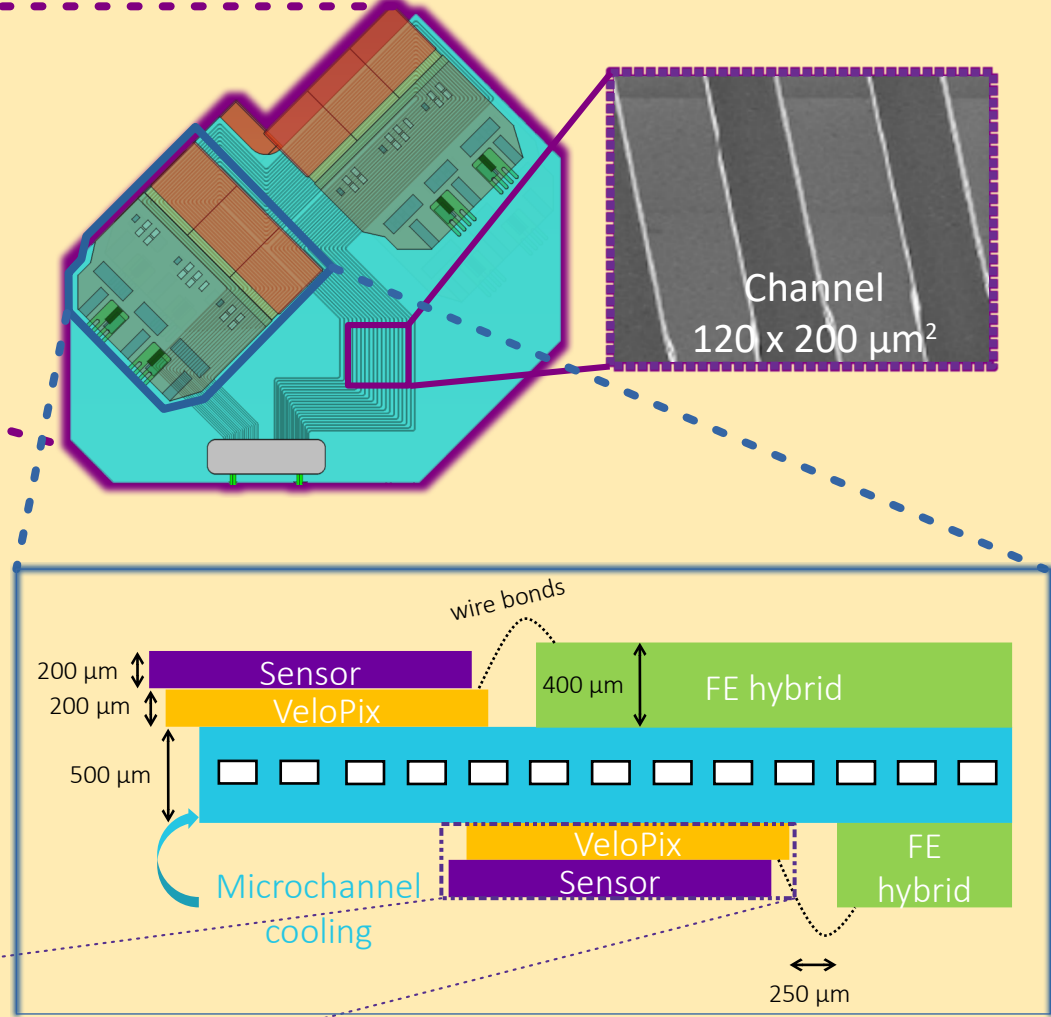
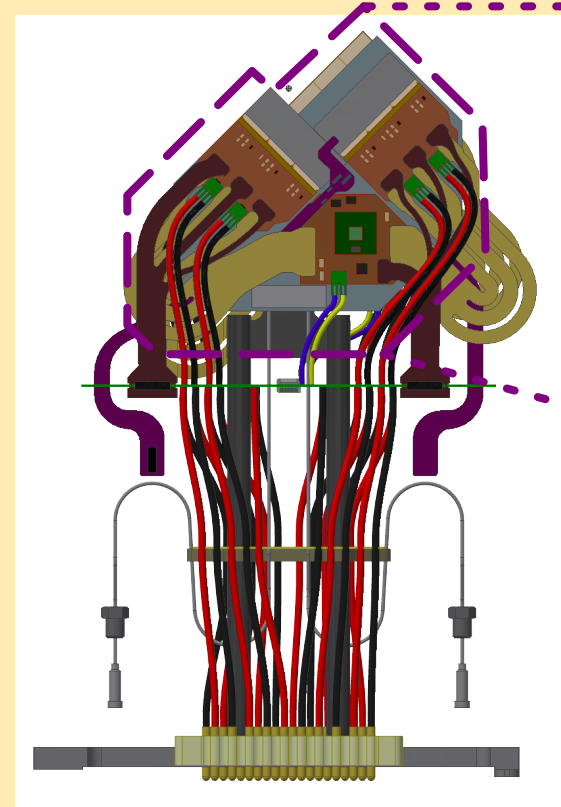
VELO upgrade [2]:

- 52 double-sided modules divided in 2 retractable halves
- 5.1 mm close to the beam when closed
- 4 Tiles (pixel sensor + ASICs) per module
 - 3 ASICs (VeloPix) per Tile
 - 256x256 Pixel per ASIC, $55 \times 55 \mu\text{m}^2$ pixels



- Single arm forward spectrometer [1]
- Focus on *b* and *c* hadron physics
- Reliance on secondary vertices reconstruction
 - VERtEX LOcator (VELO) plays a crucial role

Microchannel substrate



Electronics sandwich

- Tile: sensor + 3 bump bonded ASICs
- Tile & Front End (FE) hybrids glued to microchannel substrate
 - Heat dissipation through glue interface
 - ASICs wire bonded to FE hybrids

VELO Upgrade:

- Higher precision
- Faster readout
- Closer to beam
- Higher radiation hardness

Microchannel silicon substrate:

- Provides structural support & cooling
 - 19 microchannel etched in the substrate
 - 140/240 μm from the chips
 - Uniform heat dissipation via evaporative CO_2 [3]
 - Dissipate up to 30W per module
 - Low budget material

Assembly Steps:

- Assembly @ Manchester & Nikhef
- Bare module
- Tile glueing
- Hybrid Gluing
- Tapes & cables

Metrology:

- Performed after each assembly step
- Assign grading

Validate modules:

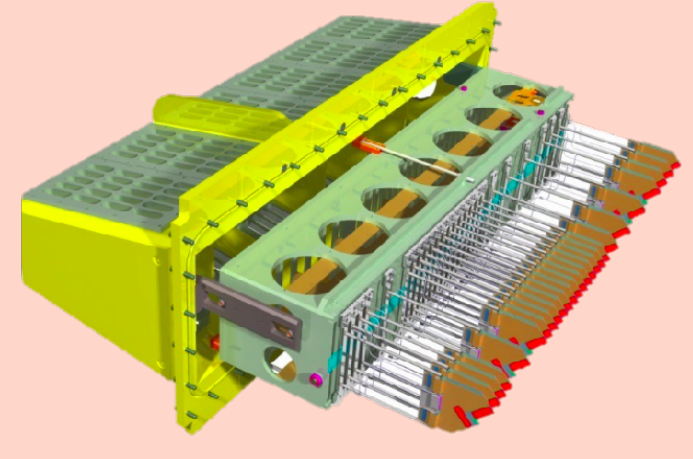
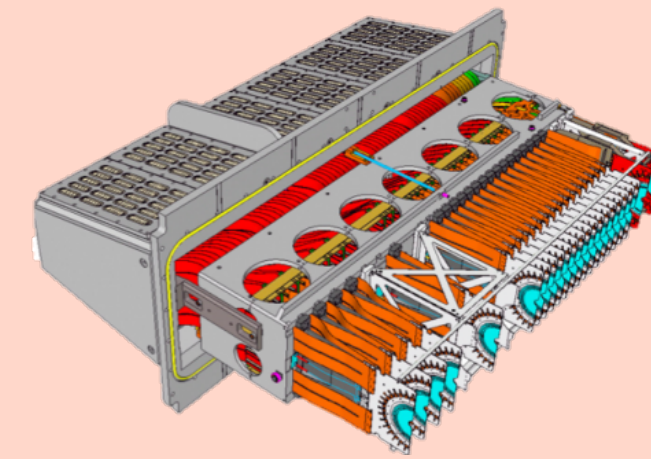
- Apply vacuum @ -30°C
- Simulate operation conditions
- Test electrical performance
- Test thermal performance

Assembly Campaign

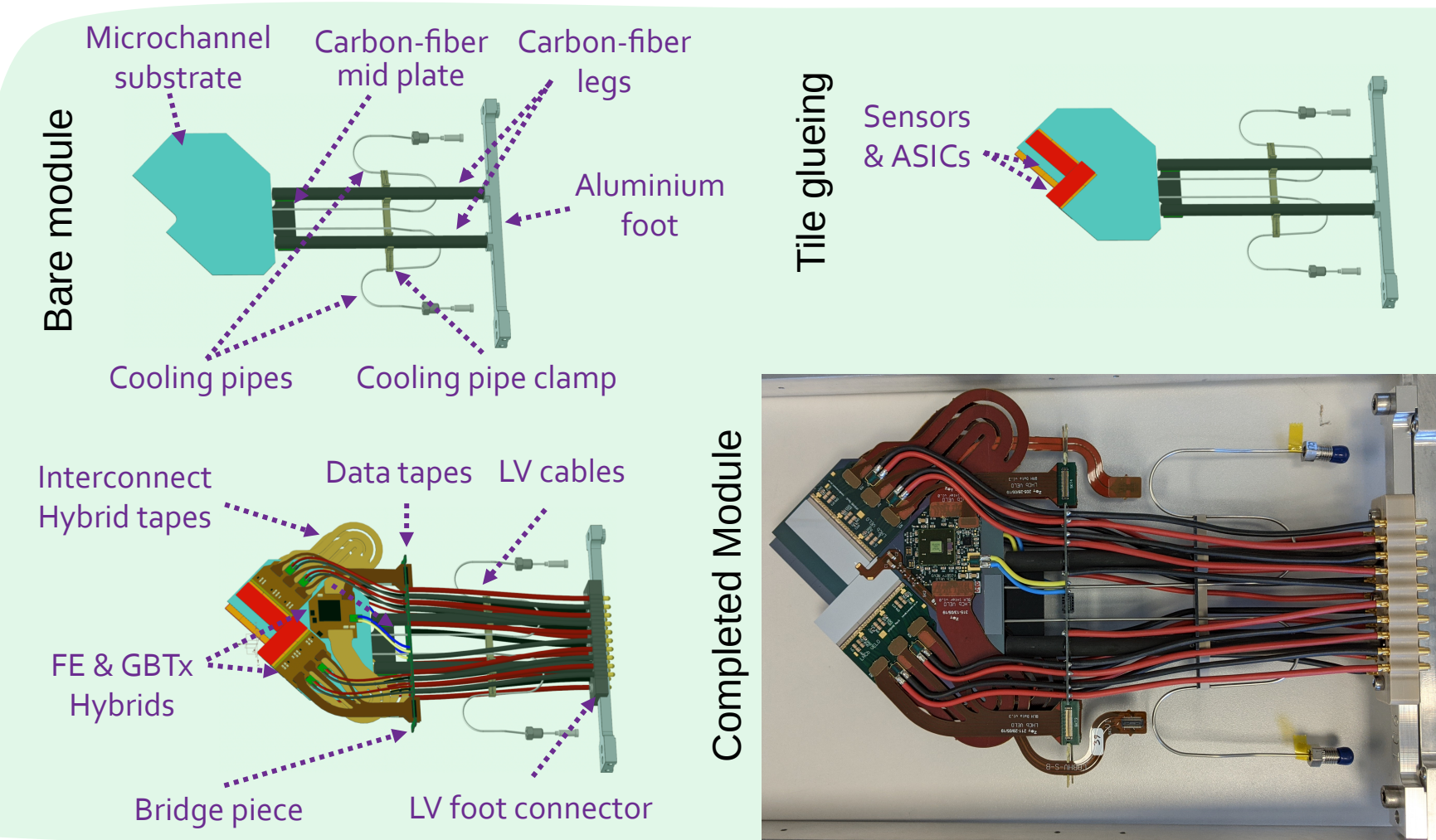
VELO Assembly @ Liverpool

Full Detector Commissioning @ CERN

OLD vs NEW



Feature	VELO	VELO Upgrade
Readout channels	strips, $\sim 0.2 \text{ M}$	pixels, $\sim 41 \text{ M}$
Readout rate	1 MHz, triggered	40 MHz, data-driven
Beam distance	8.2 mm	5.1 mm
Fluence	$4 \times 10^{14} \text{ 1 MeV neq cm}^{-2}$	$8 \times 10^{15} \text{ 1 MeV neq cm}^{-2}$



Glue interface crucial for heat dissipation & mechanical soundness



Choice: Stycast 2850FT with catalyst 23LV

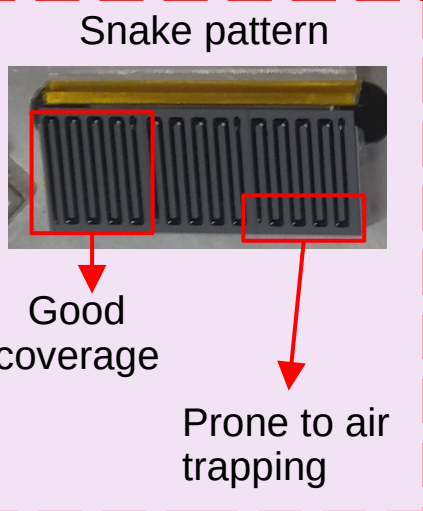
- Good thermal conductivity: 1.1 W/mK
- Radiation hard, good ageing properties
- Cons: hygroscopic catalyst \rightarrow Heat treatment to remove humidity

Pattern R&D

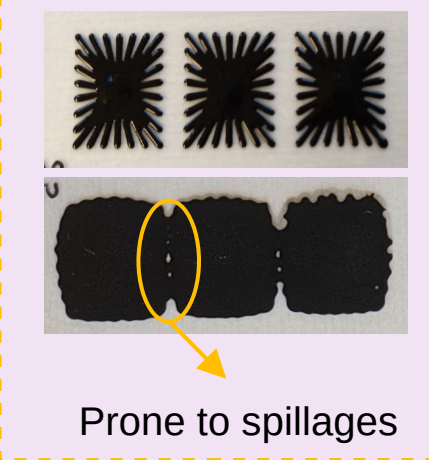
Glue Target parameters

- Thickness: 80 μm \rightarrow good heat transfer and thermal stress absorption
- Evenness: $\pm 40 \mu\text{m}$ \rightarrow uniform glue layer == uniform heat dissipation
- Coverage: 70% of the ASIC
- No air bubbles trapping when pressing down (air expansion in vacuum+heat)
- Avoid spillage in-between ASICs \rightarrow cause electronic noise

First pattern



Snake pattern

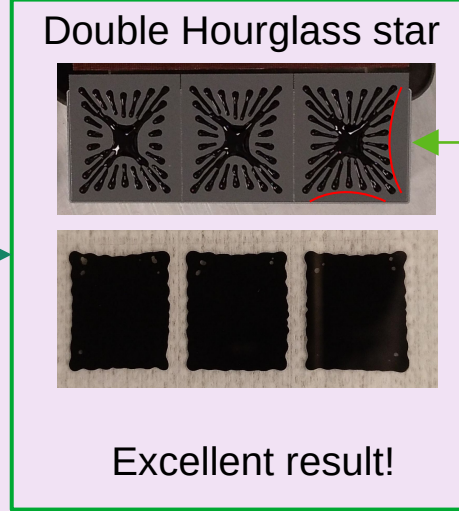


Good coverage

Prone to air trapping

Prone to spillages

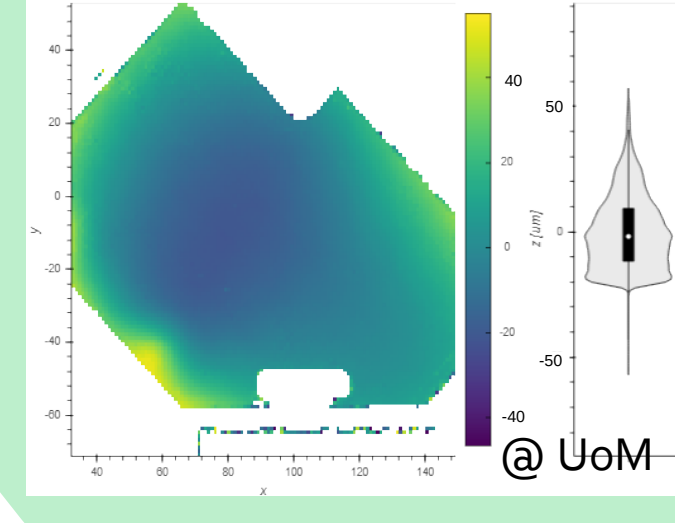
Optimised pattern



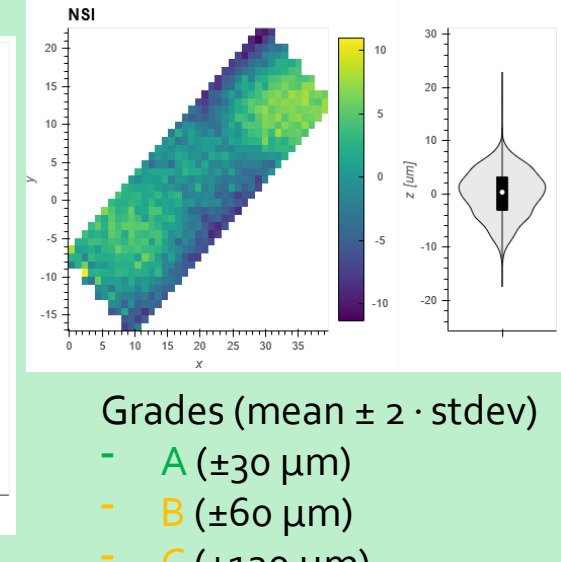
Excellent result!

Add bowing on both sides

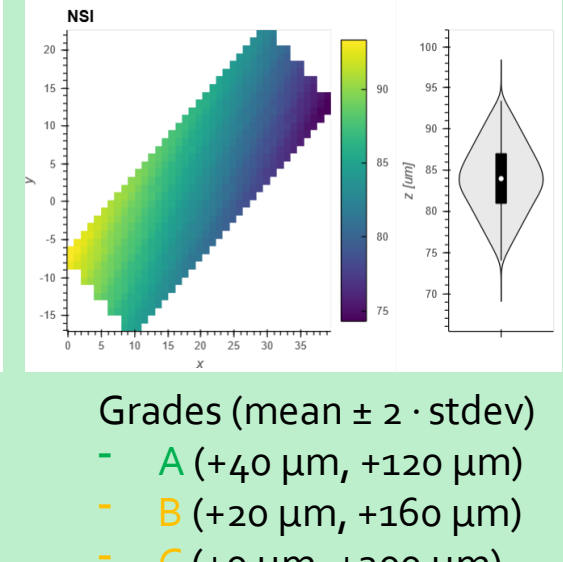
Substrate Flatness



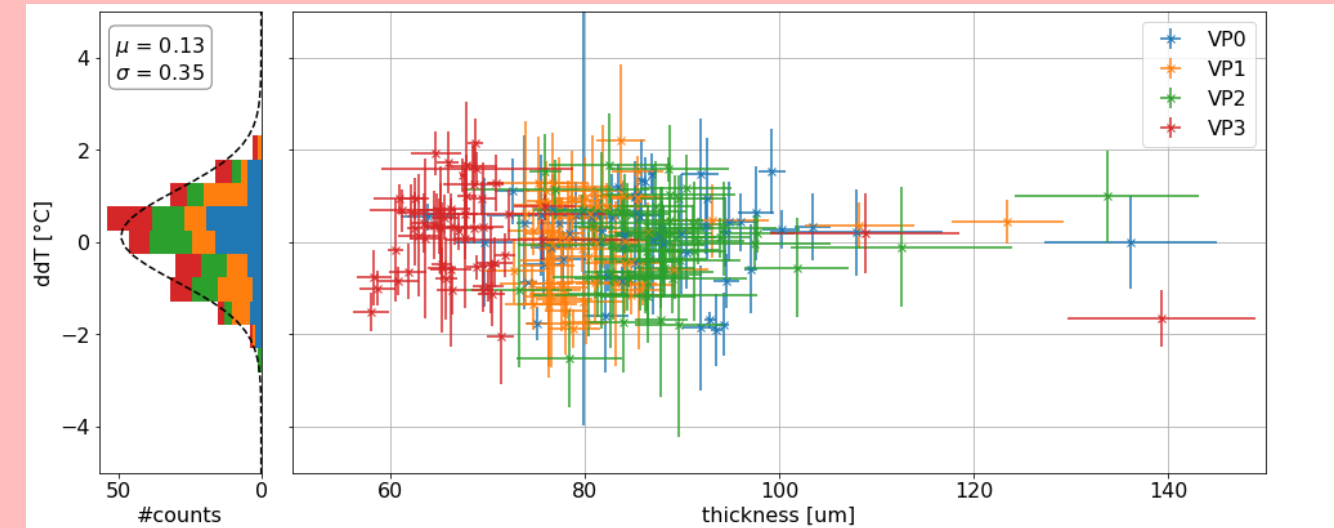
Tile Flatness



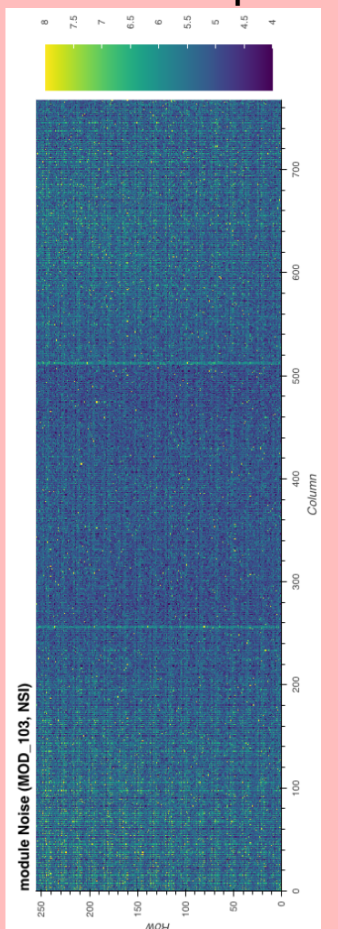
Glue Thickness



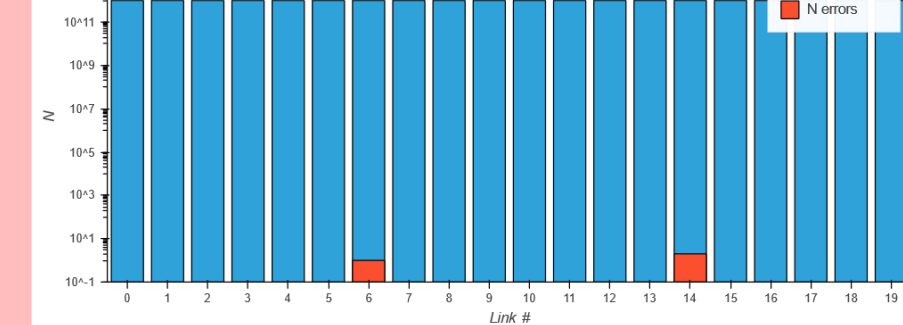
Cooling performance



Noise Maps



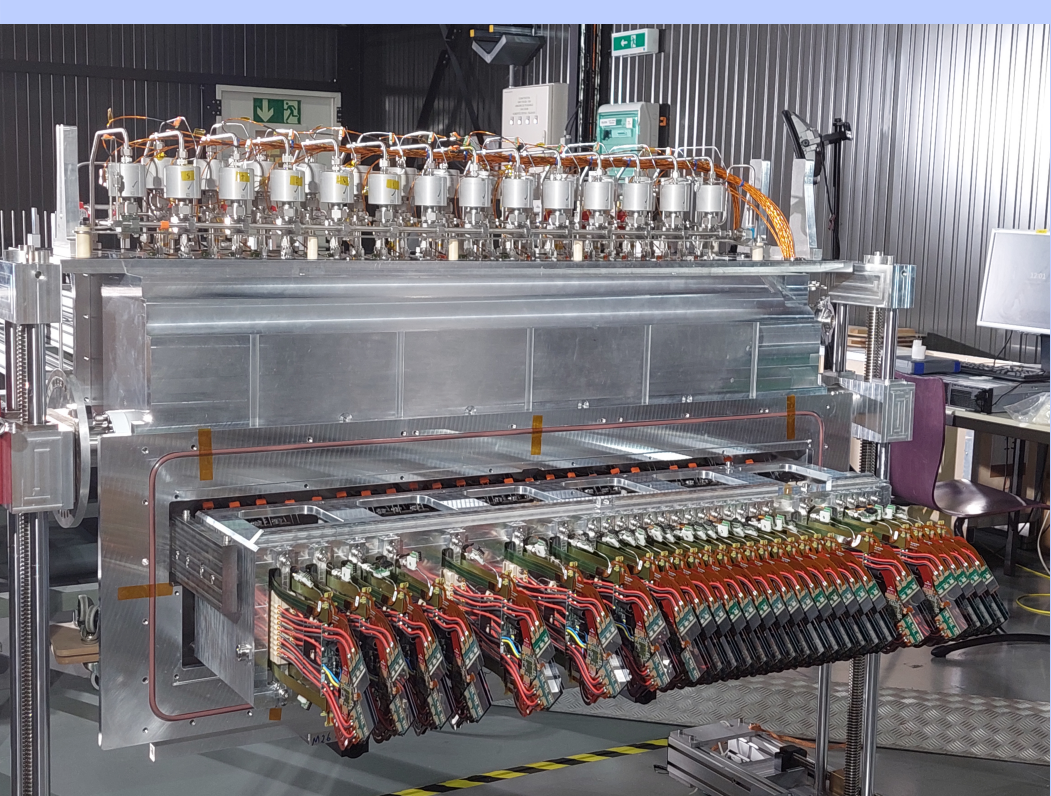
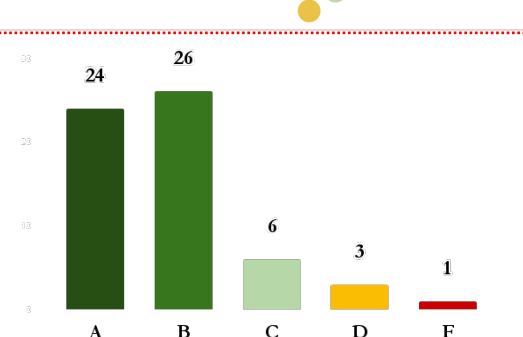
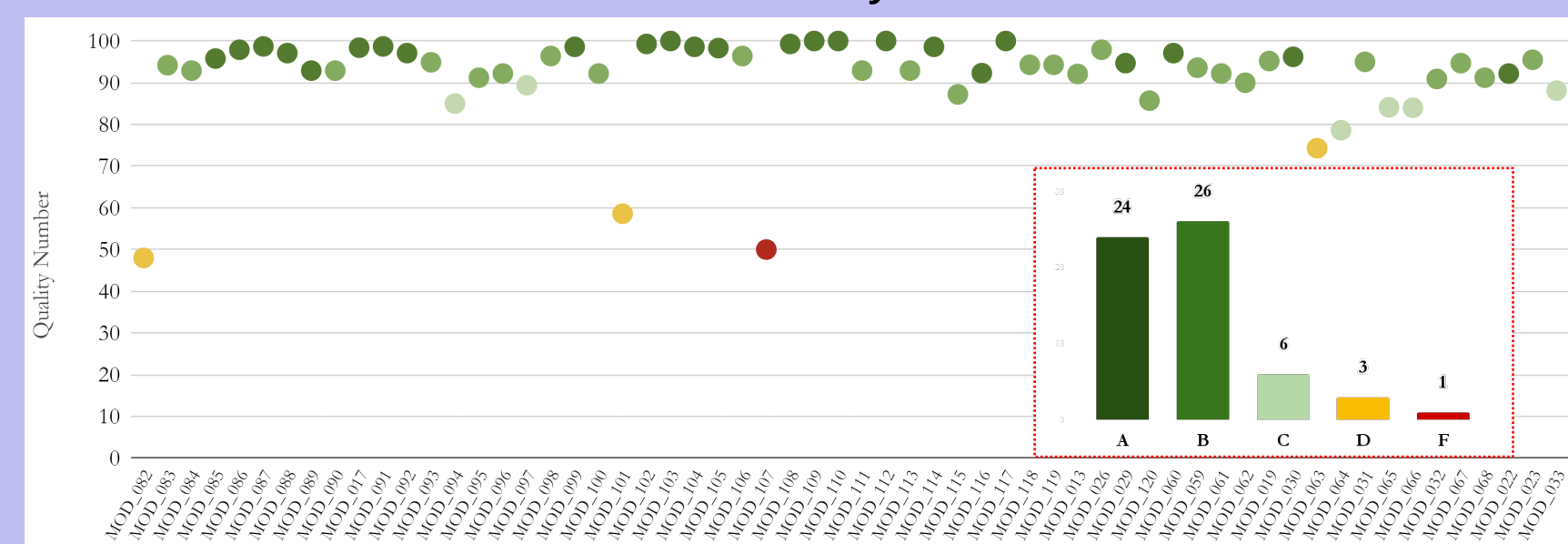
Bit Error Rate



- 60 modules produced

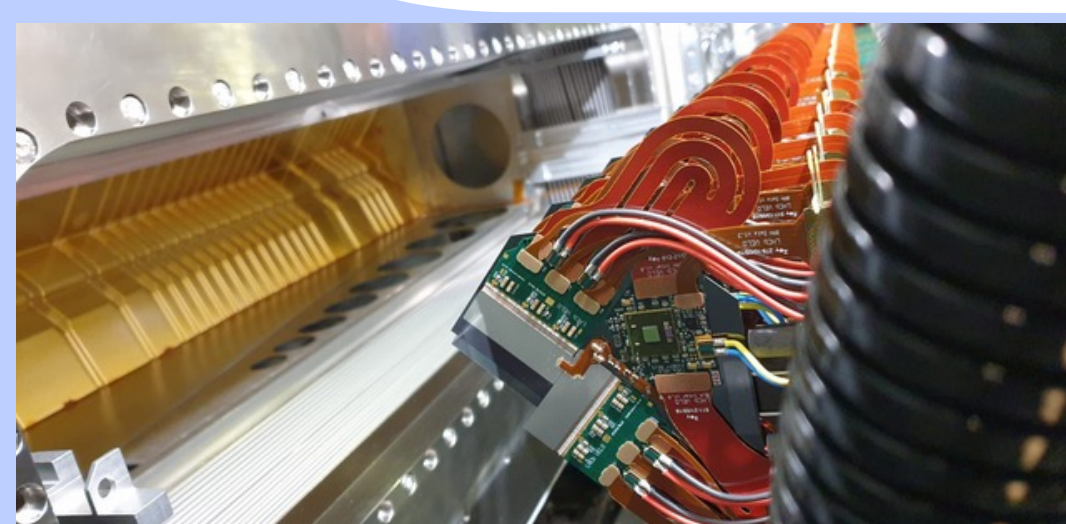
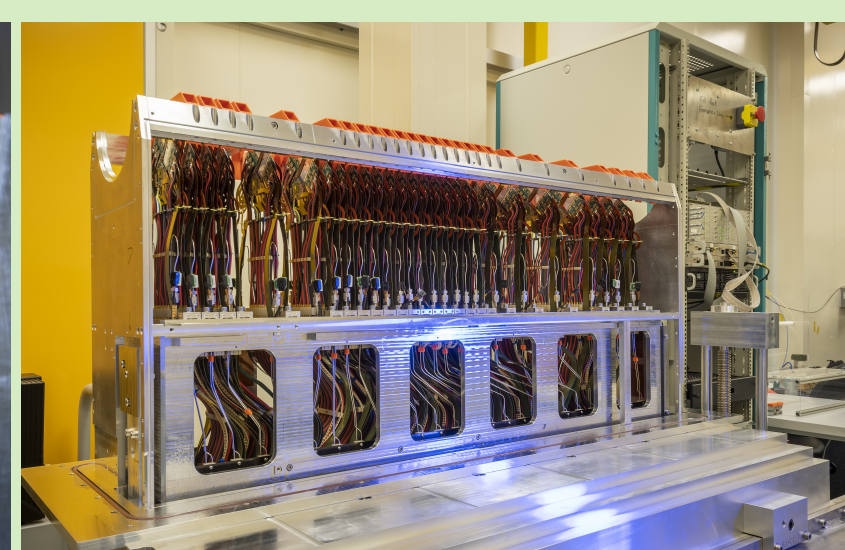
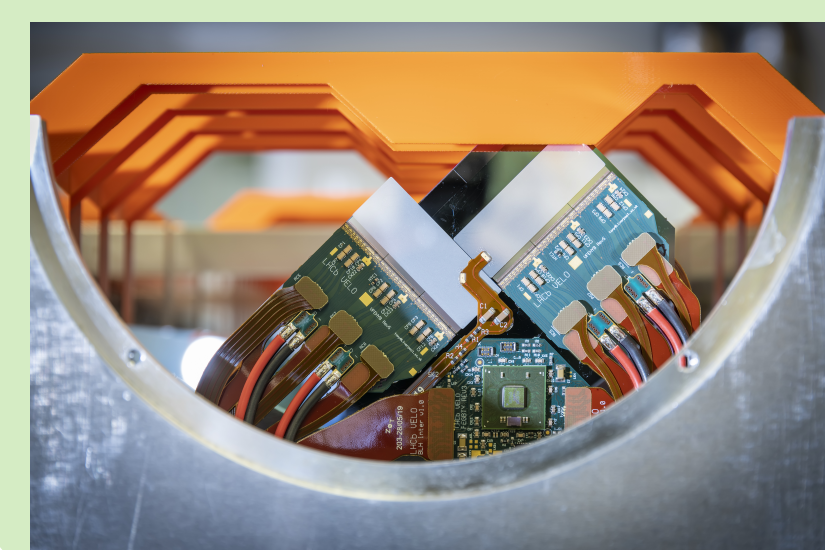
- 56 detector quality (>99% eff: A,B,C grade)

- > 93% yield



- Check leak tightness again
- Run "warm" tests:
 - Bit Error rate, HV, LV
 - Test temperature sensors
 - Metrology

Full detector successfully installed!



References

- [1] Int. J. Mod. Phys. A 30, 1530022 (2015)
- [2] LHCb-TDR-013
- [3] A Nomerotski et al 2013 JINST 8 P04004