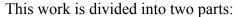
## Operation of a TPC using the SAMPA chip integrated in the SRS

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- Developments in terms of hardware and software to integrate the SAMPA chip in the SRS
- Construction of a triple-GEM based TPC to test the new readout system by building a setup to detect cosmic rays.



**IEAP CTU in Prague** 



Presenter: André Cortez

**Electronics overview** 

# FEC Board from RD51/CERN

Development a new **FPGA Firmware** 





### SAMPA CHIP 32 channels per chip with

- Front-end + ADC + DSP.
- ADC: 10-bit resolution, up to 18.5 MSPS.



FEC Board from RD51/CERN

**Developed** adapter board

**Developed** hybrid board 4x SAMPA chip

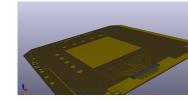






### **Time Projection Chamber prototype**

- Standard triple-GEM
- Operating with Ar/CO<sub>2</sub>(70/30)
- 10 cm x 10 cm sensitive area
- 8 cm drift volume
- 120 pad readout (independently-read)
- SAMPA integration in SRS (RD51/CERN)
- Telescope setup (double coincidence trigger with two photomultipliers)



Signal processing and event selection for the reconstruction of cosmic rays tracks

