

## A 0.22 nV/√Hz, 4.5 mW/channel cryogenic amplifier for large arrays of SiPMs in liquid Argon



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This amplifier is designed to operate at LN or Lar for reading large area SiPM arrays, hundreds of nF.

It is a hybrid solution with a SiGe HBT as input and a fully differential Bi-CMOS amplifier as second stage.

48 SiPMs @4.5Vov S/N≈8.5 (FD1-HD conditions) 96 SiPMs @4.5Vov S/N≈5.0 200 nF SiPM

**Cold Amplifier** 

The result shown here was obtained with a preamplifier that dissipates 2 mW and has a series noise of 0.37 nW/VHz.

A new version with a noise of about 0.2 nV/VHz and a power dissipation of 4.5 mW was developed (in background here). 106 107

