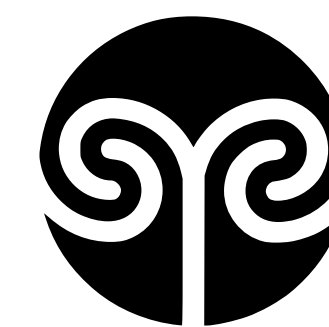# AIDA++
Three proposals from the Silicon community

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🌀 Monolithic pixels in HV-CMOS tech.

[Valerio Re (UNIBG) & Attilio Andreazza (UNIMI)]

Potential partners:
> INFN-Mi, Pv & Pisa
> Uni. Bonn (Norbert Wermes)
> Strasbourg (M. Winter et al.)
> Lancaster?

Future machines:
> Belle II upgrade
> tracker for Cepc/FCC

Key Items:
> for BelleII: r.o. speed and background compliancy
> for both: stitching

▷ Mechanics & cooling

[Any volunteer? Romualdo?]

Potential partners:
✶ the OR of the INFN units interested in the sensor development
✶ international partners?

Future machines:
✶ very common interest for every machine

Key Items:
✶ light weight mechanics
✶ extreme Silicon thinning
✶ cooling techniques (including Silicon micro-piping)

🌀 Monolithic pixels in LFoundry tech.

[Manuel Da Rocha Rolo (INFN-To]

Potential partners:
> INFN-Mi, Tn, Pd, Bo, Pg, Torino, Pv
> international partners?

Future machines:
> vtx & tracker for Cepc/FCC

Key Items:
> extend the ARCADIA program, going from the reticle size prototype to stitching up to the wafer scale
> end product: stave O(10 cm)