PM2018 - 14th Pisa Meeting on Advanced Detectors

Friday, 1 June 2018

<u>**Front, Trigger, DAQ and Data Management - Poster session**</u> **(16:00 - 20:15)**

[id] title	presenter	board
[323] Readout chain validation of INFN modules for the CTA-pSCT camera	LOPORCHIO, Serena	
[321] Test of a New Octal Amplifier Shaper Discriminator Chip for the ATLAS MDT Chambers at HL-LHC	KROHA, Hubert	
[322] Outcome of the KLOE-2 experiment after the conclusion of the data-taking period	SIRGHI, Florin Catalin	
[320] First-Level Muon Track Trigger for Future Hadron Collider Experiments	NOWAK, Sebastian	
[319] FATALIC: a fully integrated electronics for the ATLAS tile calorimeter at the HL-LHC	MADAR, Romain	
[318] Upgrade of the ATLAS detectors and trigger at the High Luminosity LHC: tracking and timing for pile-up suppression	TESTA, Marianna	
[317] WaveDAQ: an highly integrated trigger and data acquisition system	GALLI, Luca	
[316] Computing Infrastructure at the CERN Neutrino Platform prototypes experiments	BENEKOS, Nektarios	
[315] Kalman meets Molière : Optimal measurement of charged particle momentum from multiple scattering by Bayesian analysis of filtering innovations	BERNARD, Denis	
[314] Optical Fiber Center Module for the KOTO Experiment	BOGDAN, Mircea	
[313] From the Phase-0 DAQ upgrade of entire ATLAS Pixel Detector towards the Phase-2 electronics upgrdae	GABRIELLI, Alessandro	
[312] Web-based Experiment Monitoring with HTML5	RITT, Stefan	
[354] The algorithm of the CMS Level-1 Overlap Muon Track Finder trigger	BUNKOWSKI, Karol	
[353] Front-End Electronics of the Electromagnetic Barrel-Calorimeter for the PANDA Target Spectrometer*	HAHN, Christopher	
[352] Design and test of the calibration system of the MEGII Pixelated timing Counter	CATTANEO, Paolo Walter	
[351] Design of the ATLAS phase-II hardware based tracking processor	POGGI, Riccardo	
[350] Level-1 track finding with an all-FPGA system at CMS for the HL-LHC	ARDILA PEREZ, Luis	
[349] Mu2e calorimeter readout electronic	CAIULO, Davide	
[348] Software framework architecture for the high data rate soft X-rays PERCIVAL imager	BOITRELLE, Benjamin	
[347] Radiation study of FPGAs with neutron beam for the COMET Phase-I	NAKAZAWA, Yu	
[346] Testing and integration of front end electronics for INO-ICAL RPCs	GAUR, Ankit	
[345] First test results of the CHIPIX65 asynchronous front-end connected to a 3D sensor	GAIONI, Luigi	
[344] High performance DAQ for muon spectroscopy experiments	SOLDANI, Mattia	
[343] A low cost, high speed, mltichannel Analog to Digital converter board	MUSICO, Paolo	

[342] Front-end electronic system for large area photomultipliers readout	MUSICO, Paolo	
[341] Low Latency serial communication for MEG II Trigger system	FRANCESCONI, Marco	
[340] The MYTHEN-III strip detector prototypes	ANDRÄ, Marie	
[339] Data acquisition system for the EDET DH80k instrument	POLOVYKH, Mikhail	
[338] Scintillation light DAQ and trigger system for the ICARUS T600 experiment at Fermilab	BABICZ, Marta	
[337] Design of a high radiation-hard driver for Mach-Zehnder Modulators based high-speed links for hadron collider applications	PALLA, Fabrizio	
[336] Performance results of the trigger logic implemented in EUSO-SPB	BERTAINA, Mario Edoardo	
[335] Selecting and Designing the Front-end Amplifier for High-gain Photomultiplier Detectors with Optimal Timing Performance	CORSI, Francesco	
[334] Towards new Front-End Electronics for the HADES Drift Chamber System	WIEBUSCH, Michael	
[333] Reconstruction at 30 MHz for the LHCb upgrade.	SZUMLAK, Tomasz	
[332] Monte Carlo Response Function Simulations for the HEXITEC CdTe X-ray Detector	KOCH-MEHRIN, Kjell	
[331] Design and Preliminary Characterization Results of BASIC64, a New Mixed-Signal ASIC for SiPM Detectors	CALÒ, Pietro Antonio Paolo	
[330] Development of a highly selective muon trigger exploiting the high spatial resolution of monitored drift-tube chambers for the ATLAS experiment at the HL-LHC	KORTNER, Oliver	
[329] The CMS Level-1 tau lepton and vector boson fusion triggers for the LHC Run II	MARTIN PEREZ, Cristina	
[328] The Phase-I Trigger Readout Electronics Upgrade of the ATLAS Liquid Argon Calorimeters	YANG, Yi-Lin	
[327] Upgraded back-end electronics for the CMS Fast Beam Conditions Monitor	TOSI, Nicolò	
[326] C++ implementation of Bethe-Heitler, 5D, Polarized, $\gamma \to e^+ e^-$ Pair Conversion Event Generator	SEMENIOUK, Igor	
[325] An innovative radiation hardened Content-Addressable Memory	SHOJAII, Seyedruhollah	
[324] Towards the large area HVCMOS demonstrator for ATLAS ITk	PRATHAPAN, Mridula	
[367] The performance of the CMS ECAL data acquisition system at LHC Run 2	WAMORKAR, Tanvi	
[366] The Monitoring Electronics of the Laser Calibration System in the Muon g-2 experiment	IACOVACCI, Michele	
[365] Design and performance evaluation of front-end electronics for COMET straw tracker	UENO, Kazuki	
[364] Operation of Microchannel Plate PMTs with TOFPET multichannel timing electronics	LAPINGTON, Jon	
[363] Muon g-2 Calibration system data flow	MASTROIANNI, Stefano	
[362] A new readout electronics for the LHCb Muon Detector Upgrade	BRUNDU, Davide	
[361] Performance of a high-throughput tracking processor implemented on Stratix-V FPGA	LAZZARI, Federico	
[360] Development of a high voltage power supply for detectors using photo-diode	RUDRA, SHARMILI	
[359] Characterization and first field results of a new 64ch custom front-end ASIC for GEM readout	ALEXEEV, Maxim	

[358] Self-Contained Configuration Scrubbing in Xilinx FPGAs for On-detector Applications	GIORDANO, Raffaele	
[357] Silicon Drift Detectors arrays and readout ASICs for the SIDDHARTA experiment	AMIRKHANI, Aidin	
[356] Trigger Performance Verification and Simulation of the FlashCam Prototype Camera	SAILER, Simon	
[355] The new trigger/GPS module for the EEE Project	PANETTA, Maria Paola	

[358] Self-Contained Configuration Scrubbing in Xilinx FPGAs for On-detector Applications	GIORDANO, Raffaele	
[357] Silicon Drift Detectors arrays and readout ASICs for the SIDDHARTA experiment	AMIRKHANI, Aidin	
[356] Trigger Performance Verification and Simulation of the FlashCam Prototype Camera	SAILER, Simon	