



- due to the shorter radiator

FEC

EIC high-momentum hadron PID needs

Read out system and DAQ to study the prototype

photon detectors with unprecedented performance figures

ANODE WITH PAD

Hardware: collecting and processing the signals by RD51-SRS

- Front End based on APV 25 analog chip
- An APV card host one APV25 chip with double diode protection against discharges
- Each chip reads 128 channels at a 40 MHz frequency (25ns)
- 8 APV cards are used for the prototype studies, for a total of 1024 channels
- Analog data is sent via HDMI cables to the ADC card managed by the FPGA.
- Data is sent using the UDP protocol via the Ethernet port to the PC for the data collection and storage

Software: RAVEN DAQ

Raven DAQ: An original DAQ system developed in Trieste for SRS

- LabView based novel DAQ Raven system has been developed to control the data acquisition and to increase the acquisition trigger rate
- The settings of the APV25 chips and their configuration is performed via Raven
- **Fast data processing** (nearly online collected data)
- RavenDAQ takes care of **Pedestal Subtraction** (noise filtering) and **Zero Suppression** (separating physics data and meaningless data)
- **RAVEN** is enriched with extremely user friendly graphical interface

photons

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