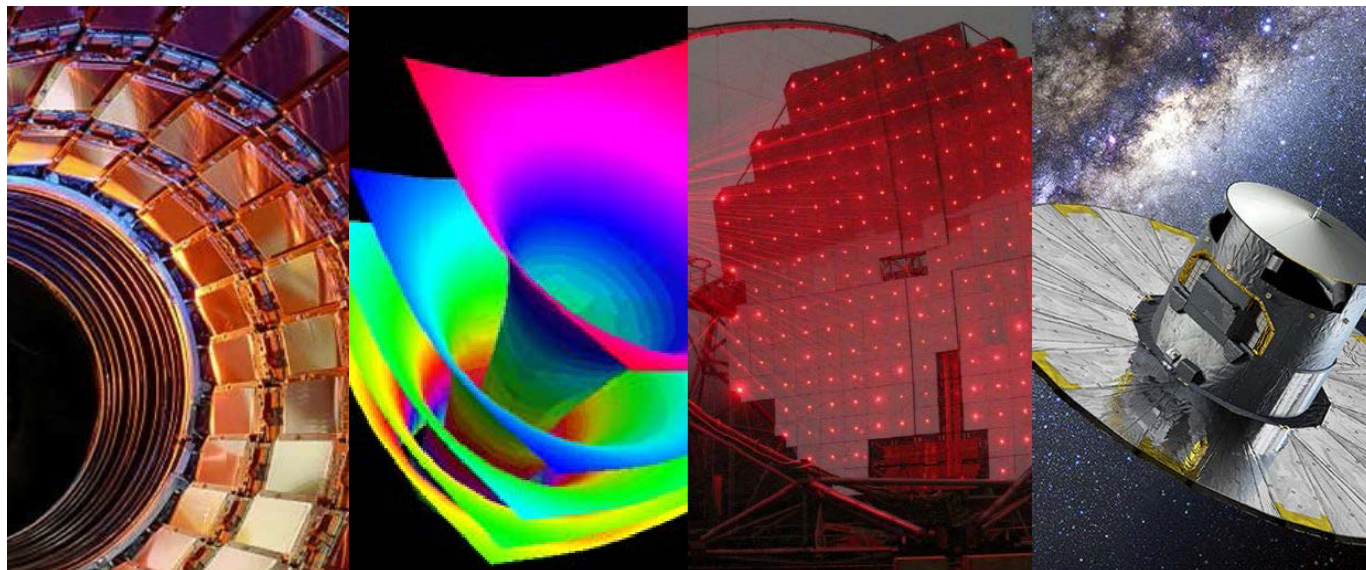




EXCELENCIA
MARIA
DE MAEZTU

Institute of Cosmos
Sciences



Time Resolution of Large Area Low Light Level Sensors

R. Ballabriga, J.M Fernández, L. Garrido,
D. Gascon, E. Grauges, S. Gomez, G.
Guixé, J. Mauricio, R. Manera, E.
Picatoste, D. Sanchez, A. Sanmukh, A.
Sanuy

ICCUB – Universitat de Barcelona

SiPM workshop

Bari

04/10/2019

I. Introduction

II. Multi-Channel Readout (HRFlexToT)

III. Active Summation (MUSIC)

IV. Towards a Hybrid Single Photon Sensor
(FastIC & FastICPix)

V. Discussion

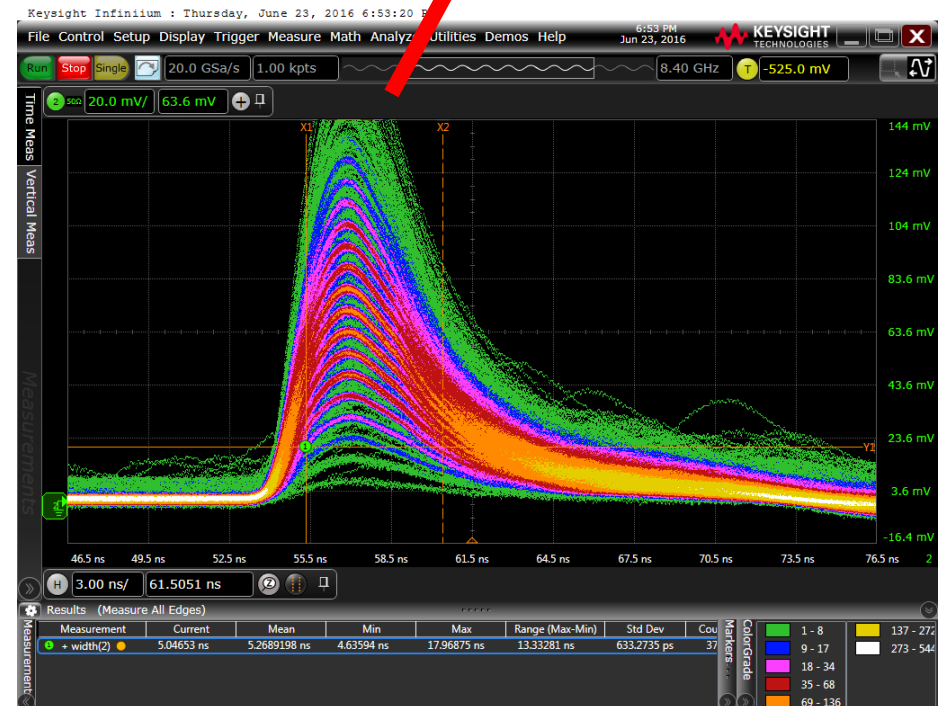
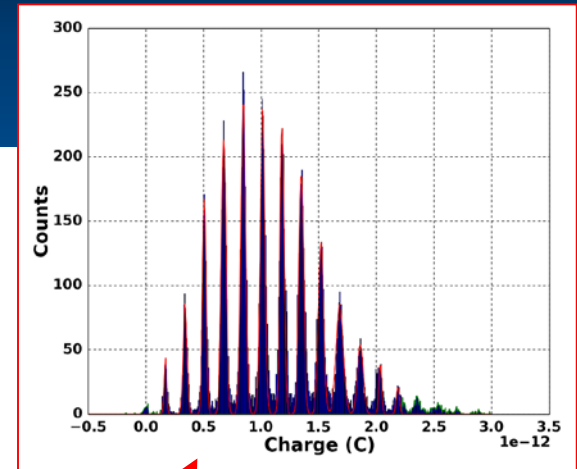
I. Introduction

- **Institute of Cosmos Science of the Univ. of Barcelona (ICCUB)**
 - Particle and Nuclear Physics, Astrophysics and Cosmology
 - Theory, Observation, Space Missions and Experiment
 - LHCb, BaBar, CTA, Gaia, Solar Orbiter, Ariel, HERD
- **Long track record on development of detectors based on photosensors and associated readout electronics**
 - Calorimeters, Scintillating Fiber Trackers, RICH
 - Cherenkov Telescopes
 - Medical Imaging
- **Strong interest in picosecond timing**
 - LHCb phase-II upgrade
 - ToF PET

I. Introduction

- SiPMs are replacing PMTs in many applications:
 - Higher PDE
 - Accurate photopeak resolution
 - Better time resolution
 - More compact and robust
 - Lower bias voltage
 - Insensitive to magnetic field
- Astrophysics
- Particle Physics
- Medical Imaging
- Homeland security
- LIDAR (autonomous driving)

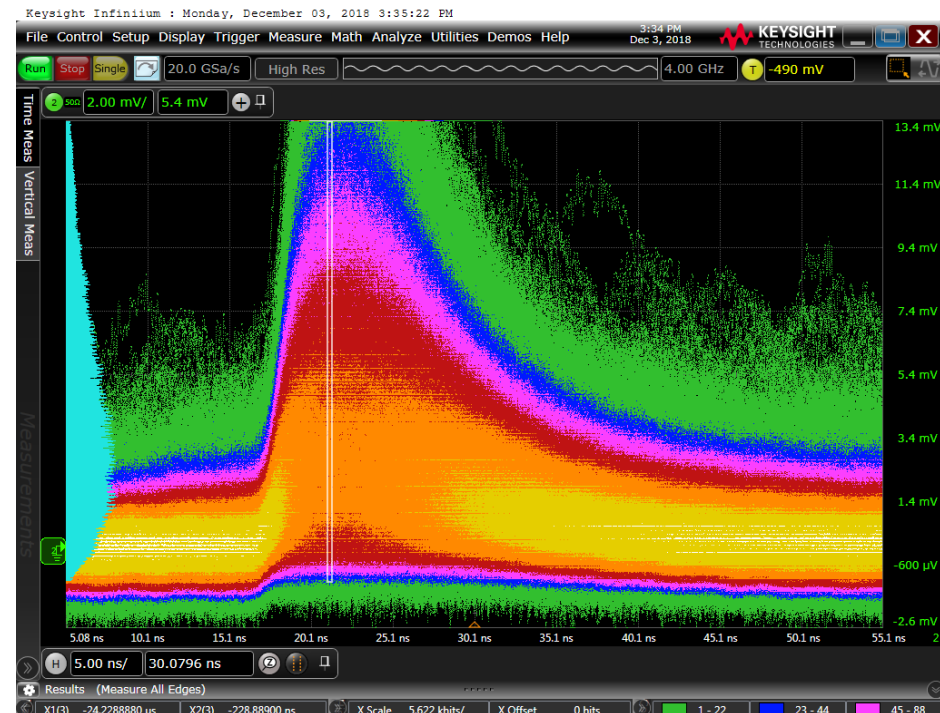
Charge spectrum by integration



I. Introduction

- SiPMs are replacing PMTs in many applications:
 - Higher PDE
 - ~~Accurate photopeak resolution~~
 - ~~Better time resolution (faster rise time)~~
 - More compact and robust
 - Lower bias voltage
 - Insensitive to magnetic field

**NOT TRUE FOR LARGE
AREA ($> 1 \text{ cm}^2$) DEVICES**

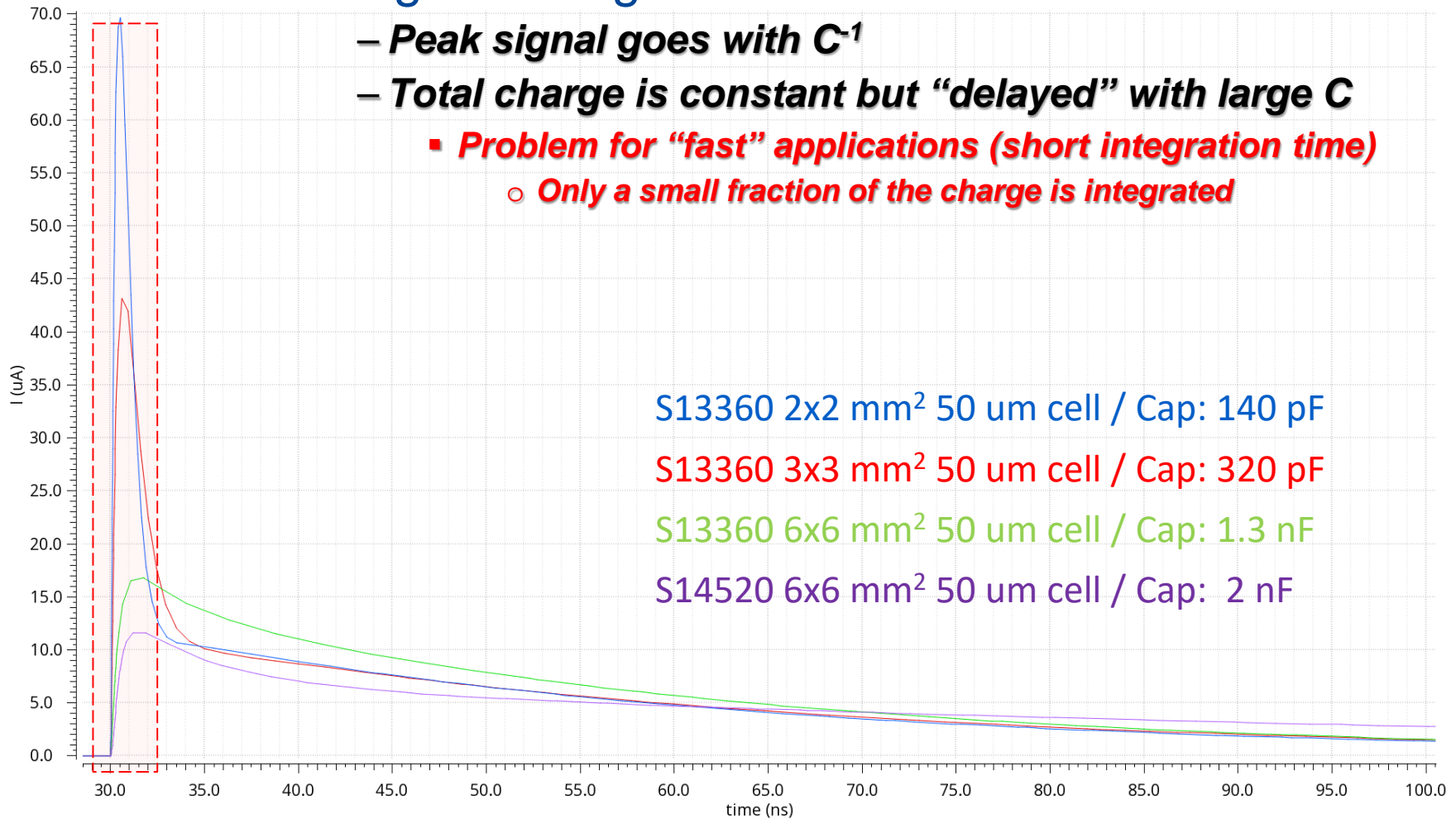


I. Introduction

Pulse Shape vs Sensor Area

- Single cell signal for different SiPMs

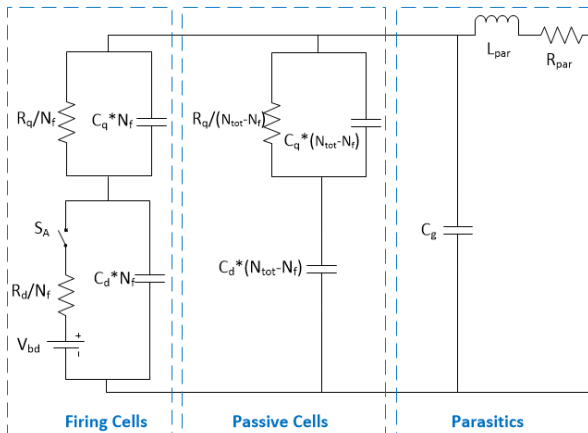
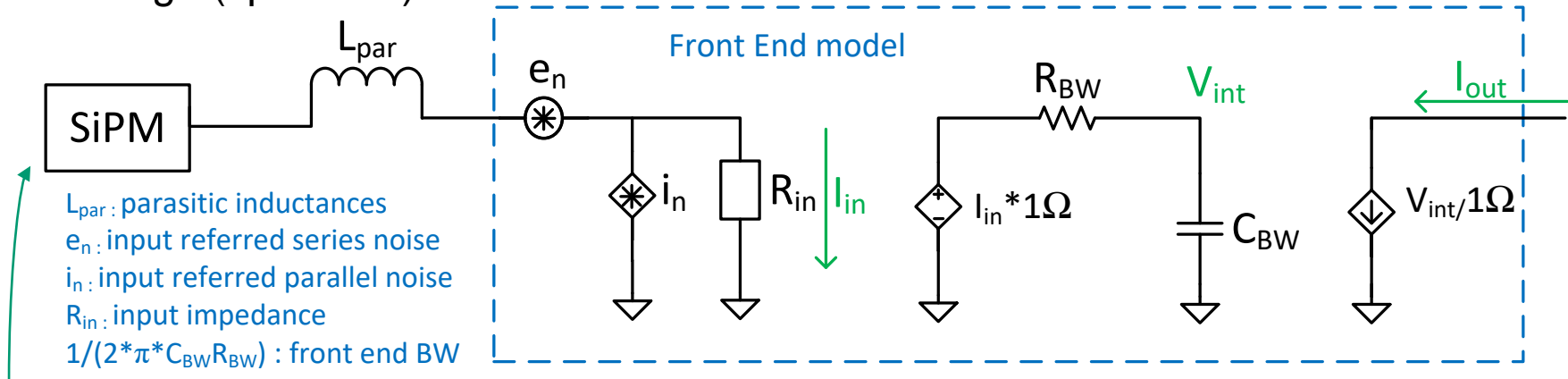
Tue Feb 12 15:43:43 2019 1



I. Introduction

Front end model

- A more detailed model of the front end
 - Not just an impedance
- Need to include also noise and FE BW to evaluate resolution
 - Charge (spectrum) and time



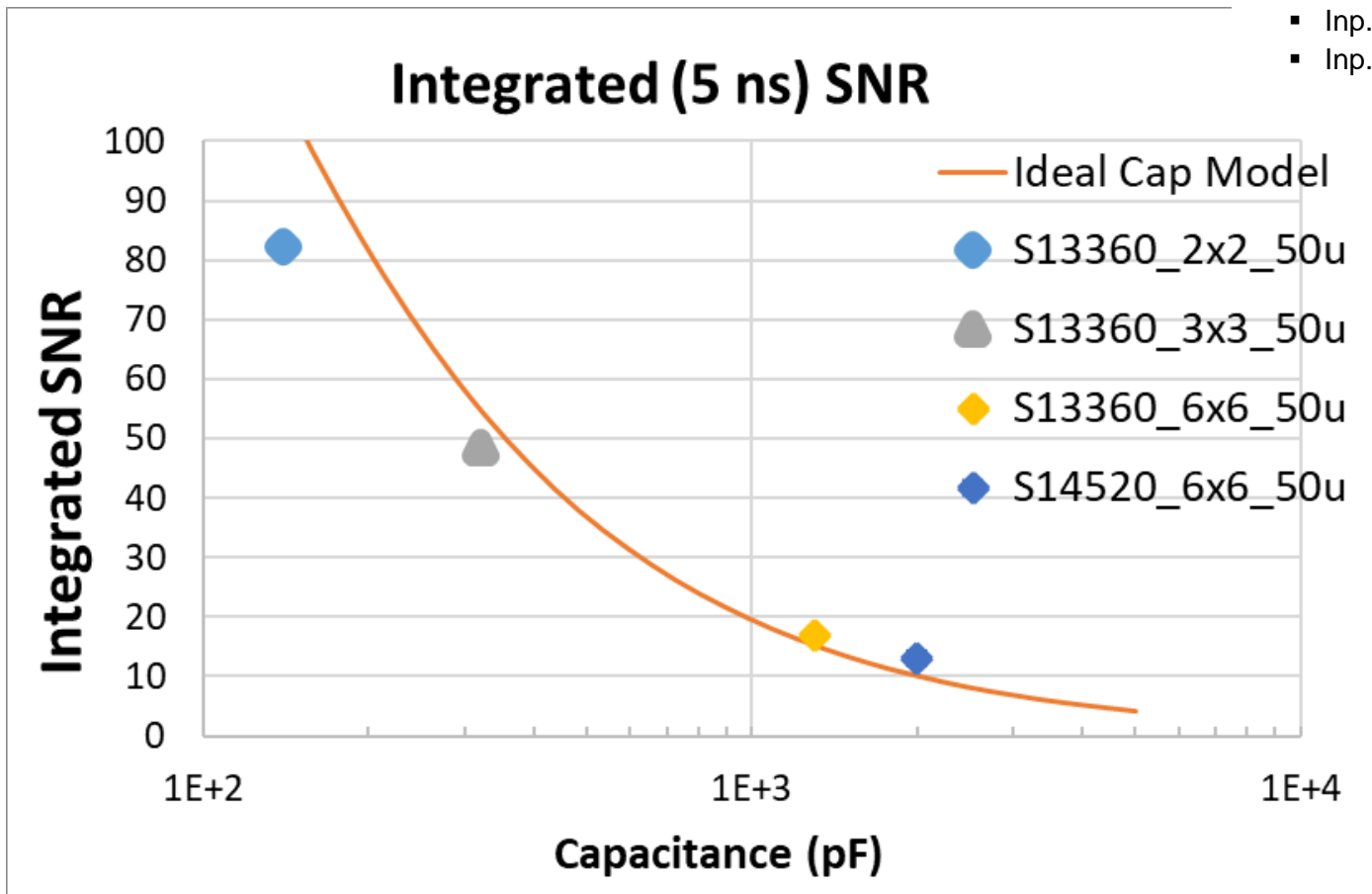
SiPM model (F.Corsi et al) or single Capacitance (ideal)

I. Introduction

Large area SiPMs

- Energy/light is typically measured by integration
- This is reflected in charge spectrum (finger plot)

- SiPM overvoltage: 4.5 V
- Amplifier parameters
 - $R_{in} = 15 \Omega$
 - $BW = 500 \text{ MHz}$
 - Inp. ref. ser. noise: $2 \text{ nV}/\sqrt{\text{Hz}}$
 - Inp. ref. par. noise: $10 \text{ pA}/\sqrt{\text{Hz}}$

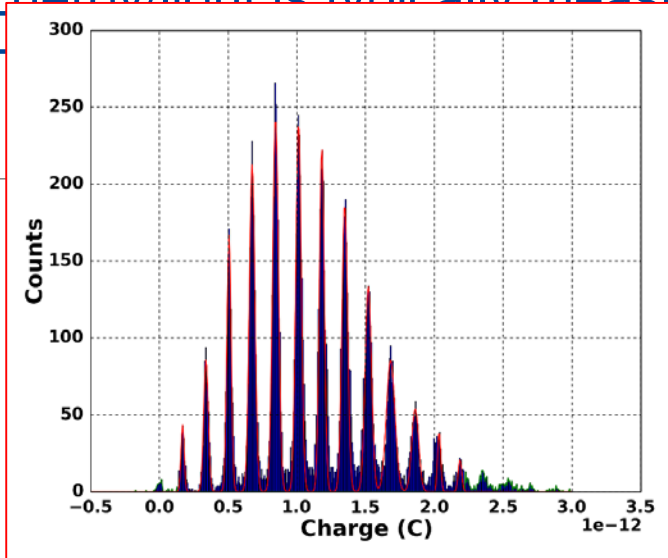


I. Introduction

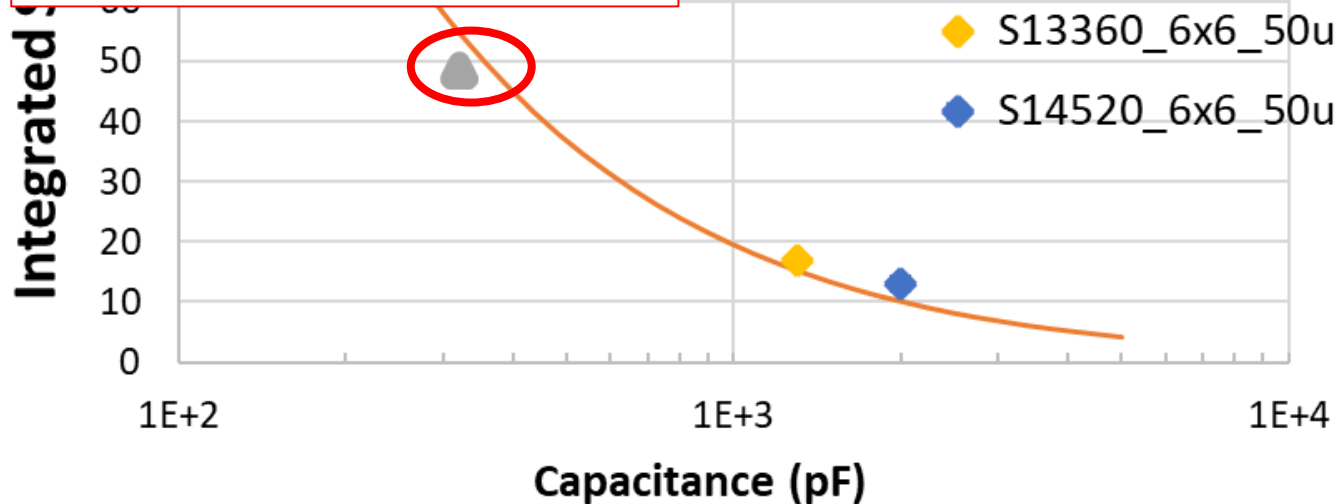
Large area SiPMs

- Energy/light is typically measured by integration
- The spectrum (finger plot)

- SiPM overvoltage: 4.5 V
- Amplifier parameters
 - $R_{in} = 15 \Omega$
 - $BW = 500 \text{ MHz}$
 - Inp. ref. ser. noise: $2 \text{ nV}/\sqrt{\text{Hz}}$
 - Inp. ref. par. noise: $10 \text{ pA}/\sqrt{\text{Hz}}$



5 ns) SNR

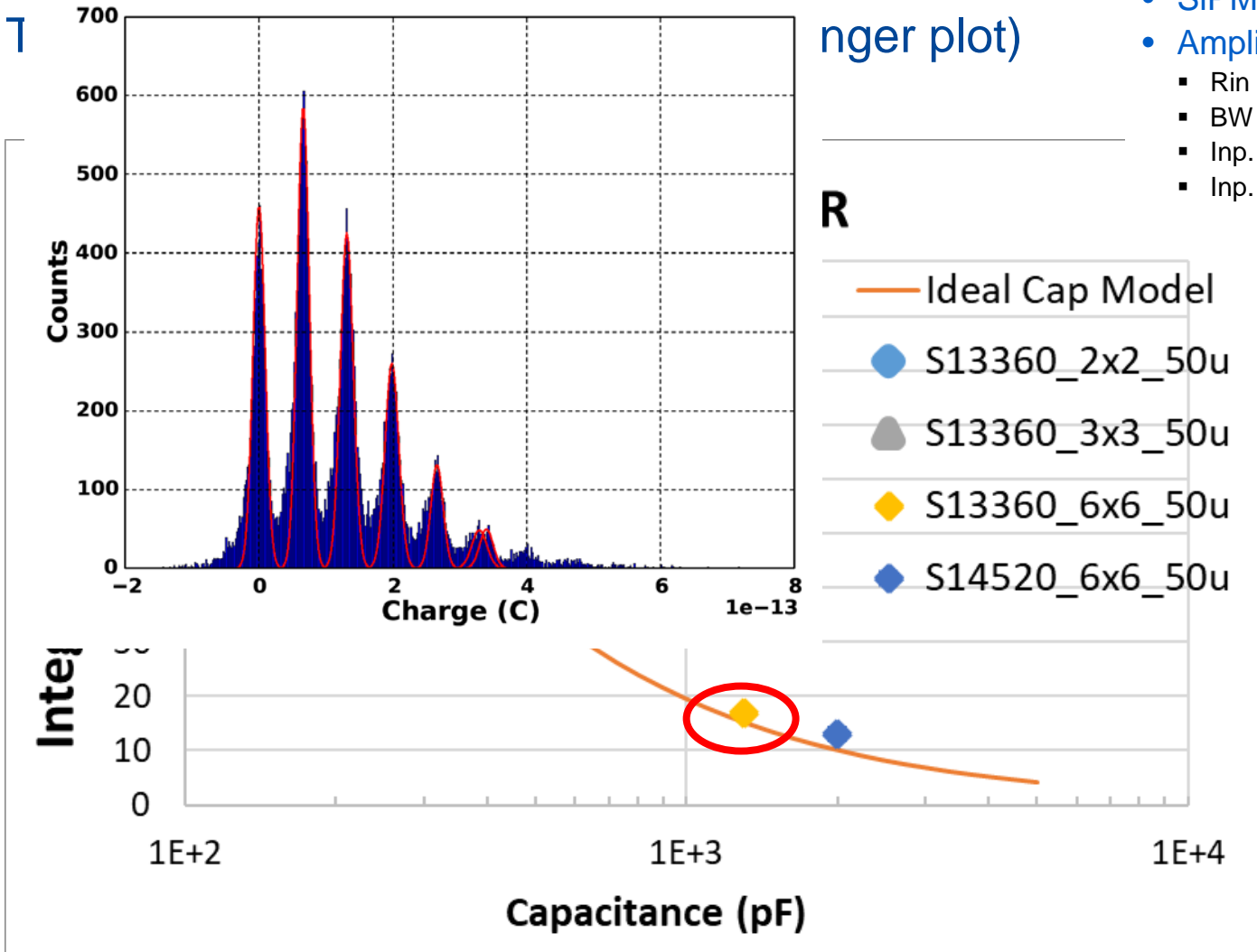


I. Introduction

Large area SiPMs

- Energy/light is typically measured by integration (single channel)
- T

- SiPM overvoltage: 4.5 V
- Amplifier parameters
 - $R_{in} = 15 \Omega$
 - $BW = 500 \text{ MHz}$
 - Inp. ref. ser. noise: $2 \text{ nV}/\sqrt{\text{Hz}}$
 - Inp. ref. par. noise: $10 \text{ pA}/\sqrt{\text{Hz}}$

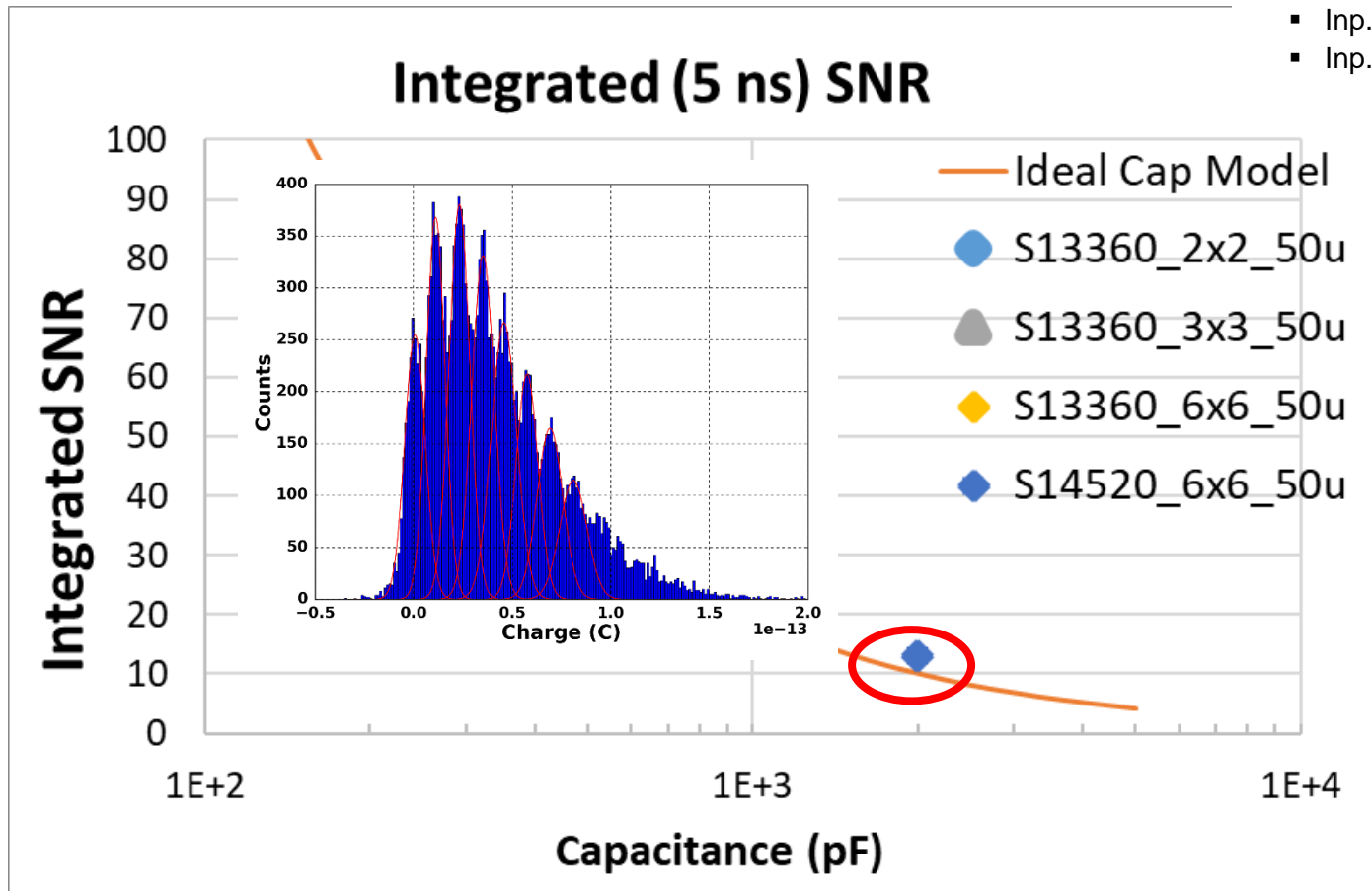


I. Introduction

Large area SiPMs

- Energy/light is typically measured by integration
- This is reflected in charge spectrum (finger plot)

- SiPM overvoltage: 4.5 V
- Amplifier parameters
 - $R_{in} = 15 \Omega$
 - $BW = 500 \text{ MHz}$
 - Inp. ref. ser. noise: $2 \text{ nV}/\sqrt{\text{Hz}}$
 - Inp. ref. par. noise: $10 \text{ pA}/\sqrt{\text{Hz}}$



I. Introduction

Large area SiPMs: jitter

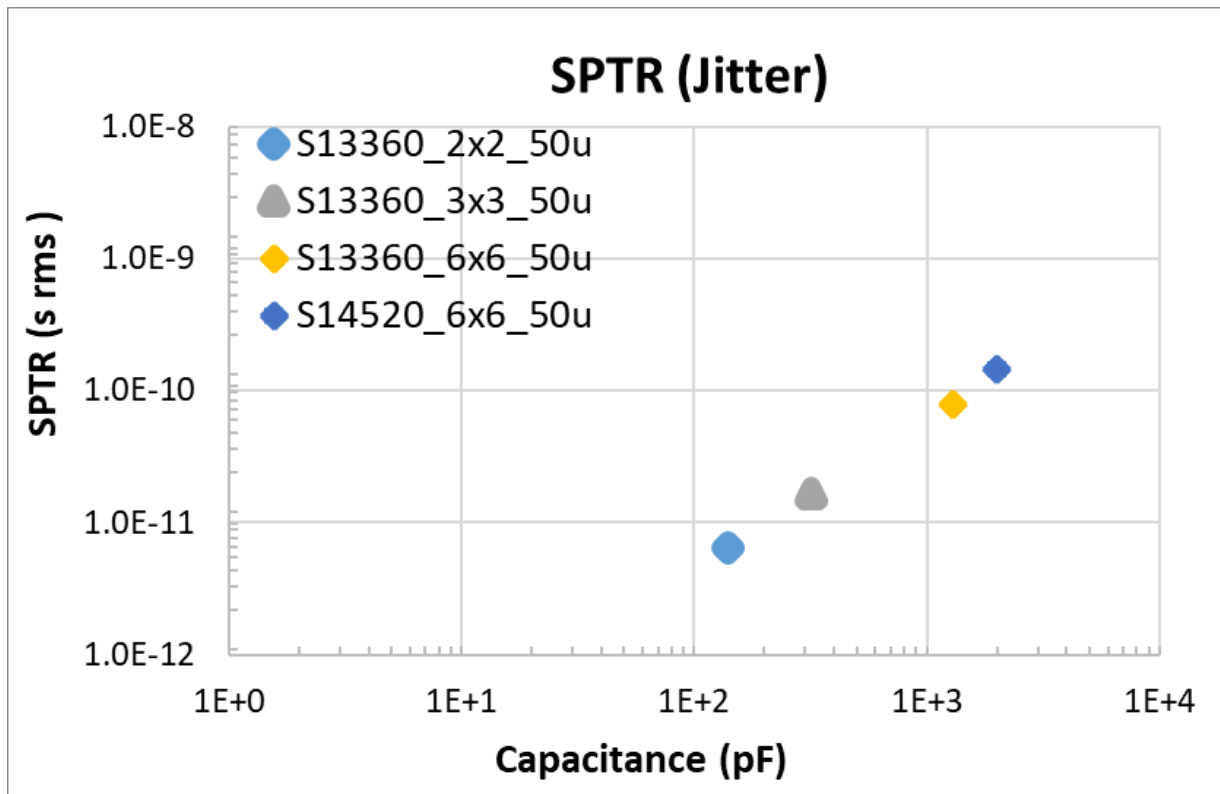
- Jitter (Single Photon Time Resolution, SPTR):

$$\text{SPTR} = \frac{I_{no}}{\partial I_{o1p} / \partial t}$$

I_{no} → Integrated output noise
 $\partial I_{o1p} / \partial t$ → 1 cell signal gradient

– Only electronics contribution (no SPAD jitter or skews)

- SiPM overvoltage: 4.5 V
- Amplifier parameters
 - $R_{in} = 15 \Omega$
 - $BW = 500 \text{ MHz}$
 - Inp. ref. ser. noise: $2 \text{ nV}/\sqrt{\text{Hz}}$
 - Inp. ref. par. noise: $10 \text{ pA}/\sqrt{\text{Hz}}$



I. Introduction

Large area SiPMs: jitter

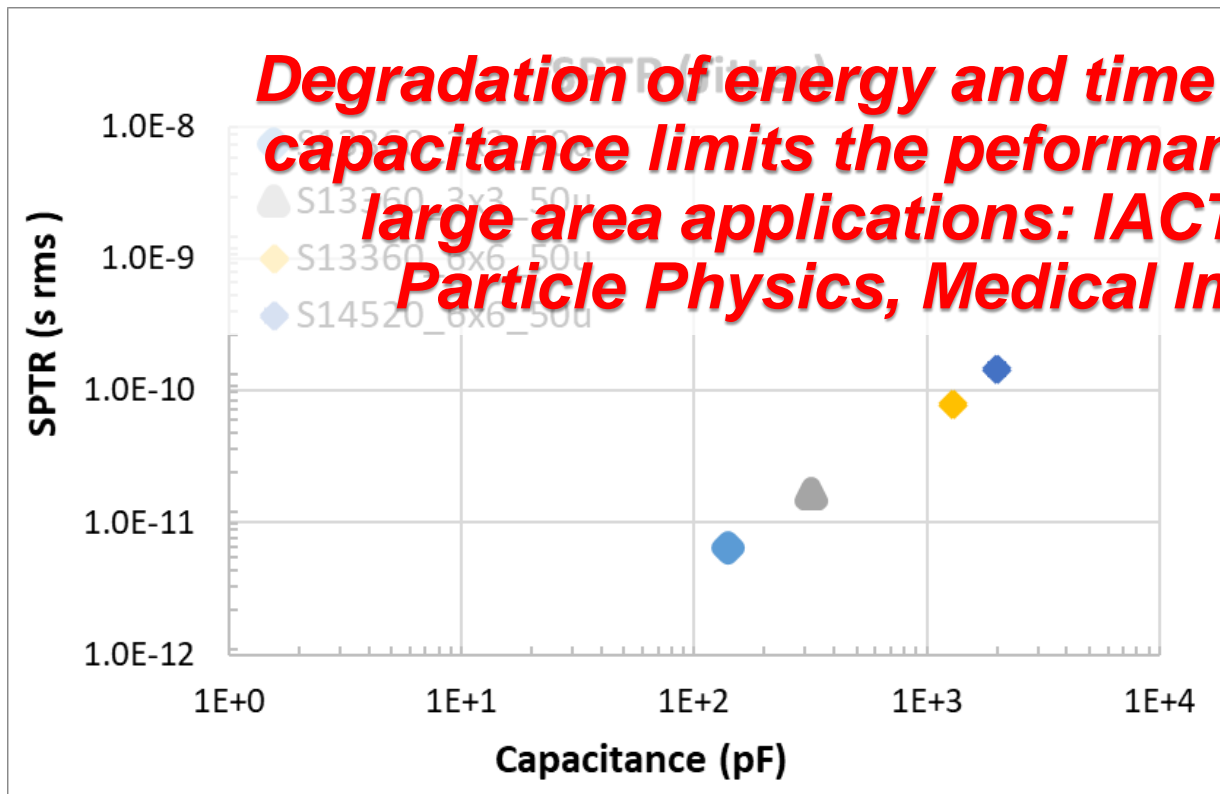
- Jitter (Single Photon Time Resolution, SPTR):

$$\text{SPTR} = \frac{I_{no}}{\frac{\partial I_{o1p}}{\partial t}}$$

I_{no} → Integrated output noise
 $\frac{\partial I_{o1p}}{\partial t}$ → 1 cell signal gradient

– Only electronics contribution (no SPAD jitter or skews)

- SiPM overvoltage: 4.5 V
- Amplifier parameters
 - $R_{in} = 15 \Omega$
 - $BW = 500 \text{ MHz}$
 - Inp. ref. ser. noise: $2 \text{ nV}/\sqrt{\text{Hz}}$
 - Inp. ref. par. noise: $10 \text{ pA}/\sqrt{\text{Hz}}$



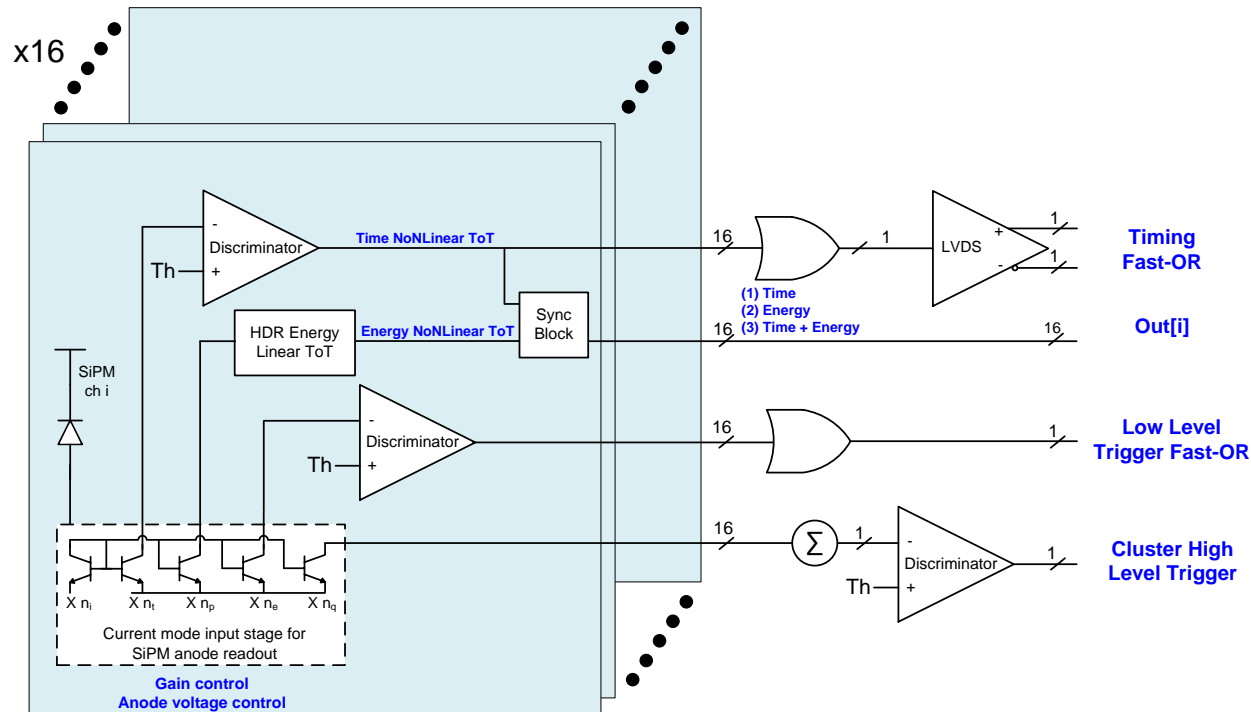
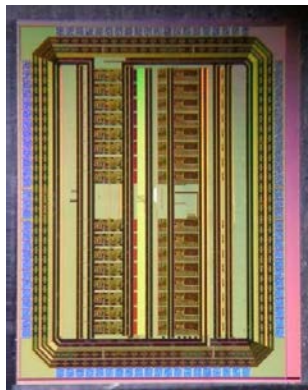
- I. Introduction
- II. Multi-Channel Readout (HRFlexToT)**
- III. Active Summation (MUSIC)
- IV. Towards a Hybrid Single Photon Sensor (FastIC & FastICPix)
- V. Discussion

II. MultiCh readout: HRFlexToT

• HRFlexToT current mode ASIC.

- Linear Time over Threshold with high dynamic range (>9 bits)
- Lower power consumption (about 3.5 mW/ch)
- Different trigger levels and cluster trigger for monolithic crystals.
- Different scintillator time constants.

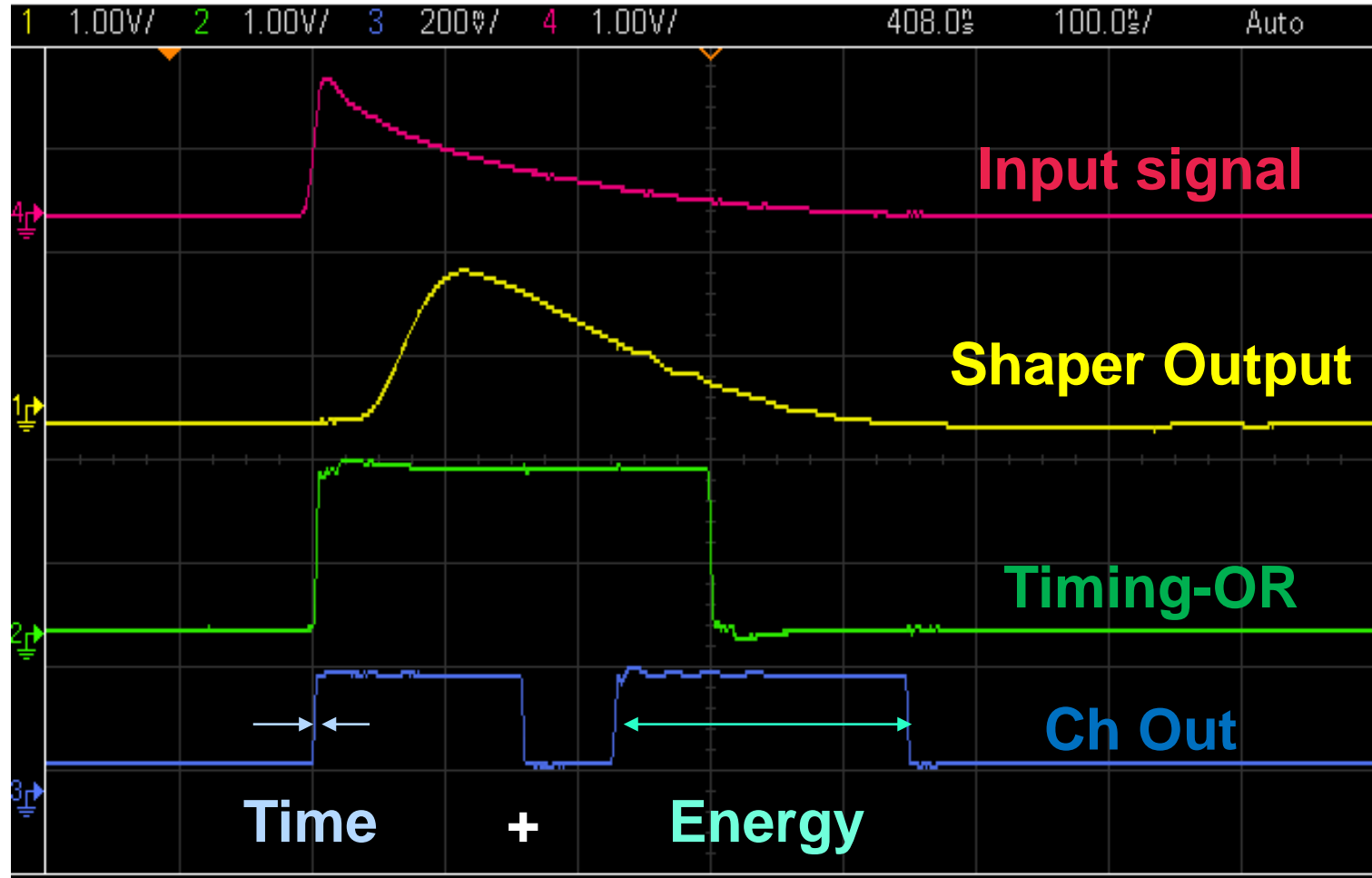
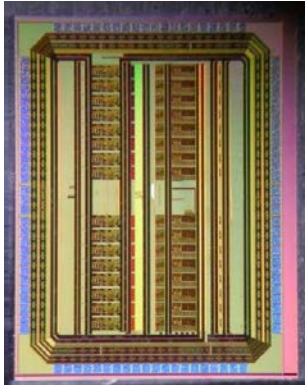
HRFlexToT 180 nm CMOS



S. Gomez, D. Sanchez, D. Gascon, J. Mauricio et al,
L. Freixas, J. Marin, J.M. Perez. P. Rato et al.

II. MultiCh readout: HRFlexToT

HRFlexToT 180 nm CMOS

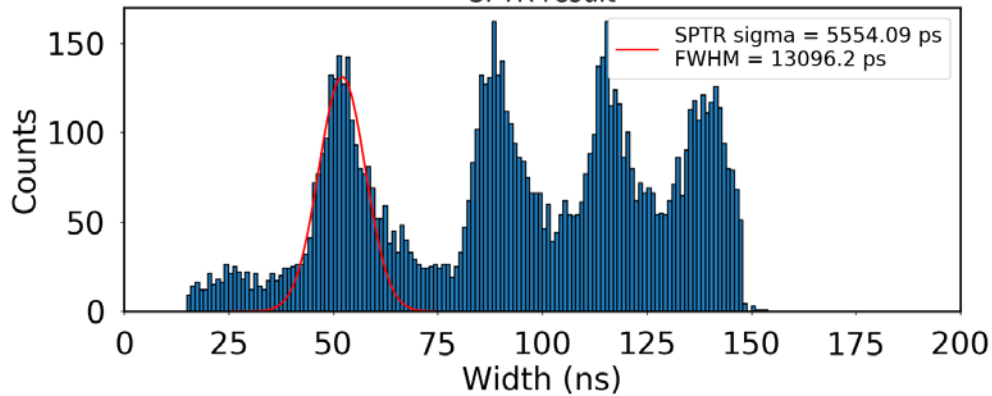


S. Gomez, D. Sanchez, D. Gascon, J. Mauricio et al,
L. Freixas, J. Marin, J.M. Perez. P. Rato et al.

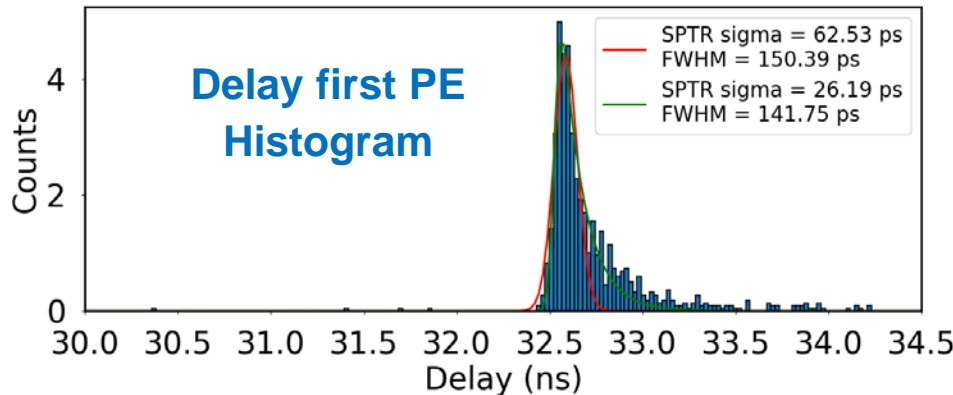
II. MultiCh readout: HRFlexToT

Time width Histogram

SPTR result



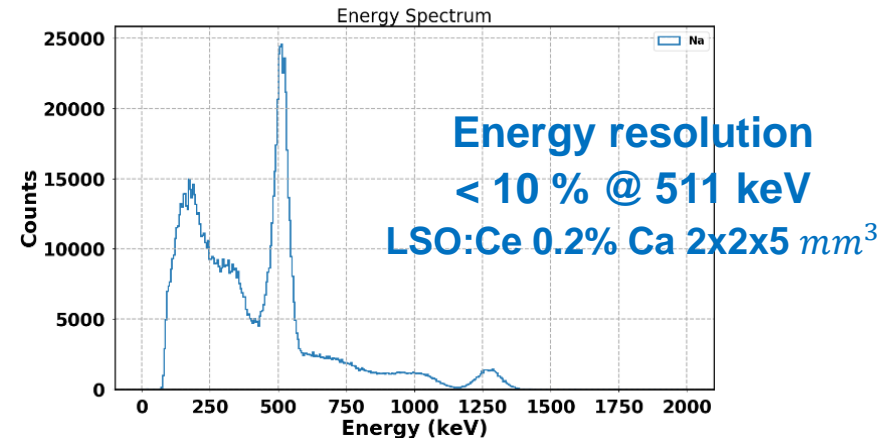
Delay first PE Histogram



Bias voltage 38V (11.5V of Overvoltage)

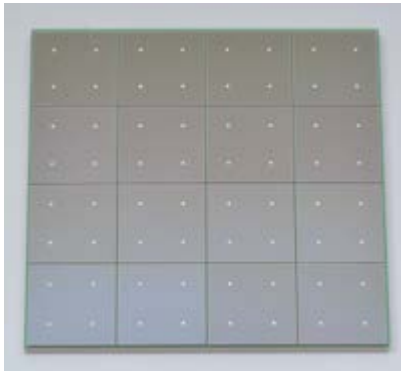
Sensor	NINO*	HRFlexToT*
HPK S13360 (3x3mm ² , 50μm pixel pitch)	160 ps (10V of VoV)	167 ps (10V of VoV)
FBK NUV-HD (4x4mm ² , 40μm pixel pitch)	135 ps (12V of VoV)	142 ps (11.5V of VoV)

***SPTR in terms of FWHM**



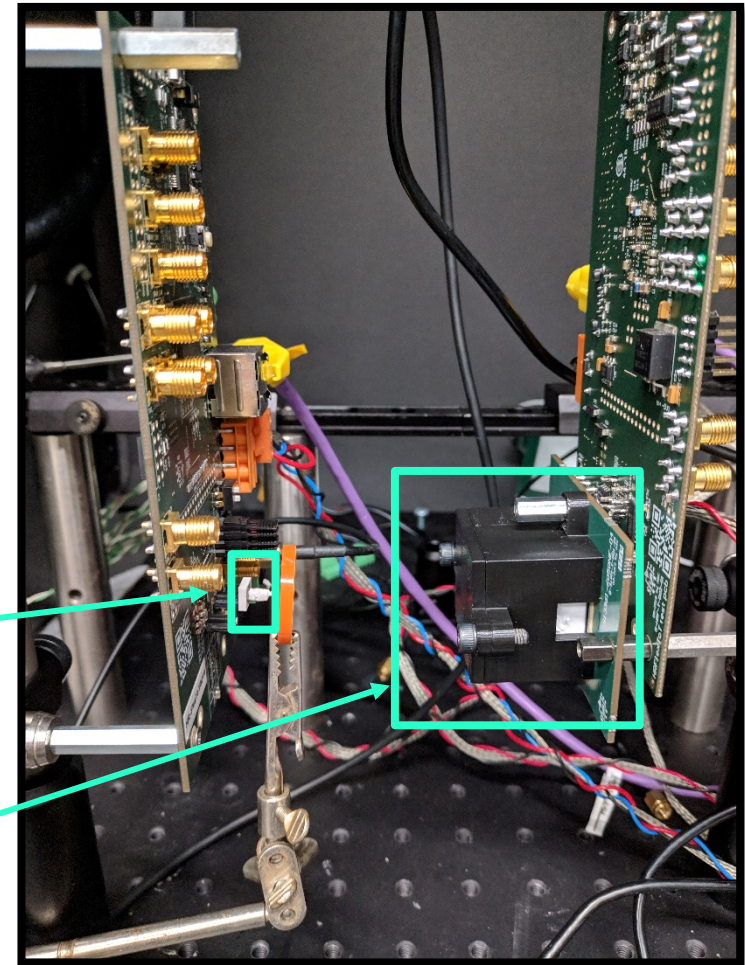
II. MultiCh readout: HRFlexToT: monolithic crystals

- Coincidence Time Resolution (CTR)
- Sensors and crystals involved:
 - Reference detector:
 - LSO:Ce 0.2% Ca $2 \times 2 \times 5 \text{ mm}^3$
 - S13360-3050CS (1 channel, $3 \times 3 \text{ mm}^2$)
 - Jitter = 90 ps FWHM
 - Monolithic crystal:
 - LFS $20 \times 25 \times 25 \text{ mm}^3$ (with Teflon)
 - S13361-6050PE-04 (16 channel, $6 \times 6 \text{ mm}^2$)



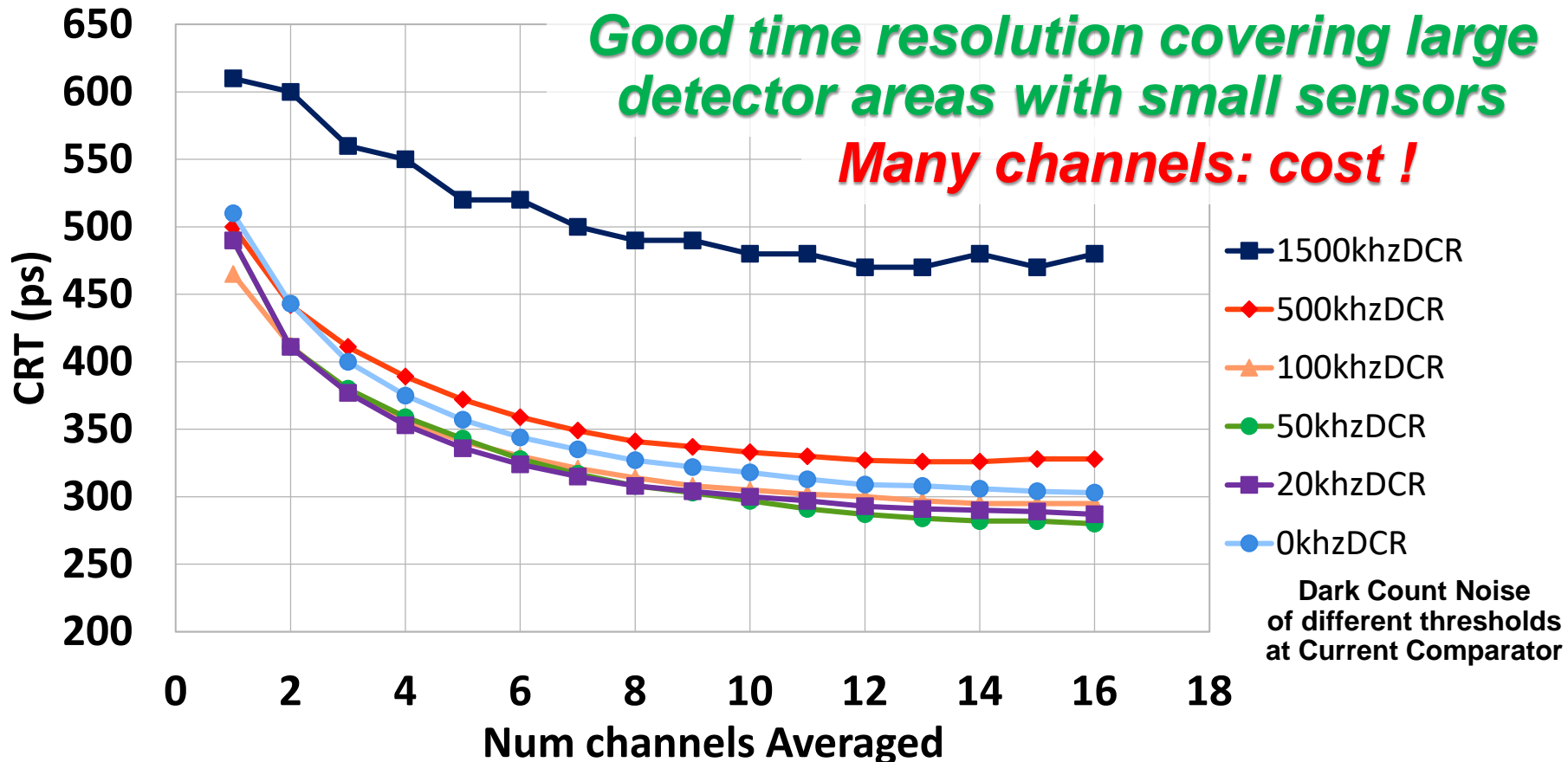
Reference
detector

Monolithic
detector



II. MultiCh readout: HRFlexToT: monolithic crystals

- Time-walk correction + averaging of the N fastest channels.



CTR = 280 ps FWHM → Measured

CTR = 236 ps FWHM → After subtracting TDC and Reference jitter

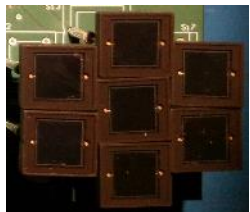
- I. Introduction
- II. Multi-Channel Readout (HRFlexToT)
- III. Active Summation (MUSIC)**
- IV. Towards a Hybrid Single Photon Sensor (FastIC & FastICPix)
- V. Discussion

III. Combining small sensors: active summation: MUSIC

- MUSIC 8 ch ASIC performs single ch or summation

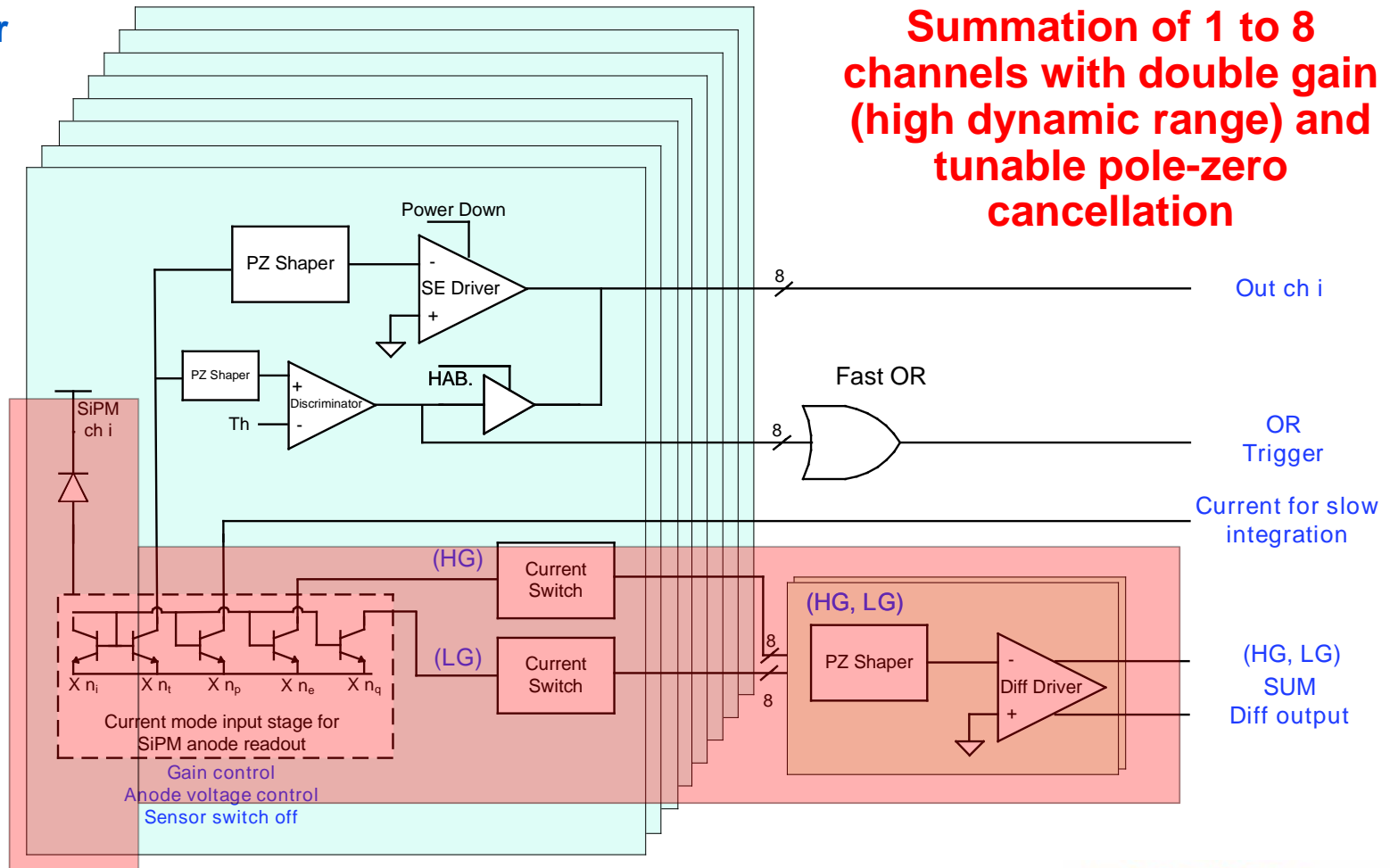
1 x PMT (CTA)

18 mm diameter



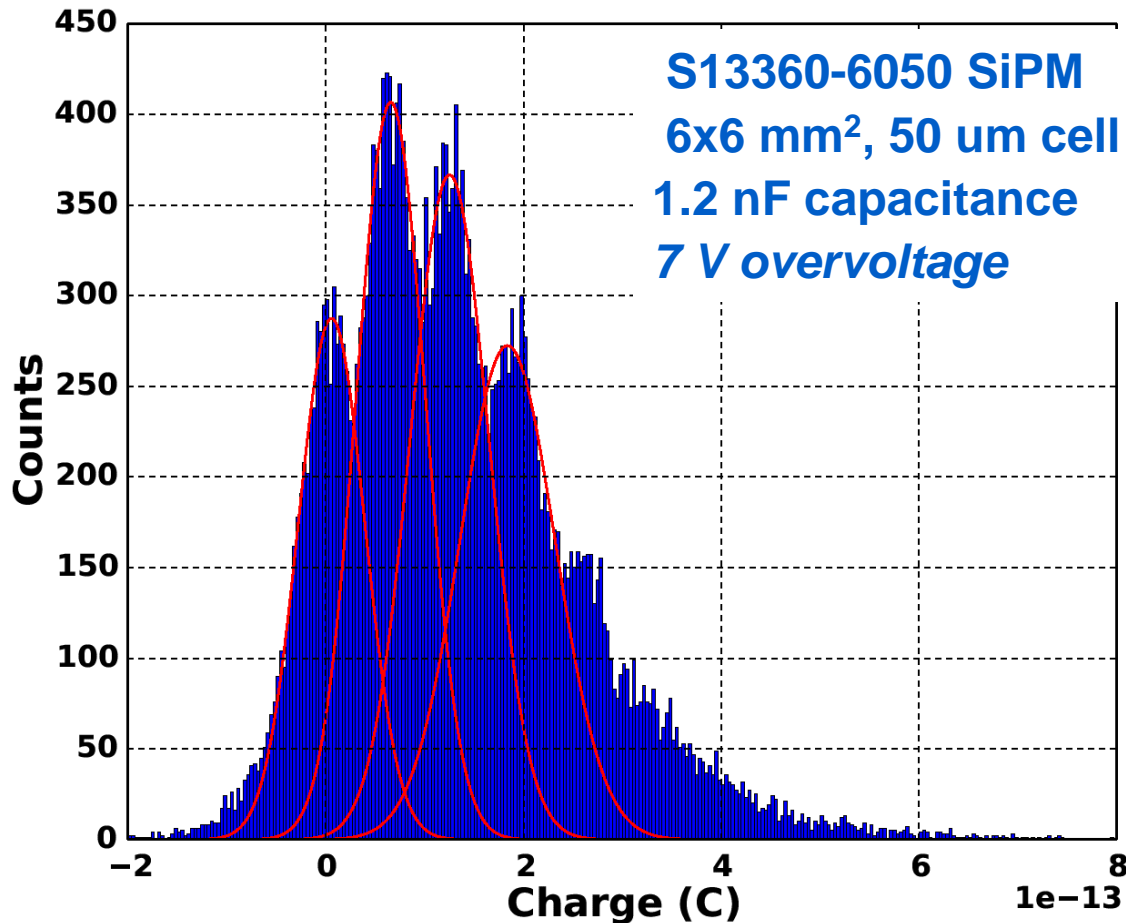
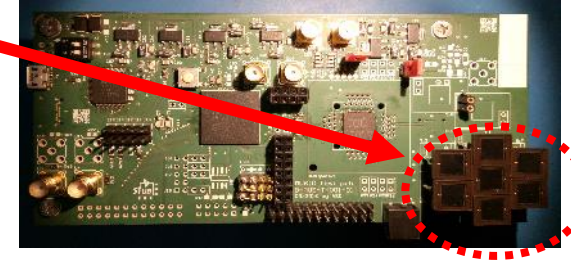
7 x SiPM

6x6 mm² each



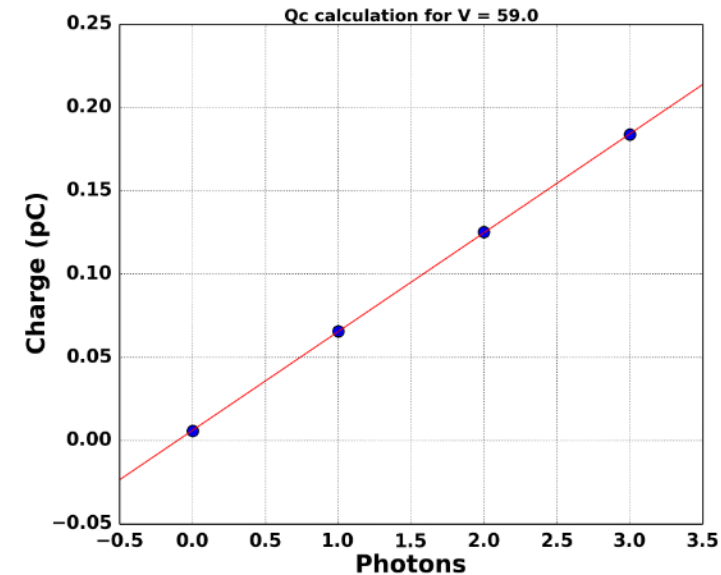
III. Combining small sensors: active summation : MUSIC

- MUSIC configuration: the adder takes 7 channels
 - Noise is much higher ($\sqrt{7}$)
 - But pe (cell) peaks can still be identified
 - Channels have been equalized by MUSIC anode ctrl voltage



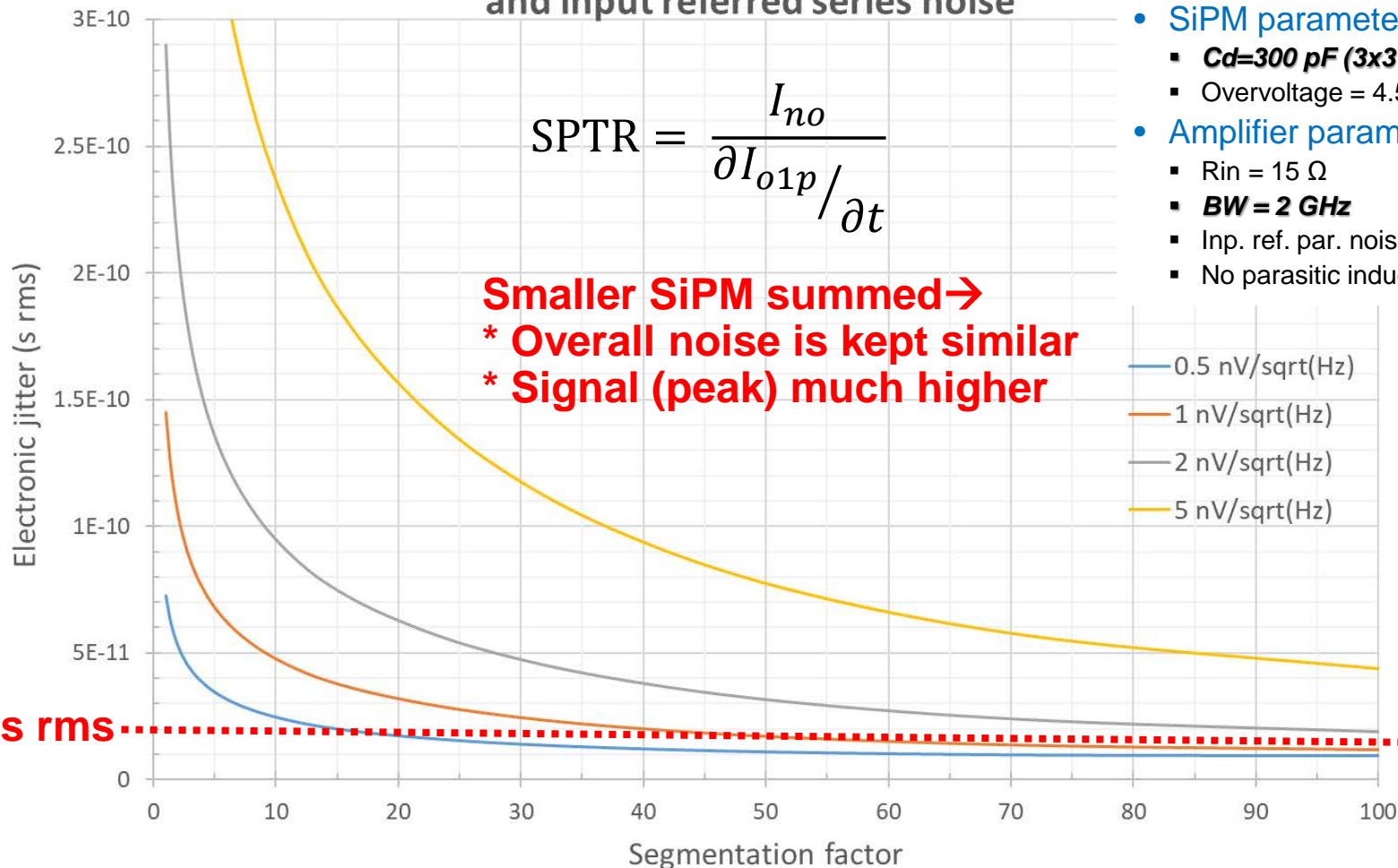
1 x PMT
 18 mm diameter

≈
 7 x SiPM
 6x6 mm²
 each



III. Combining small sensors: active summation

Electronic jitter as function of sensor segmentation and input referred series noise



- SiPM parameters
 - Cd=300 pF (3x3 mm² device)
 - Overvoltage = 4.5 V
- Amplifier parameters
 - Rin = 15 Ω
 - BW = 2 GHz
 - Inp. ref. par. noise: 10 pA/sqrt(Hz)
 - No parasitic inductance

- I. Introduction
- II. Multi-Channel Readout (HRFlexToT)
- III. Active Summation (MUSIC)
- IV. Towards a Hybrid Single Photon Sensor (FastIC & FastICPix)**
- V. Discussion

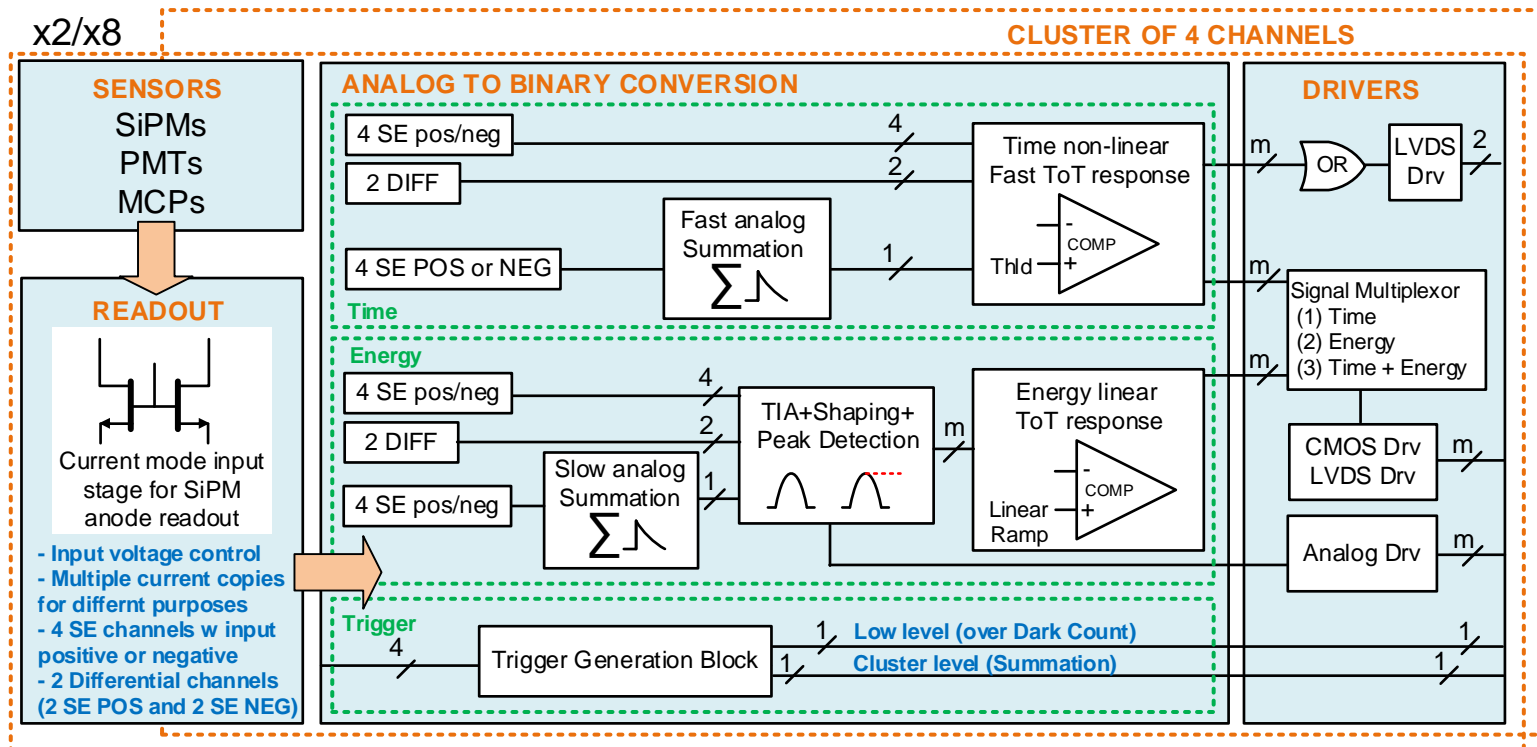
IV. Towards a picosecond hybrid single photon sensor

First step: 65 nm FE: FastIC

- FASTIC current mode ASIC.

- **8 Inputs:** 8 Single Ended (positive or negative) or 4 differential.
- **4/8 Outputs:** CMOS, LVDS and Analog.
- Summation in clusters of 4 channels.
- Energy: Linear Time over Threshold with high dynamic range.
- Different trigger levels and cluster trigger for monolithic crystals.

- First submission scheduled for end 2019: 8 ch
- Final chip: 32 channel (< 10 mW/ch)



IV. Towards a picosecond hybrid single photon sensor

First step: 65 nm FE: FastIC Performance

- Preliminary simulation benchmarking of FASTIC:

Sensor	Jitter (ps), sigma, 1 firing cell							
	Over-voltage	NINO	HRFlexToT	S.E. (+)	S.E. (-)	DIFF	SUM4 (+)	SUM4 (-)
		Extrapolation	Simulation					
LCT4, 50 um, 3600 cells	4,5	31,12	39	25	38,6	22	-	-
LCT5 S13360, 50 um, 3600 cells	4,5	29,86	37,42	31	29	23,6	20,8	18,9
	7	17,96	22,5	18,5	18	15	13,3	12,8
	10	11,81	14,8	13,5	13	11	9	8

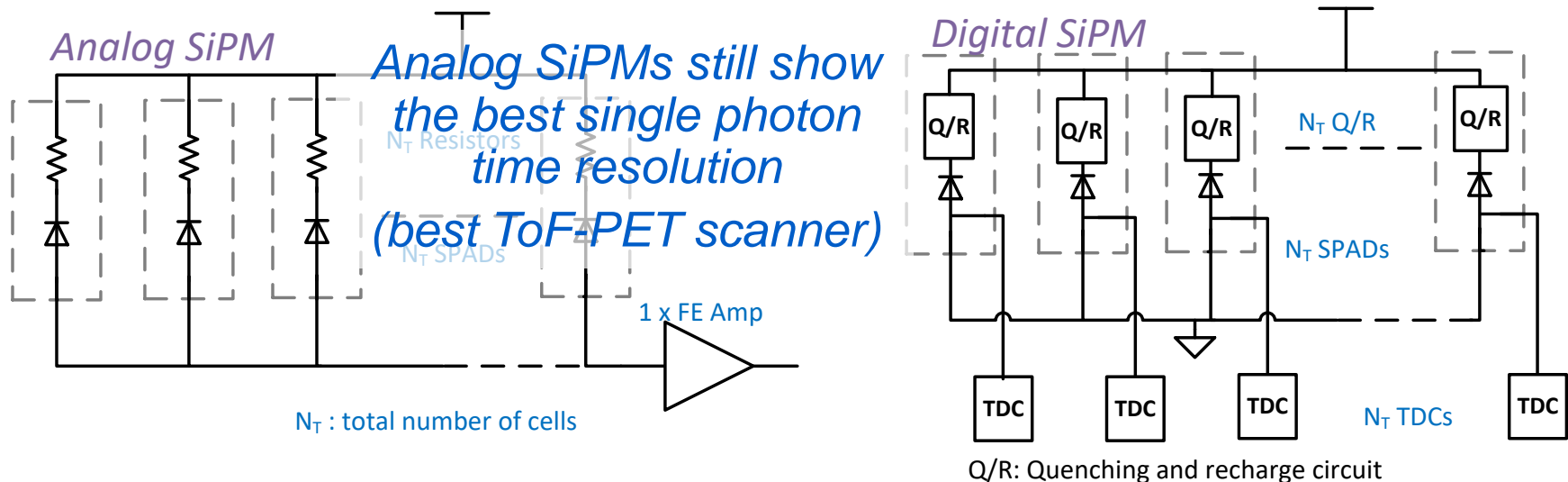
- FASTIC in DIFF and SUM modes should outperform NINO:

Sensor	Jitter (ps), sigma, 1 firing cell							
	Over-voltage	NINO	HRFlexToT	S.E. (+)	S.E. (-)	DIFF	SUM4 (+)	SUM4 (-)
		Extrapolation	Simulation					
LCT4, 50 um, 3600 cells	4,5	0	20,20	-24,49	19,37	-41,47	-	-
LCT5 S13360, 50 um, 3600 cells	4,5	0	20,20	3,67	-2,97	-26,53	-43,57	-58,00
	7	0	20,20	2,94	0,25	-19,70	-35,00	-40,28
	10	0	20,20	12,51	9,15	-7,37	-31,23	-47,64

IV. Towards a picosecond hybrid single photon sensor

Analog versus Digital SiPMs

- Digital SiPMs might overcome C limitation, however:
 - Limited fill factor due to in pixel electronics
 - Solved by 3D integration ?
 - Power and resource hungry:
 - 1 TDC per pixel or complex TDC sharing
 - Digital FE and trigger network jitter



Pros

- Simplicity
- High Fill Factor (PDE)

Cons

- Large capacitance degrades timing
- Xtalk degrades timing

Pros

- Individual photon timing available

Cons

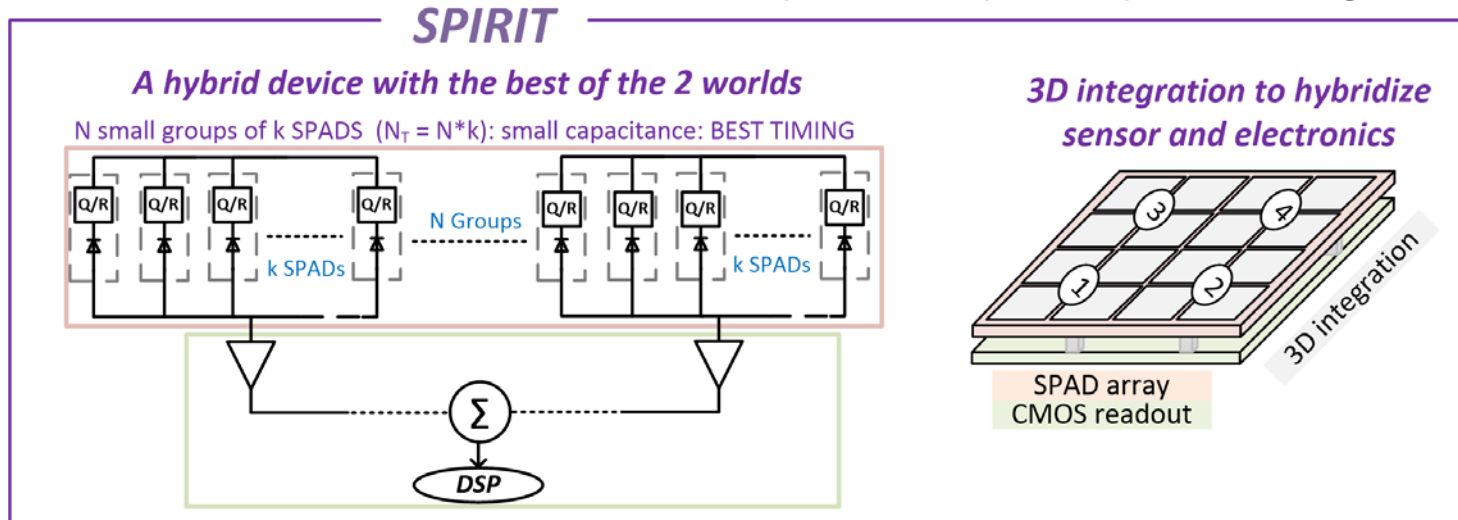
- Complexity (cost and power)
- Fill factor degradation
- Xtalk degrades timing

IV. Towards a picosecond hybrid single photon sensor

Hybrid sensor and FastICPix ASIC

- **New hybrid sensor**

- Idea is to **combine actively the signal of small micropixel sub-arrays**
- Based on the **best** single photon **sensor technologies**
- With **ultrafast readout** electronics (≤ 65 nm) using **3D integration**



- First step ongoing: approved **ATTRACT H2020 project**
 - Phase-I: FastICPix: validate our approach in a full system simulation
- **Unprecedented time resolution** for large sensitive areas
 - Active summation and combination of small groups of SPADs which show a capacitance **two orders of magnitude lower than aSiPMs**

- I. Introduction
- II. Multi-Channel Readout (HRFlexToT)
- III. Active Summation (MUSIC)
- IV. Towards a Hybrid Single Photon Sensor (FastIC & FastICPix)
- V. Discussion**

V. Discussion

- The performance of SiPM as large area detector is still hampered by huge detector capacitance
 - Particularly for fast shaping times or to achieve the ultimate timing
- Improving FE electronics may help
 - Lower noise and input impedance, higher bandwidth, etc
 - But the margin of improvement is limited:
 - Noise: power increase, thermal noise of passives, common-mode
 - Bandwidth: interconnection parasitics (RLC) imposes a limit in the effective BW or Slew rate one can achieve with large sensors
- Sensor segmentation and active summation can help
 - Encouraging results with **MUSIC chip** and good prospects with **FastIC**
- A hybrid single photon sensor may approach the limit SPTR
 - Large segmentation: capacitance “reduced” by 2 orders of magnitude
 - Parasitics won’t limit the bandwidth anymore
 - Compatible with alternative technologies: pixelated MCPs, tynodes, etc

Thanks a lot for your attention !!!

Questions ?

sgomez@fqa.ub.edu



UNIVERSITAT DE
BARCELONA

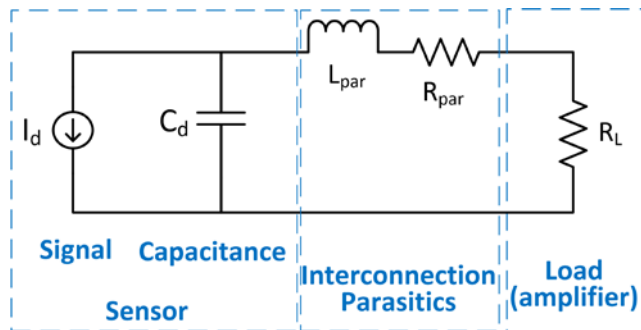
Back up Sergio

I. Introduction

• Single Photon Sensors

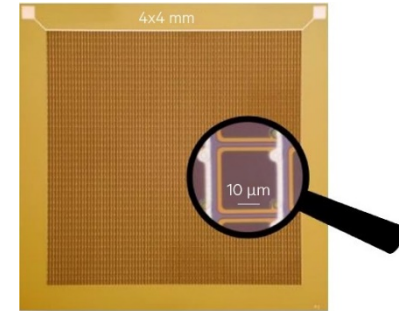
Vacuum Photomultipliers

- $G = 10^5 - 10^7$
- $C_d \sim 10 \text{ pF}$
- $L \sim 10 \text{ nH}$
- $\text{SPTR}^* < 50 \text{ ps FWHM}$
 - MCP-PMTs

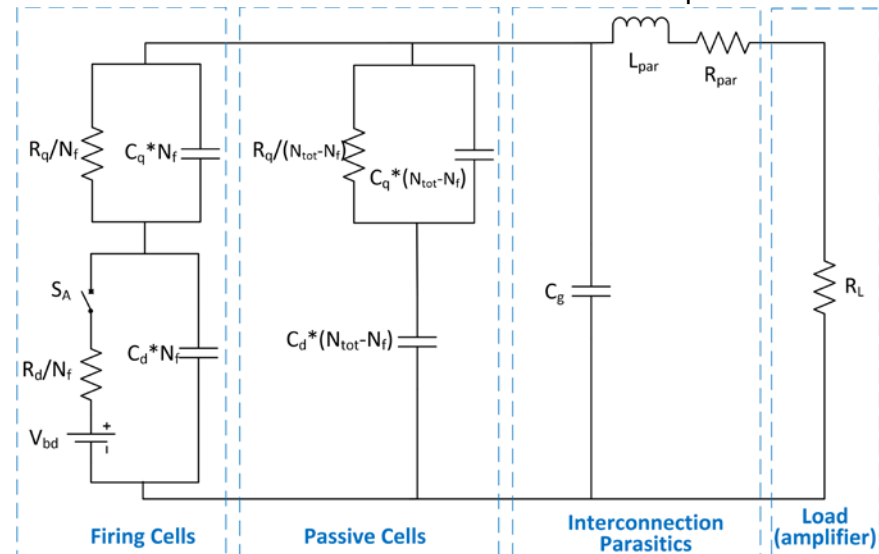


Silicon Photomultipliers

- $G^* = 10^5 - 10^7$
- C_d from 10 pF to 1 nF
- $L \sim 10 \text{ nH}$
- $\text{SPTR} < 100 \text{ ps FWHM}$
 - Small SiPMs



www.azosensors.com/article.aspx?ArticleID=865



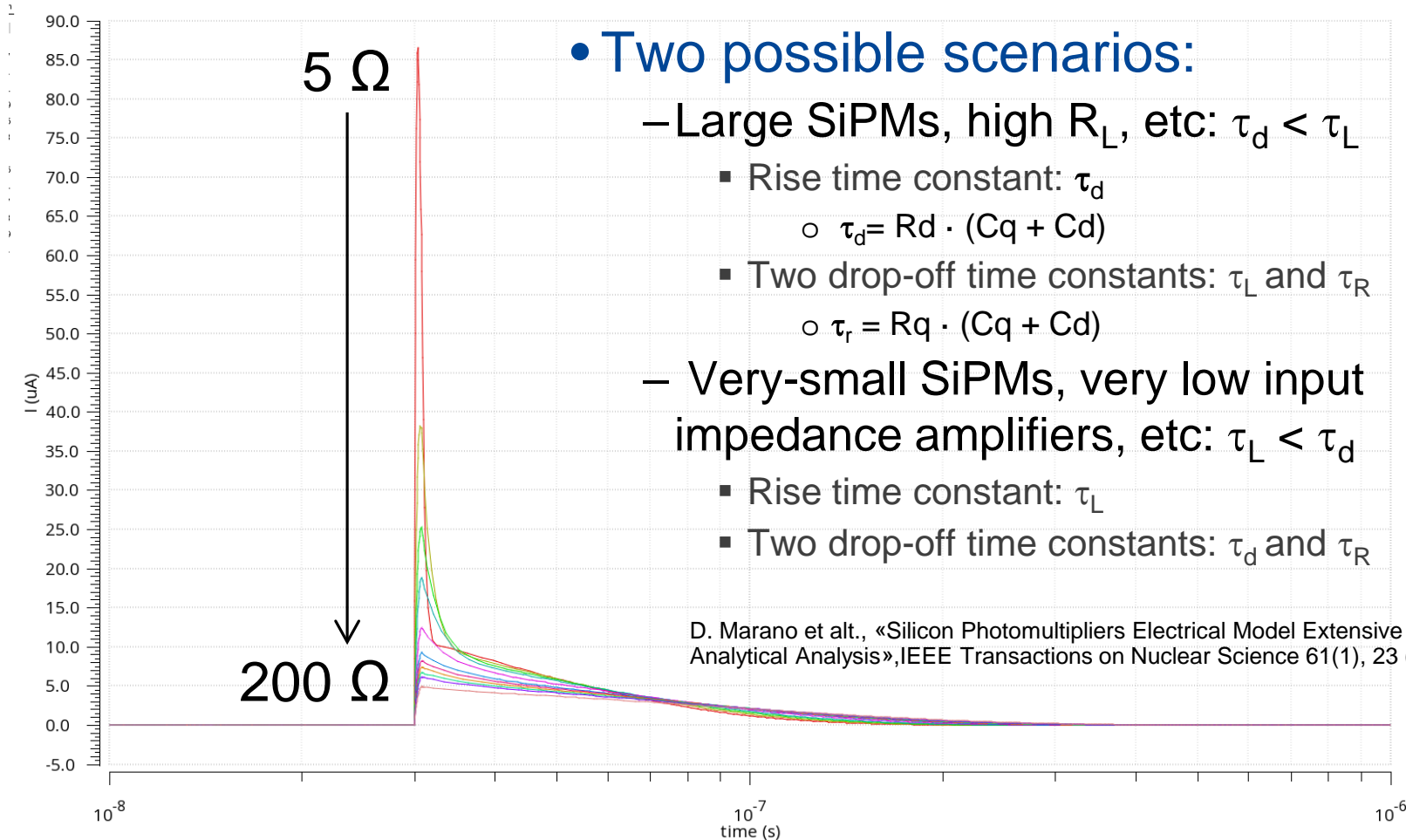
* SPTR: Single Photon Time Resolution

* Effective SiPM gain is not obvious: strong dependence on shaping times

I. Introduction

Load impedance (current)

- The signal (load current) shape and amplitude depends on R_L
 - Peak current signal is inversely proportional to R_L



- Two possible scenarios:

- Large SiPMs, high R_L , etc: $\tau_d < \tau_L$

- Rise time constant: τ_d

- $\tau_d = R_d \cdot (C_q + C_d)$

- Two drop-off time constants: τ_L and τ_R

- $\tau_r = R_q \cdot (C_q + C_d)$

- Very-small SiPMs, very low input impedance amplifiers, etc: $\tau_L < \tau_d$

- Rise time constant: τ_L

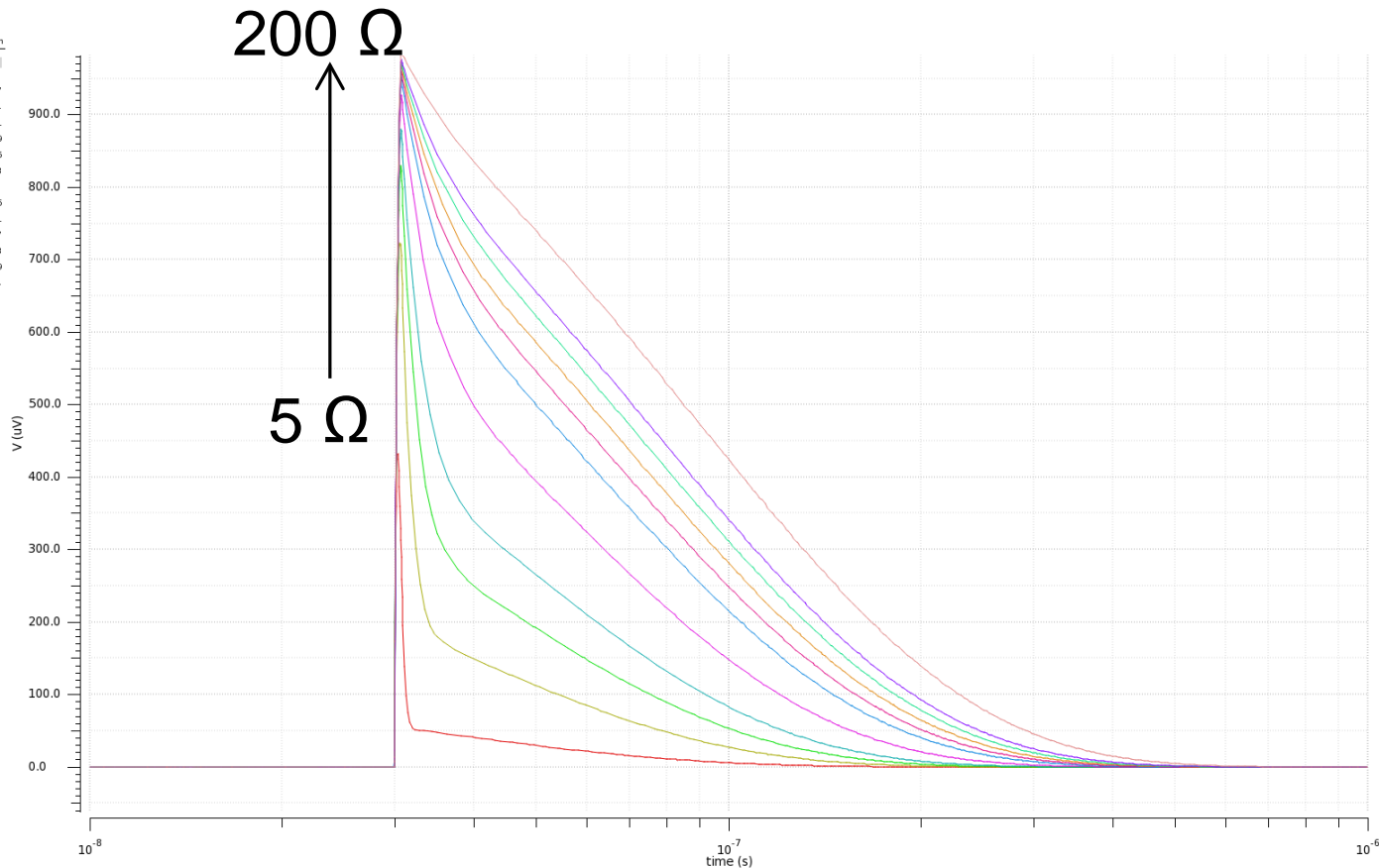
- Two drop-off time constants: τ_d and τ_R

D. Marano et al., «Silicon Photomultipliers Electrical Model Extensive Analytical Analysis», IEEE Transactions on Nuclear Science 61(1), 23 (2014).

I. Introduction

Load impedance (voltage)

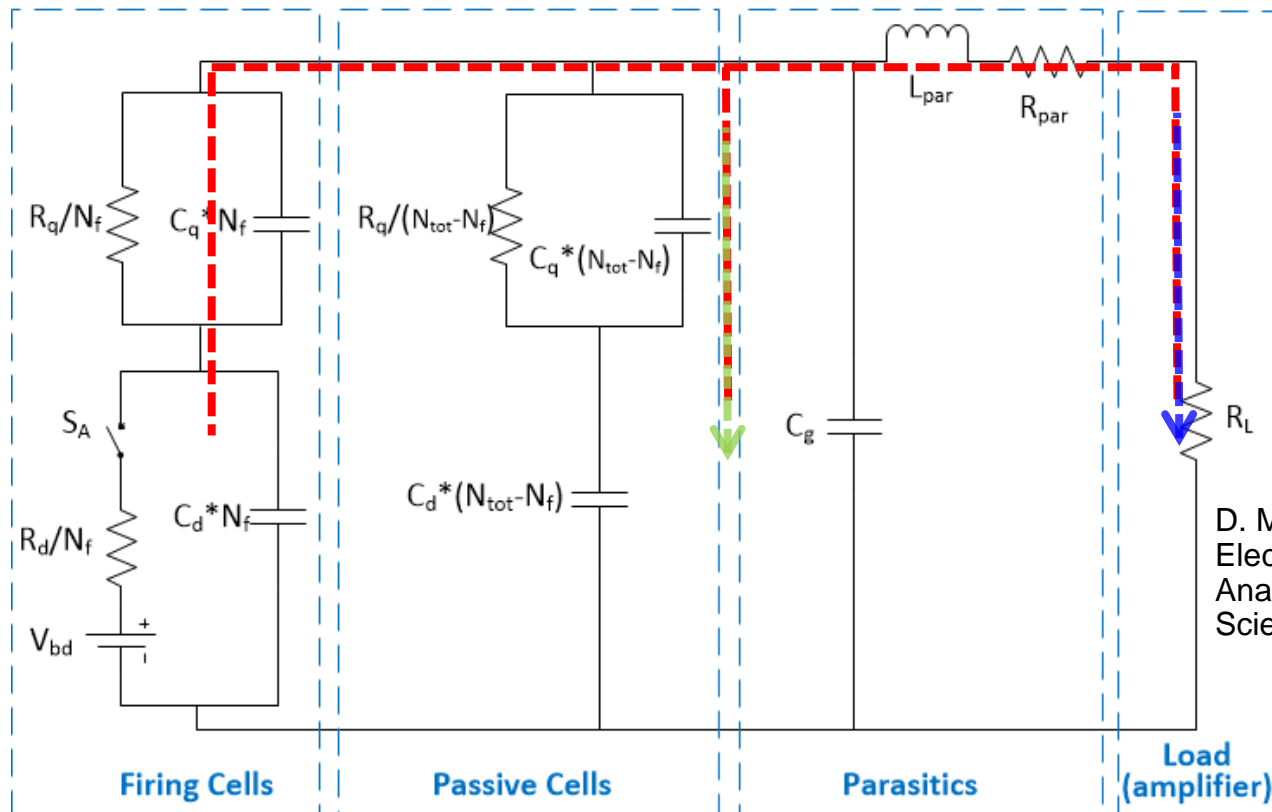
- If we sense the load voltage the situation is a little bit different:
 - Shape also depends on R_L (long drop-off times for large R_L)
 - But now the signal increases with R_L (particularly for low τ_L)



I. Introduction

Parasitic Capacitance effect

- Passive cells and load impedance form a low pass filter !
 - We are sensing the current or the voltage on R_L
 - Passive cells and parasitic capacitances create a current divider with R_L
 - Peak signal goes with C_{par}^{-1}



**Low pass filter
time constant:**

$$\tau_L \approx R_L \cdot N_{tot} \cdot (C_q // C_d)$$

** Approximate: we should include also C_g here*

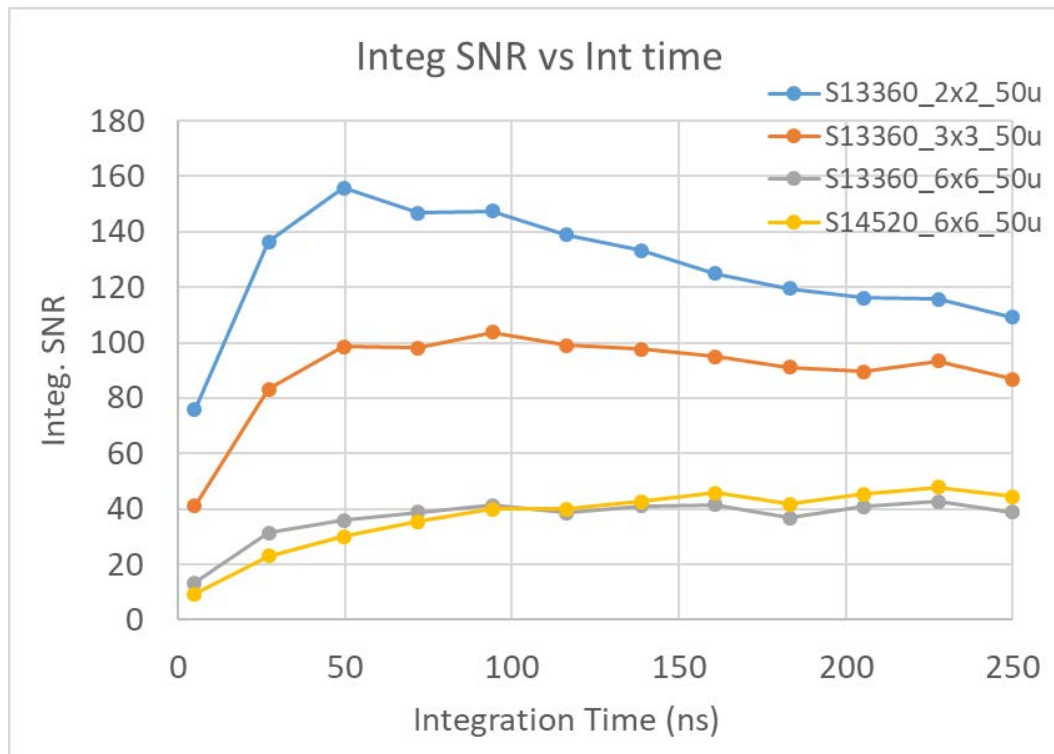
D. Marano et al., «Silicon Photomultipliers Electrical Model Extensive Analytical Analysis», IEEE Transactions on Nuclear Science 61(1), 23 (2014).

I. Introduction

Integration (shaping) time

- Longer shaping times help to dramatically improve SNR of the charge spectrum
 - Application dependent !
- Longer shaping times:
 - Increase signal collection (important for large devices)
 - Mitigates the effect of series noise
 - Warning: but increases the effect of parallel noise (important for small devices)!

- SiPM overvoltage: 4.5 V
- Amplifier parameters
 - $R_{in} = 15 \Omega$
 - BW = 500 MHz
 - Inp. ref. ser. noise: 2 nV/sqrt(/Hz)
 - Inp. ref. par. noise: 10 pA/sqrt(/Hz)

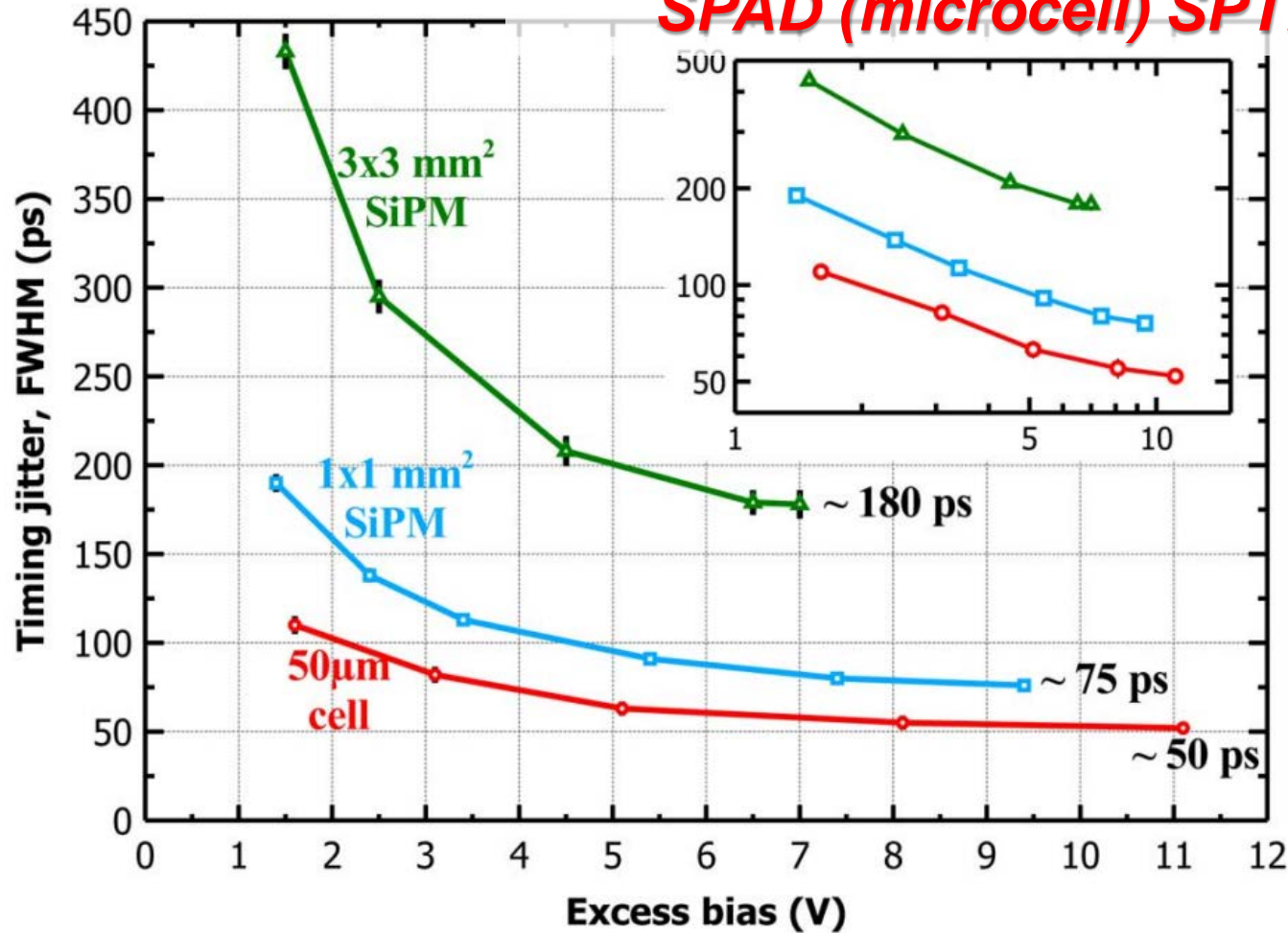


Disclaimer: increasing integration time may not work because of DCR, correlated noise, pile-up...

I. Introduction

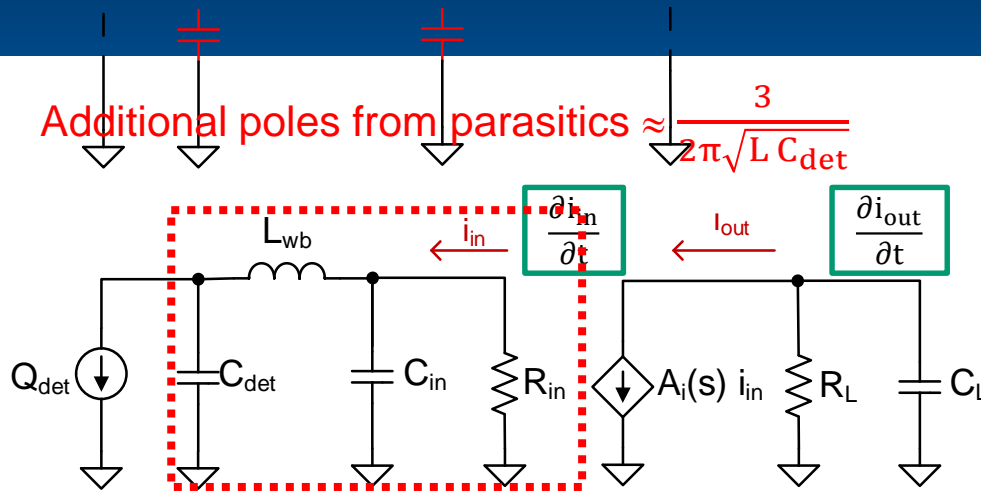
Large area SiPMs: jitter

SiPM technology is still far from the SPAD (microcell) SPTR limit



F. Acerbi *et al.*, "Characterization of Single-Photon Time Resolution: From Single SPAD to Silicon Photomultiplier," in *IEEE Transactions on Nuclear Science*, vol. 61, no. 5, pp. 2678-2686, Oct. 2014.

I. Introduction: Effect of interconnects (inductance)

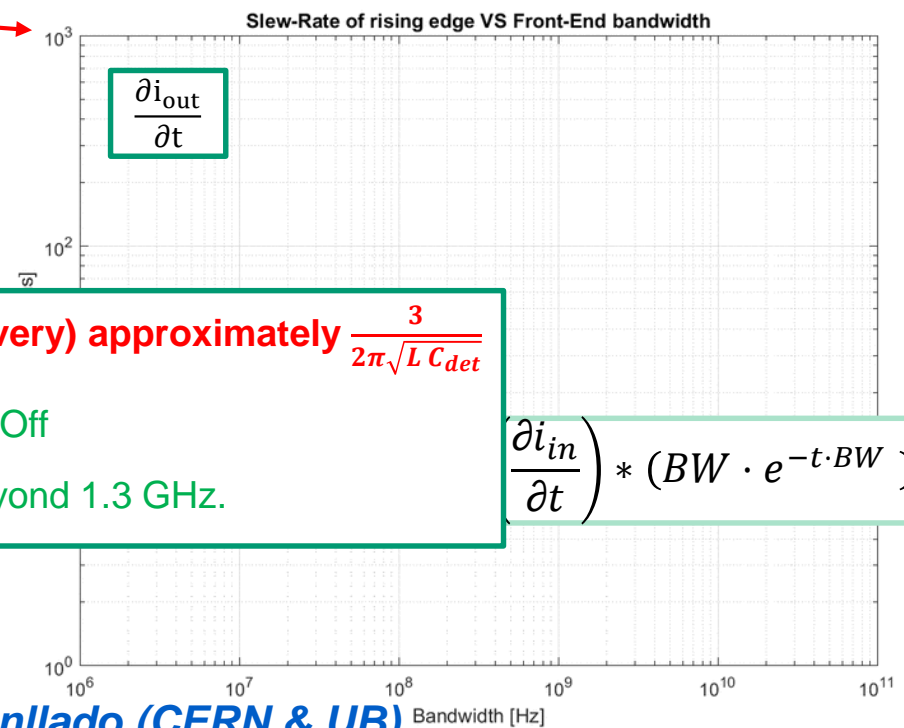
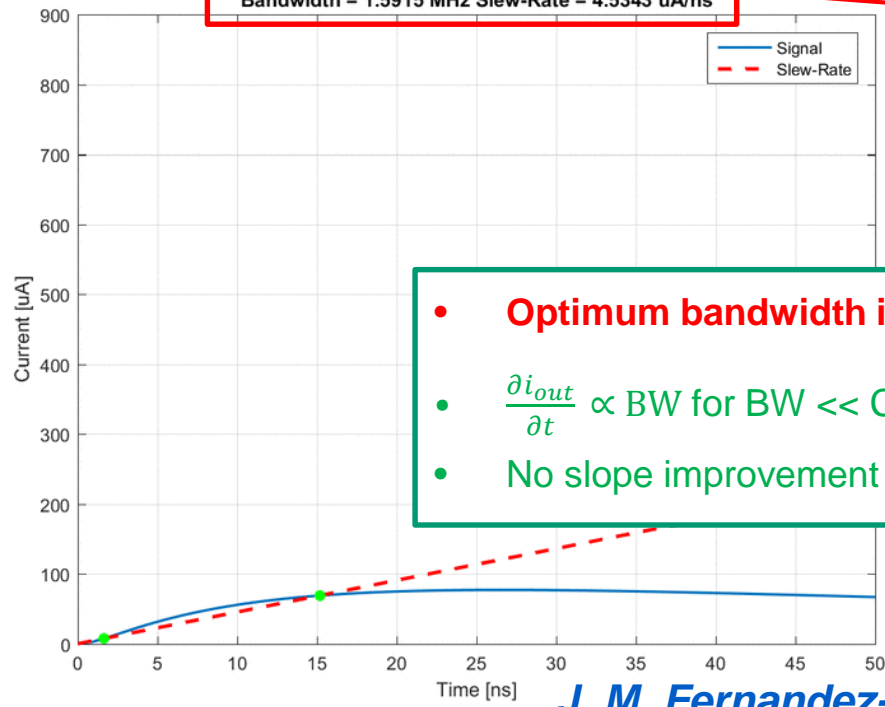


Optimum bandwidth for connecting inductances
5 nH – 25 nH and Cdet = 10 pF – 1 nF

Lwb / Cdet

5 nH / 10 pF	5 nH / 1 nF	25 nH / 10 pF	25 nH / 1 nF
1.3 GHz	270 MHz	560 MHz	81 MHz

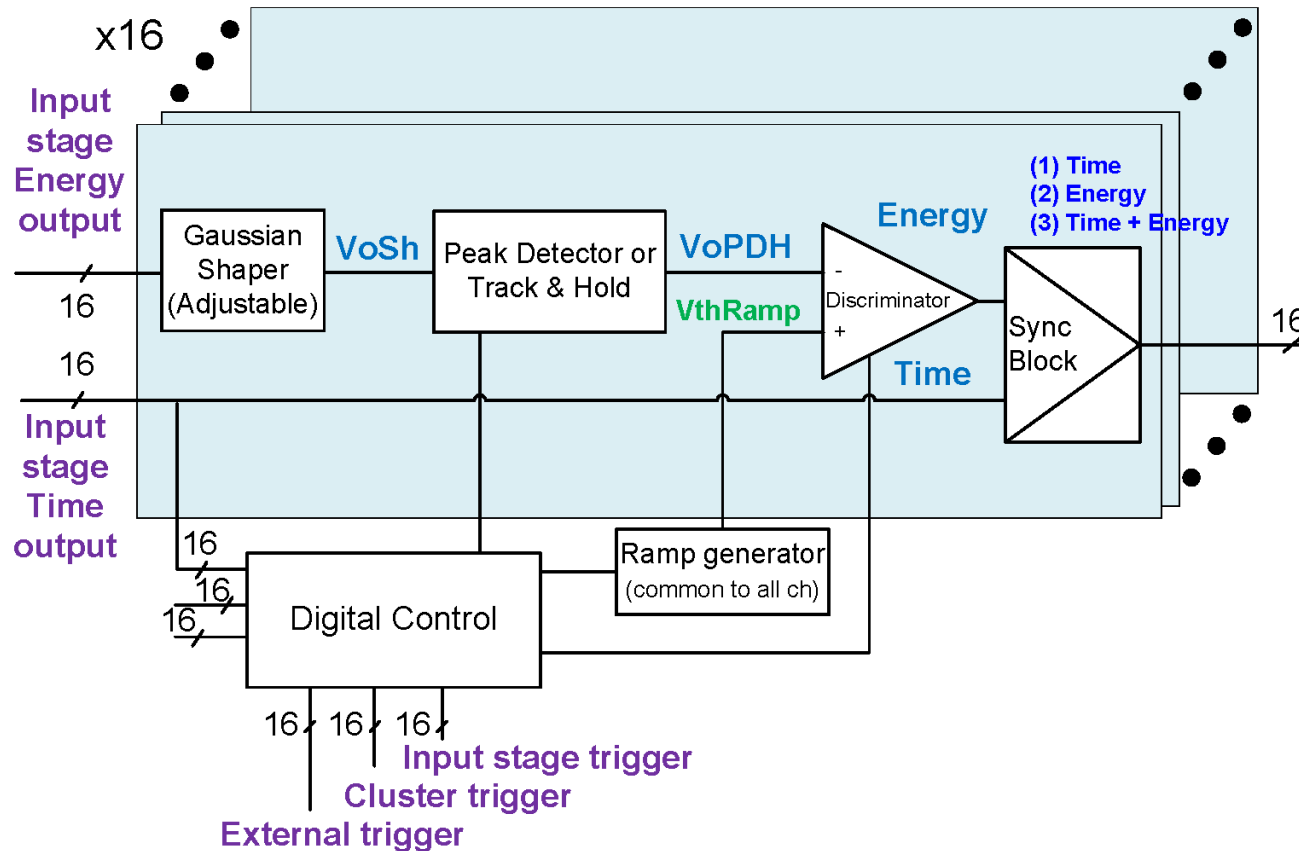
Bandwidth = 1.5915 MHz Slew-Rate = 4.5343 uA/ns



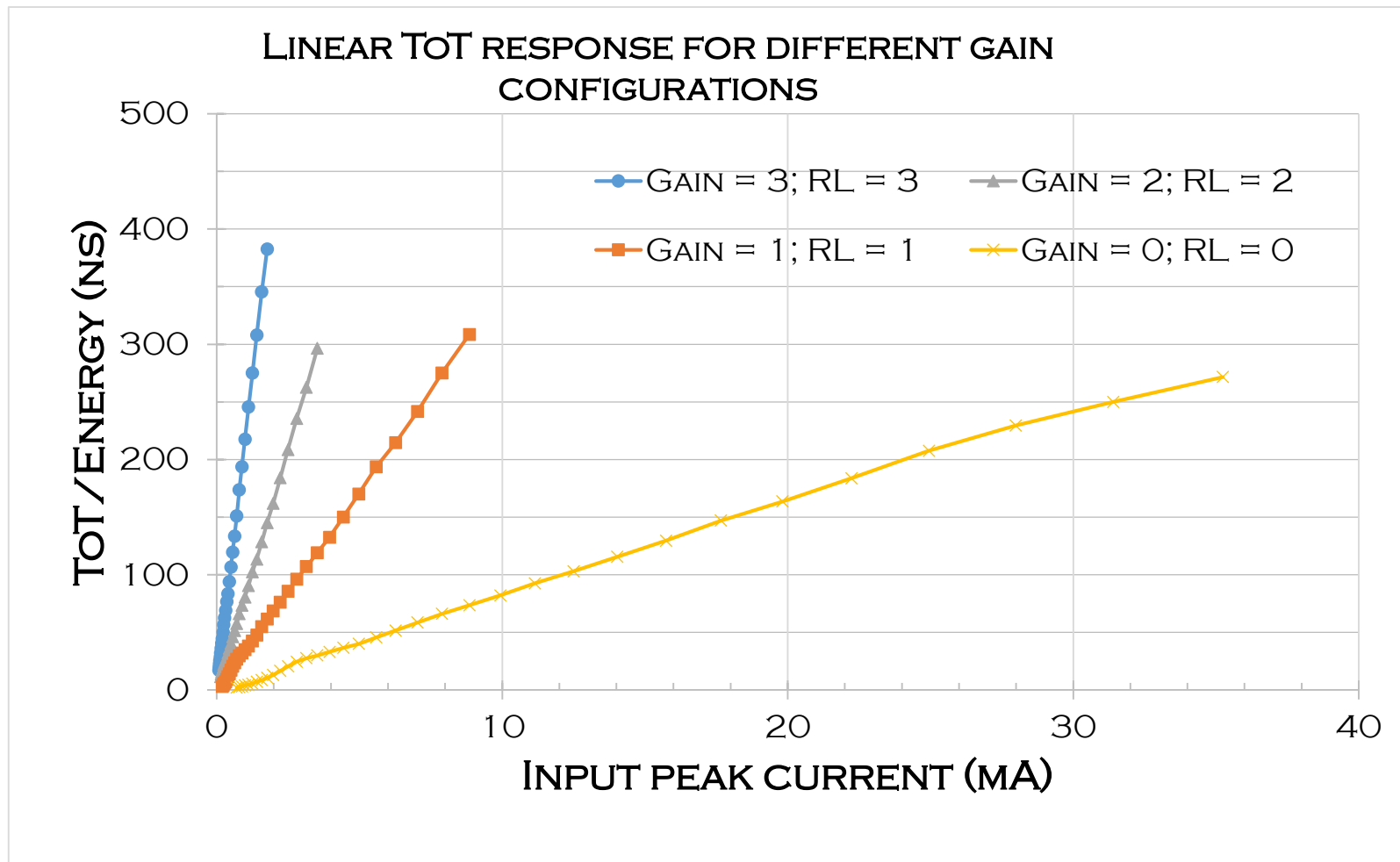
- Optimum bandwidth is (very) approximately $\frac{3}{2\pi\sqrt{L C_{det}}}$
- $\frac{\partial i_{out}}{\partial t} \propto BW$ for $BW \ll \text{Cut-Off}$
- No slope improvement beyond 1.3 GHz.

II. MultiCh readout: HRFlexToT

- Linear energy readout

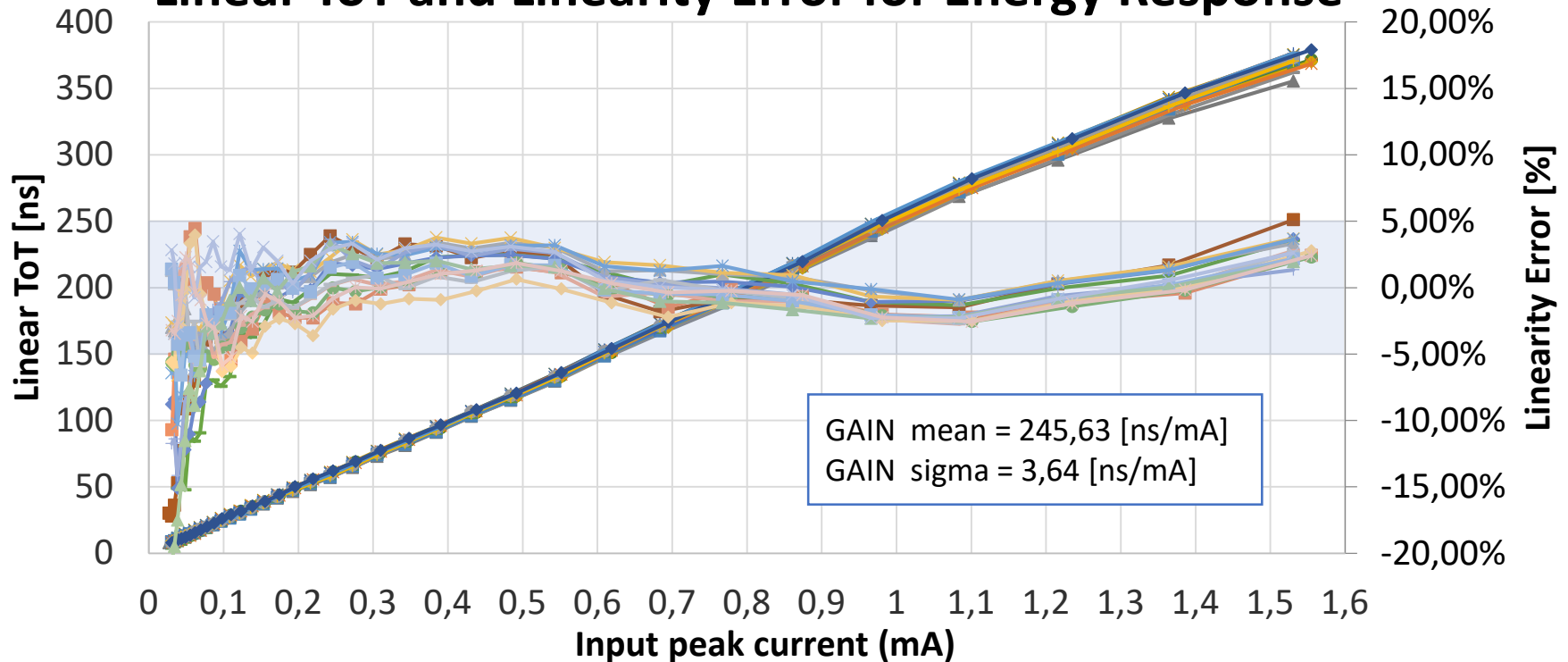


II. MultiCh readout: HRFlexToT



II. MultiCh readout: HRFlexToT

Linear ToT and Linearity Error for Energy Response



- | | | | | | |
|----------------------|----------------------|----------------------|----------------------|----------------------|----------------------|
| ▲ Energy Ch 0 | ✕ Energy Ch 1 | ✱ Energy Ch 2 | ● Energy Ch 3 | ◆ Energy Ch 4 | ■ Energy Ch 5 |
| — Energy Ch 6 | ◆ Energy Ch 7 | ■ Energy Ch 8 | ▲ Energy Ch 9 | ✱ Energy Ch 10 | ✕ Energy Ch 11 |
| ● Energy Ch 12 | ✕ Energy Ch 13 | — Energy Ch 14 | ◆ Energy Ch 15 | ■ Error Energy Ch 0 | ▲ Error Energy ch 1 |
| ◆ Error Energy Ch 2 | ■ Error Energy Ch 3 | ▲ Error Energy Ch 4 | ✕ Error Energy Ch 5 | ✱ Error Energy Ch 6 | ● Error Energy Ch 7 |
| ✱ Error Energy Ch 8 | — Error Energy Ch 9 | — Error Energy Ch 10 | ◆ Error Energy Ch 11 | ■ Error Energy Ch 12 | ▲ Error Energy Ch 13 |
| ✱ Error Energy Ch 14 | — Error Energy Ch 15 | | | | |

III. Combining small sensors (passive)

- Alternative to obtain large area sensors:
 - Parallel connection: large capacitance !
 - Series (simple or hybrid) connection: lower capacitance (but lower signal):
 - Useful for reducing sensor recovery time not so clear for SNR (depends on amplifier Z_{in})

✓ **Two options for series connection**

	Simple	Hybrid
Bias	280V (×4 segmented) ☹️	70V (common) 😊
Gain uniformity	Automatic gain equalization 😊	Required ☹️
Potential diff. bw/ adjacent segments	~70V ☹️	0V 😊
External circuit	No 😊	Required ☹️

→ **Both work!**
But “hybrid” is more advantageous.

Simple

Hybrid (signal: series, bias: parallel)

W. Ootani, “SiPM, Status and Perspectives”, Special Workshop on Photon Detection with MPGDs, June 10-11, 2015 CERN

III. Combining small sensors: active summation

• Why active summation?

- Assume a large sensor is divided in m smaller subunits
 - $C_{largeDet} = m \cdot C_{smallDet}$
- Total noise for active and passive summation can be similar

- If series noise dominates

- $\sigma_{n,largeDet} \sim \sqrt{m} \cdot \sigma_{n,smallDet}$

- If we sum m small detectors

- $\sigma_{n,m \cdot smallDet} \sim \sqrt{m} \cdot \sigma_{n,smallDet}$

- Thus

- $\sigma_{n,m \cdot smallDet} \sim \sigma_{n,largeDet}$ \longrightarrow

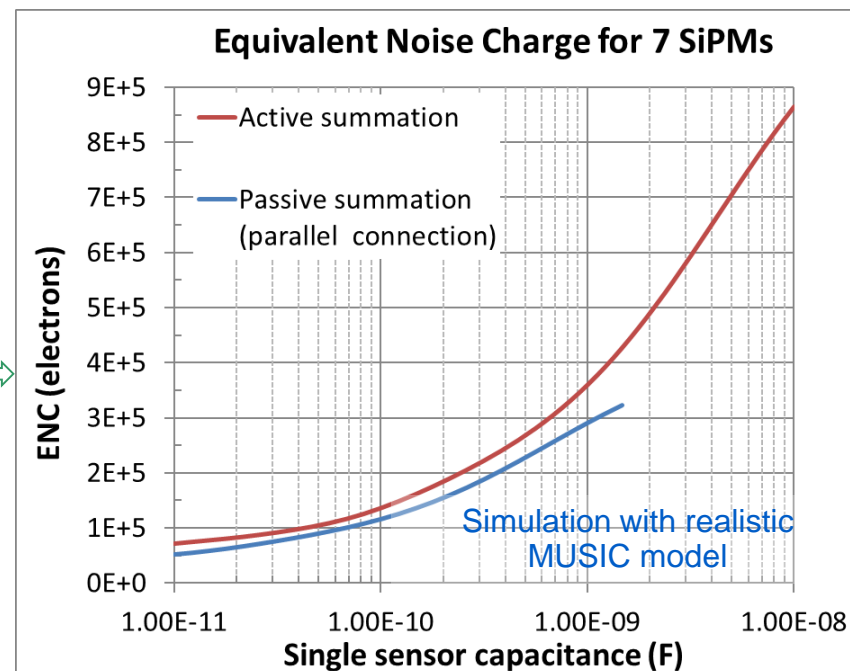
- But signal (peak) much higher!

- Peak signal goes with C^{-1}

- $S_{pk,m \cdot smallDet} \sim m \cdot S_{pk,largeDet}$

- Summation circuit requirements:

- Low noise and high BW



III. Combining small sensors: active summation : MUSIC

- SHIP experiment is a new general-purpose beam dump facility at the SPS (CERN) to search for hidden particles
 - Predicted by a very large number of recently elaborated models of Hidden
 - Dark matter, neutrino oscillations, and the origin of the full baryon asymmetry



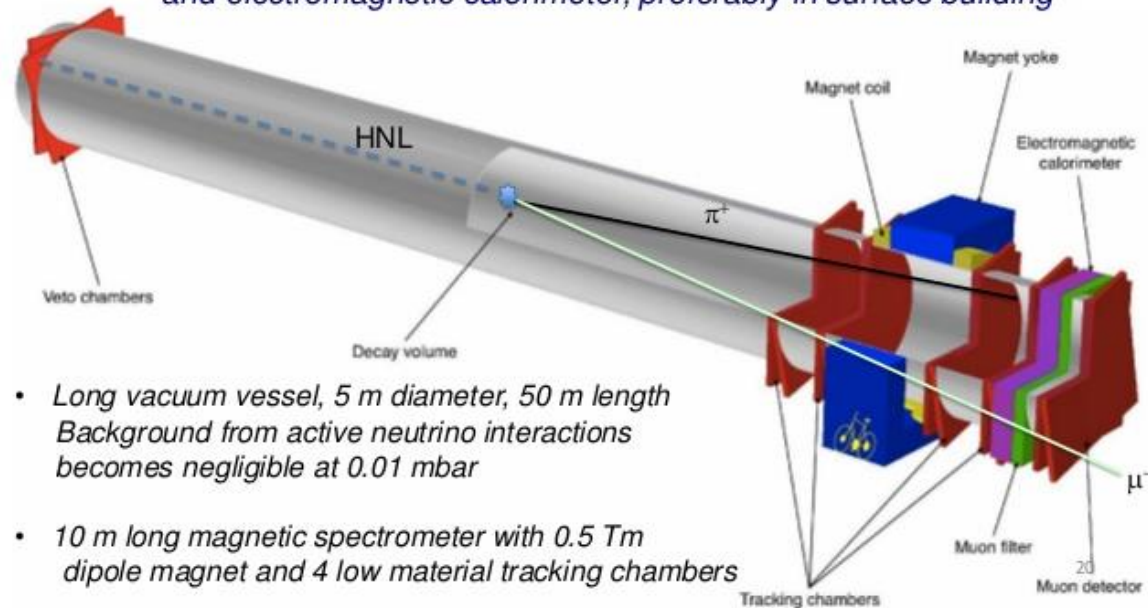
SHiP

Search for Hidden Particles



- *Reconstruction of the HNL decays in the final states: $\mu^- \pi^+$, $\mu^- \rho^+$ & $e^- \pi^+$*

↳ *Requires long decay volume, magnetic spectrometer, muon detector and electromagnetic calorimeter, preferably in surface building*

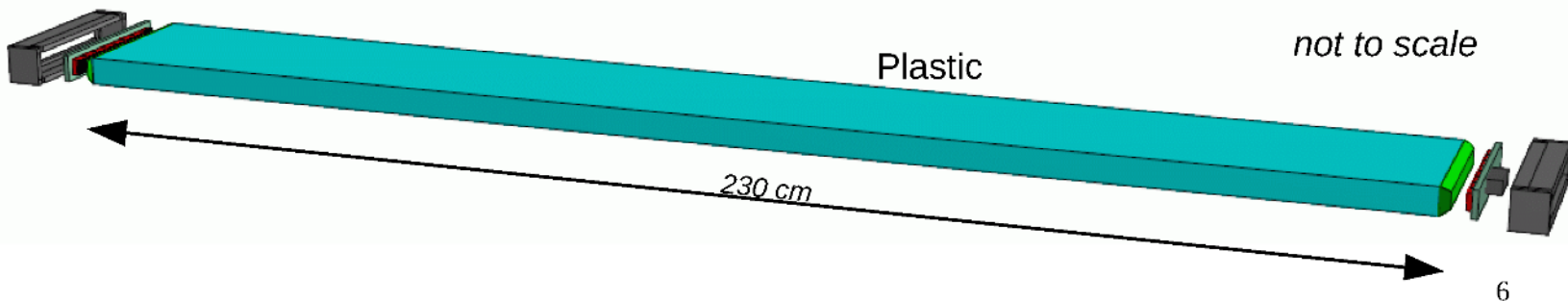
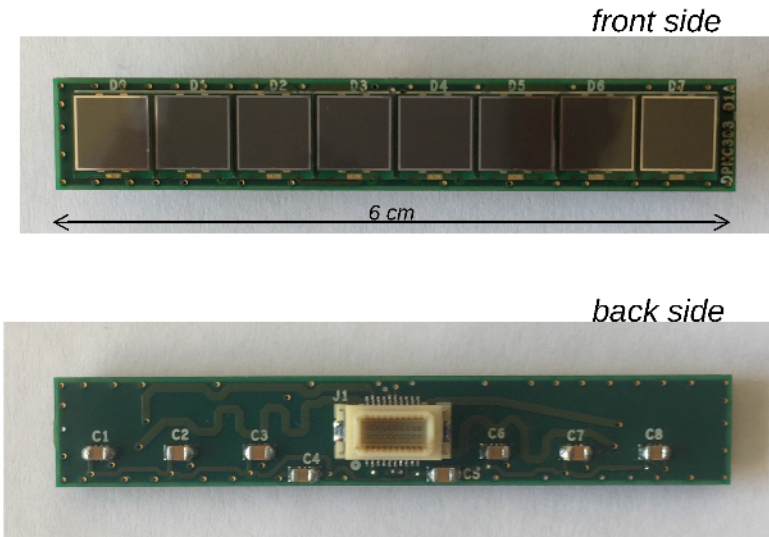


- *Long vacuum vessel, 5 m diameter, 50 m length
Background from active neutrino interactions becomes negligible at 0.01 mbar*
- *10 m long magnetic spectrometer with 0.5 Tm dipole magnet and 4 low material tracking chambers*

III. Combining small sensors: active summation : MUSIC

Bar and sensors for ToF/ND280

- Bar: 230 cm x 6 cm x 1 cm
- Plastic material:
 - EJ200 (BC408) or EJ208(BC412)
 - Attenuation length ~ 4 m
 - 1.42 kg/bar
- Readout from both ends
 - 8 sensors of 6 mm x 6 mm
 - Example: S13360-6050PE

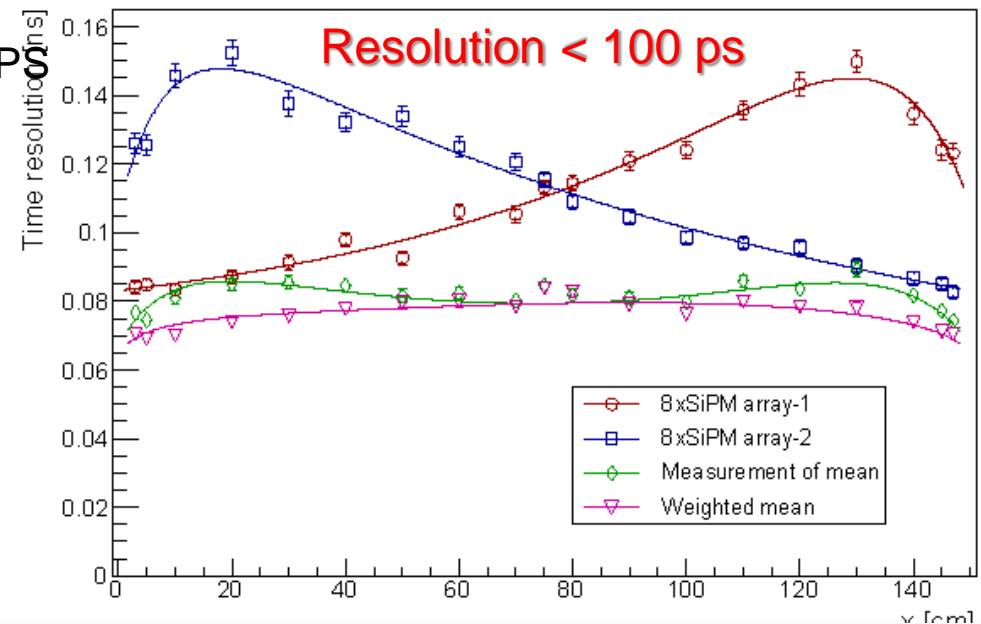
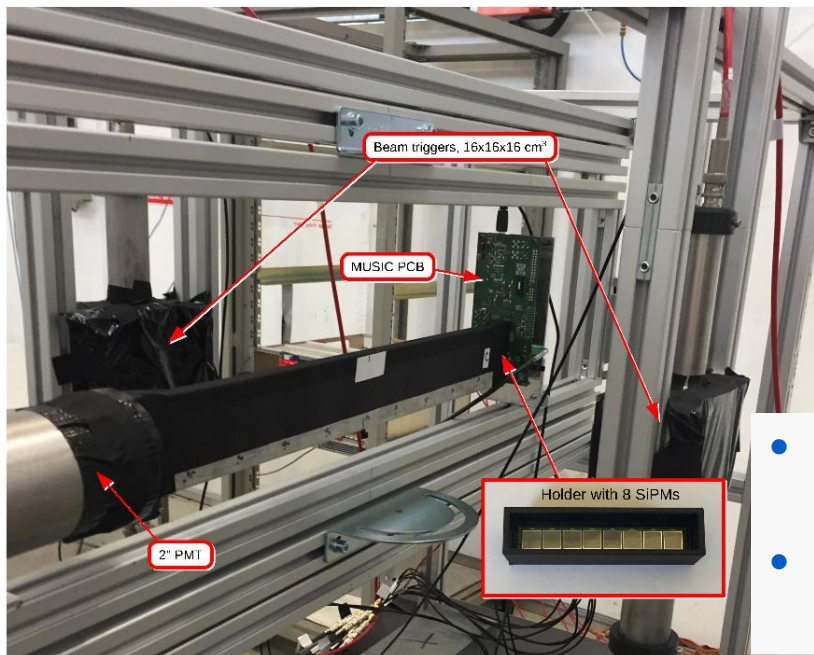


III. Combining small sensors: active summation: MUSIC

- Timing sub-detector test beam with MUSIC chip

- By Univ. Geneva & Univ. Zurich
- MUSIC in summation mode (8 6x6 mm² SiPMs)
 - Bar read-out at both ends
- 2.5 GeV/c muon beam at the CERN PS
- Readout with Wavecatcher
 - Fast analog memory (LAL & IRFU/CEA)

NIM, 924, 2019



- Measurements with the 150 cm x 6 cm x 1 cm bar.
- Time resolution as measured by the SiPM arrays at both ends of the bar as a function of the interaction point along the bar.

© A. Kornezev (Univ. Geneva)

IV. Towards a picosecond hybrid single photon sensor

First step: 65 nm FE: FastIC

- Technical and financial collaboration between CERN (KT funded) and University of Barcelona (ICCUB) (Grant FPA2016-80917-R)



- Technological advancement in detector and FE technology
 - Enormous progress in SiPMs and MCPs
 - New TDC development @ CERN: picoTDC (~3 ps bin).
- **FastIC: new Front-End chip in 65 nm**
 - Multipurpose chip: SiPMs, MCPs, etc
 - Single ended (pos/neg), differential and active summation
 - Binary (linear / non-linear ToT) and Analog output
 - First submission scheduled for end 2019: 8 ch
 - Final chip: 32 channel (< 10 mW/ch)

IV. Towards a picosecond hybrid single photon sensor

First step: 65 nm FE: FastIC

- Technical and financial collaboration between CERN (KT funded) and University of Barcelona (ICCUB) (Grant FPA2016-80917-R)



- Technological advancement in detector and FE technology
 - Enormous progress in SiPMs and MCPs
 - New TDC development @ CERN: picoTDC (~3 ps bin).
- **FastIC: new Front-End chip in 65 nm**
 - Multipurpose chip: SiPMs, MCPs, etc
 - Single ended (pos/neg), differential and active summation
 - Binary (linear / non-linear ToT) and Analog output
 - First submission scheduled for end 2019: 8 ch
 - Final chip: 32 channel (< 10 mW/ch)

**From
channels
to pixels** →



IV. Towards a picosecond hybrid single photon sensor

Hybrid implementation

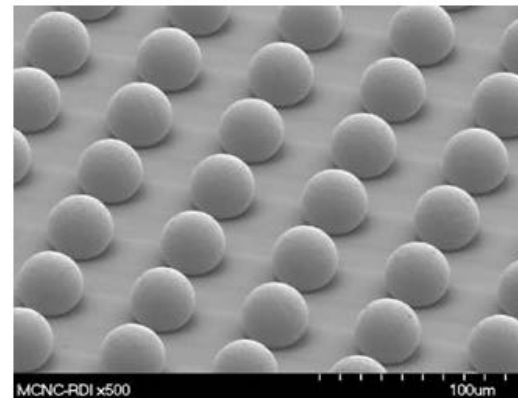
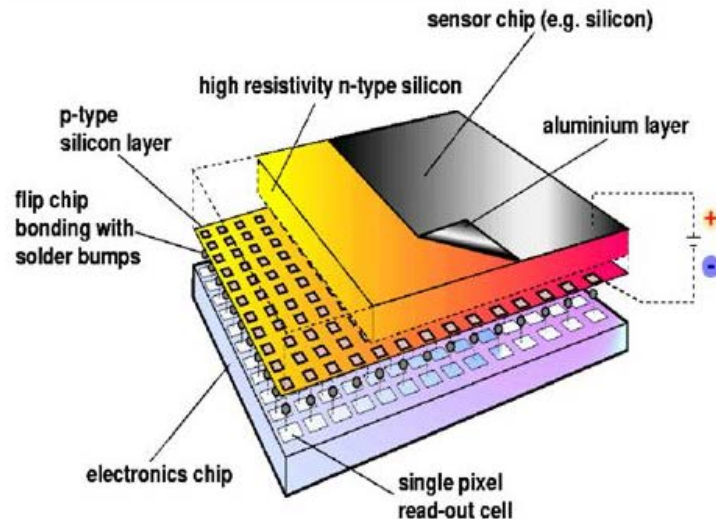
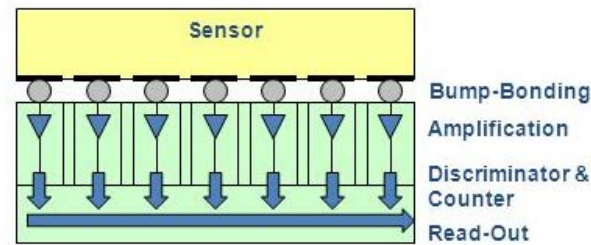
- **New hybrid single photon sensor**

- Using hybrid technology
- Well-known for pixel detectors (& CMOS imagers)

Hybrid Detectors are a 'sandwich' of a sensor and read-out chip.

Each pixel can have its own analogue and digital elements.. Amplification, shaping, discrimination, counters etc.

Very short connections to read-out reduce cross talk and noise.



High density solder bump bonding is a key technology

Collaboration with
CERN Medipix team

M. Campbell, R. Ballabriga,
N. Egidos, J.M. Fernandez,
X. Llopart

IV. Towards a picosecond hybrid single photon sensor

Adaptability and Scalability

- **Adaptability and reconfigurability:**

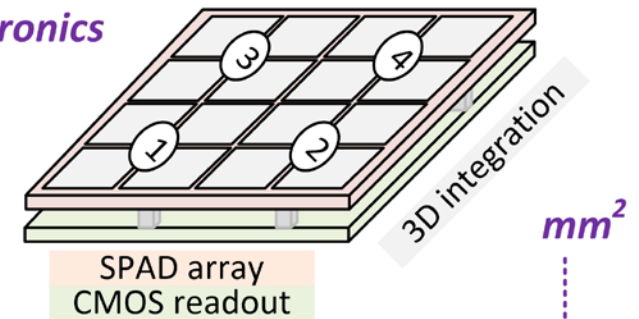
- Sensor layer easily exchanged to target different applications
- The resolution can be dynamically adjusted

- **Scalability:** the architecture and the 3D integration approach allow to build arbitrarily large arrays

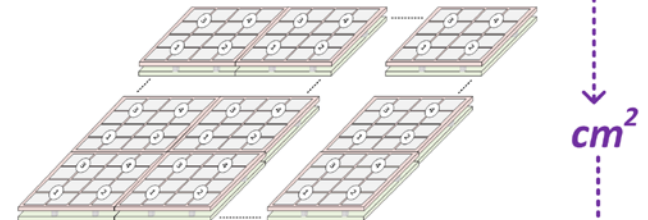
- **Low cost** for mass production

- Will be based on mass production CMOS processes
- 3D integration requirements are quite relaxed compared to 3D SPAD imagers
 - TSV pitch > 300 μm

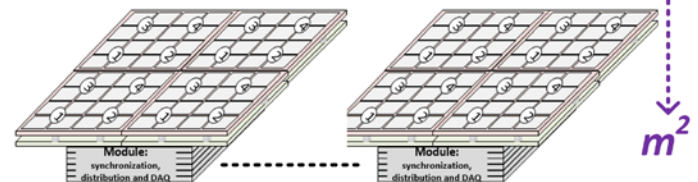
3D integration to hybridize sensor and electronics



Monolithic arrays from seamless hybrids



Modules to build large detection areas (PET rings, m^2 panels with ps timing)



Module: synchronization and DAQ

IV. Towards a picosecond hybrid single photon sensor

ATTRACT

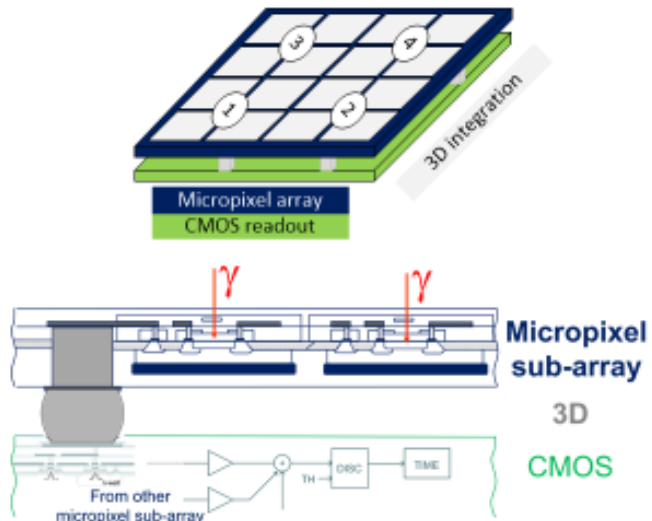
- First step ongoing: **approved ATTRACT H2020 project**
 - **Phase-I: FastICPix**: validate our approach in a full system simulation

DEVELOPING BREAKTHROUGH TECHNOLOGIES FOR SCIENCE AND SOCIETY



FastICPix: Integrated Signal Processing for a New Generation of Active Hybrid Single Photon Sensors with Picosecond Time Resolution

The Idea is to combine actively the signal of small micropixel sub-arrays based on the fastest single photon sensor technologies with ultrafast readout electronics using 3D integration.



It could have applications in medical imaging by enabling real time PET (Positron Emission Tomography), LIDAR, fluorescence lifetime imaging, homeland security and IOT / vision systems.

Our project is coordinated by the University of Barcelona **in partnership with** CERN.

It is part of wider collaborative effort involving sensor and ASIC design, 3D integration, module and applications with additional collaborators: CEA, EPFL, FBK, IFAE, LAL and University of Geneva.

Contact email dgascon@fqa.ub.edu

**Attract Kick-Off Meeting,
Geneva, May 2019**

M. Campbell, R. Ballabriga, N. Egidos, J.M. Fernandez, X. Llopart
D. Gascon, S. Gomez, R. Manera, J. Mauricio, A. Sanmukh



Back up David

I. The signal: model of a micro-cell

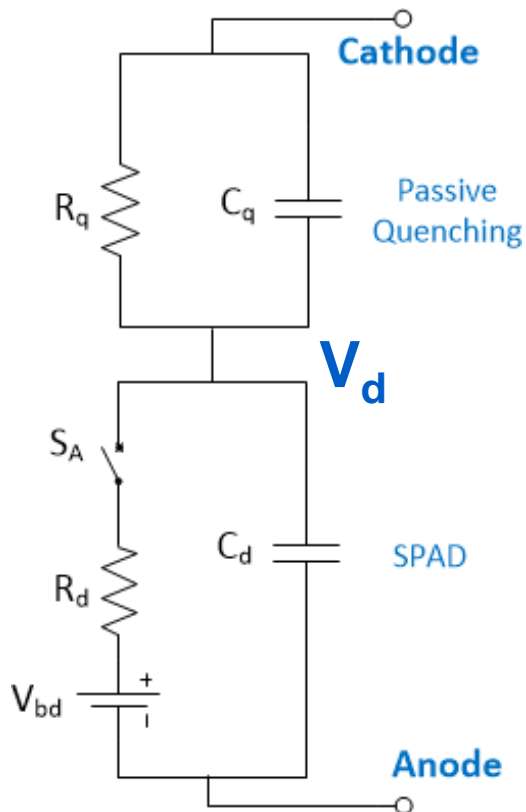
• Model of a passively quenching micro-cell

– Junction (Single-Photon Avalanche Diode, SPAD)

- C_d : diode capacitance (V_d is the voltage across the diode)
- R_d : junction resistance limiting avalanche current
 - Determined by the electric field in the junction and the mobility of the charge carriers
- V_{bd} : breakdown voltage
- S_A : switch modeling the avalanche (closes). Quenches (opens) when:
 - Electric field in the avalanche not large enough
 - Active quenching: lowering voltage
 - Insufficient amount of free charge carriers inside the junction at any given time
 - Passive quenching: limiting current I_{smin} (μA)

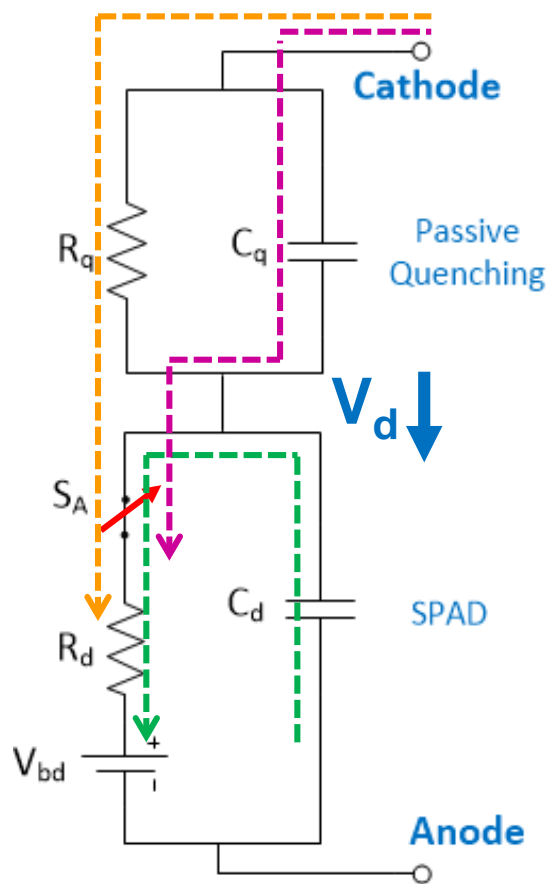
– Quenching resistor

- R_q : quenching resistance
- C_q : parasitic capacitance of the quenching resistor



N. Otte et al., "Silicon Photomultiplier Handbook," *Under Preparation*

I. The signal: model of a micro-cell: avalanche



1) When avalanche starts S_A closes

2) C_d discharges through R_d

– Current limited by R_d

3) V_d decreases

– $I_{R_d}(t)$ decreases

4) C_q is charged

– Potential cathode to anode is fixed (V_{bias})

– During avalanche micro-cell signal (current flowing into cell) is due to C_q charging

– Time constant is $\tau_d = R_d \cdot (C_q + C_d)$

4) Current also flowing through R_q

– $I_{R_q}(t) = (V_{bias} - V_d(t)) / R_q$

5) Avalanche stops at t_q when $I_{R_d}(t) < I_{Smin}$

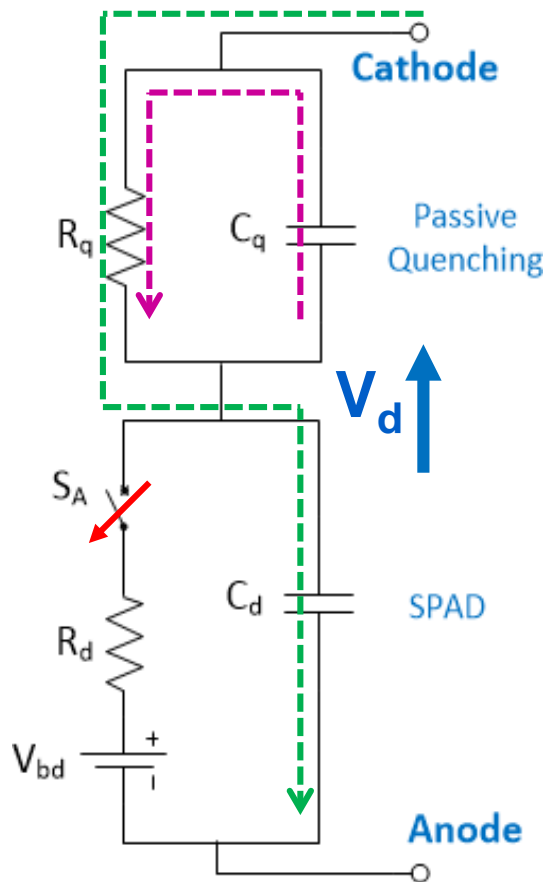
– At that moment $V_d(t_q)$

– If R_q is not high enough: no quenching !!!

Min R_q ?

$$R_q + R_d > (V_{bias} - V_{bd}) / (I_{Smin})$$

I. The signal: model of a micro-cell: recharge



1) When avalanche is quenched S_A opens

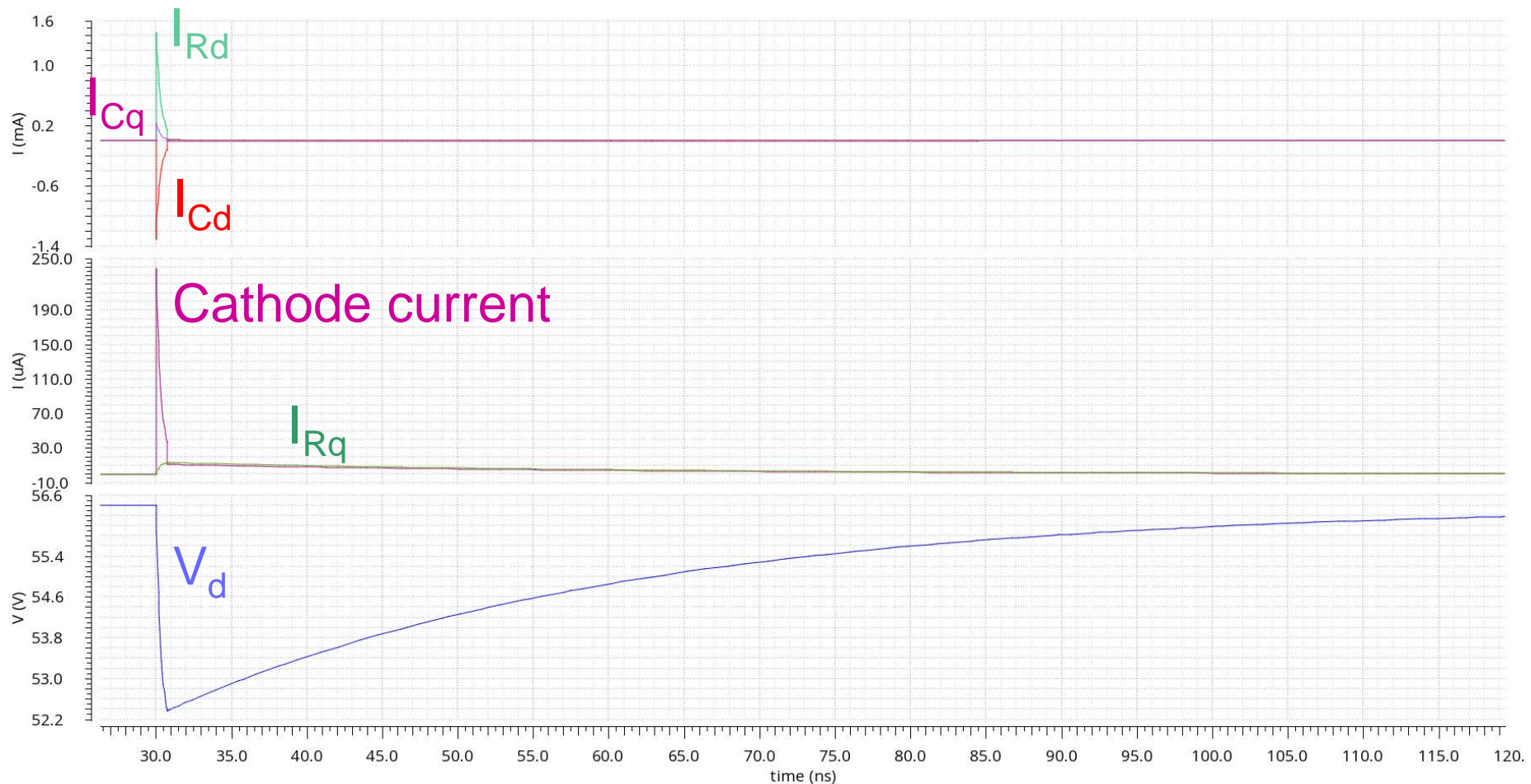
2) C_d is recharged through R_q
 – Time constant is $\tau_r = R_q \cdot (C_q + C_d)$

3) V_d increases
 – $I_{R_q}(t)$ decreases

4) C_q is discharged through R_q

5) Recharge ends when $V_d(t) = V_{bias}$
 – At this point all currents are null

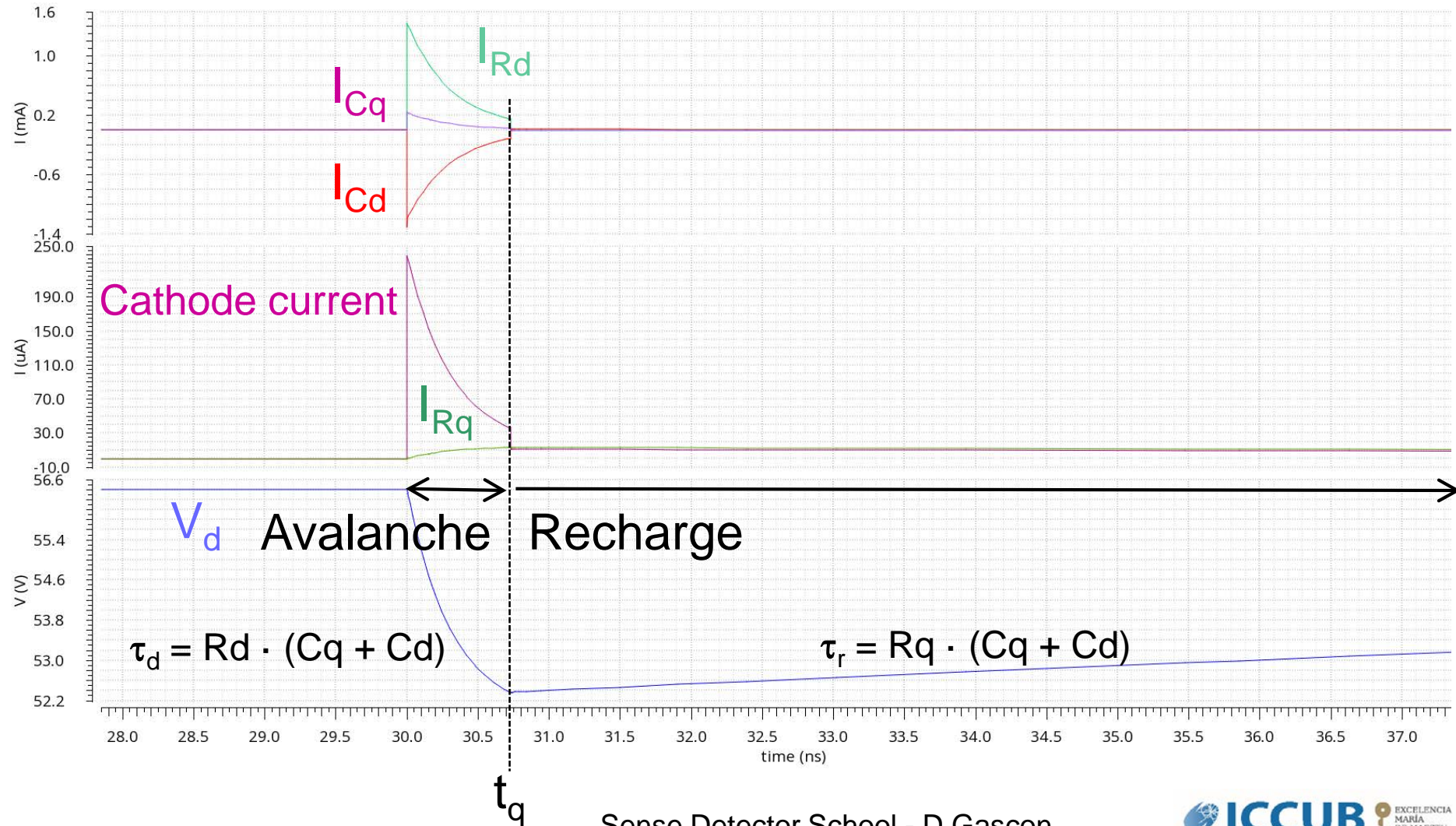
I. The signal: model of a micro-cell: simulation



- **Simulation parameters:** $C_d=84.5$ fF, $C_q=16.8$ fF, $R_q=300.8$ K Ω , $R_d= 3$ K Ω , $V_{bd}=51.9$ V, $V_{bias}=V_{bd}+4.5V$, S_A quenching current: 40 μA

I. The signal: model of a micro-cell: simulation

- Let's zoom around quenching time t_q :



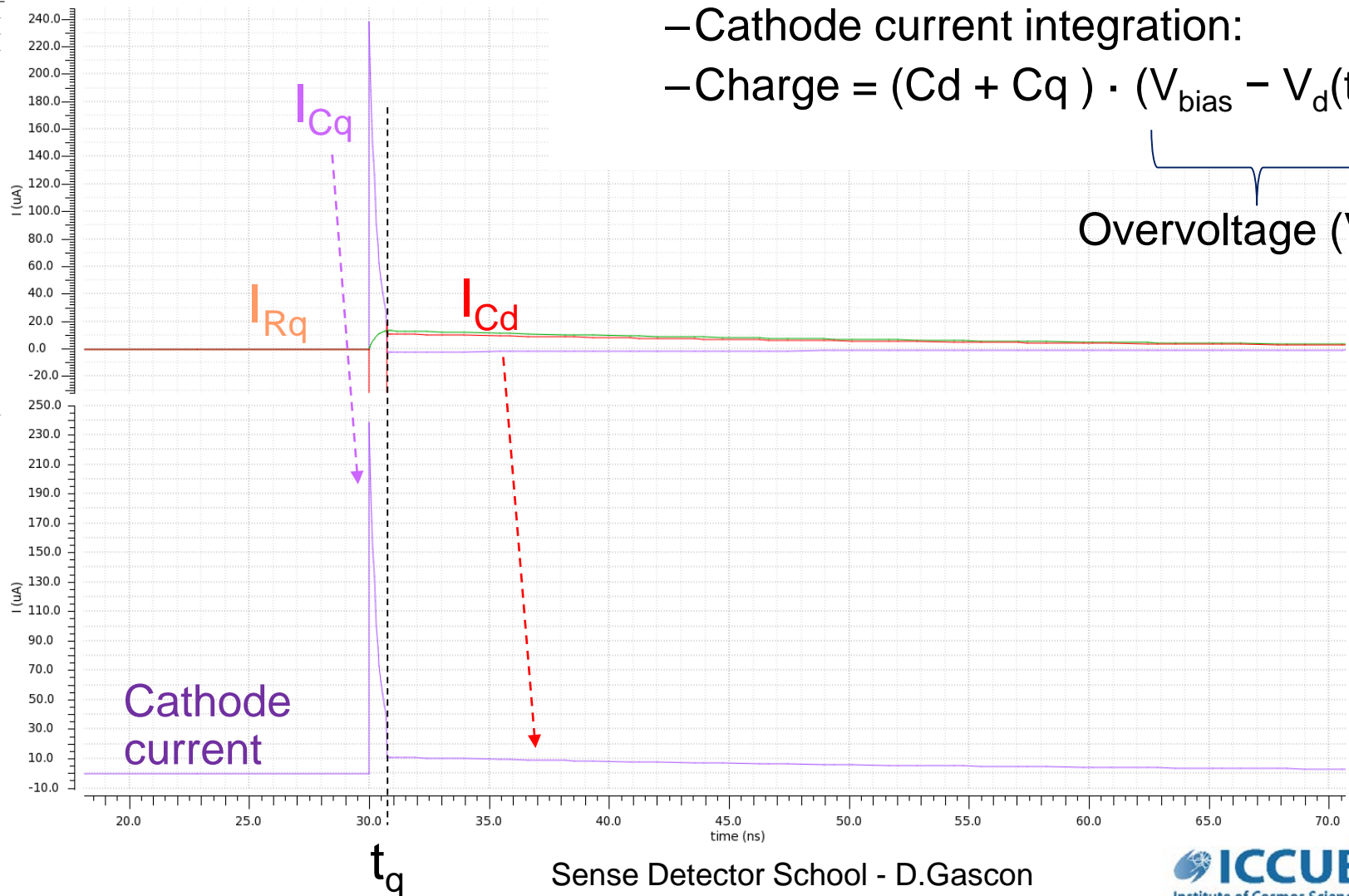
I. The signal: model of a micro-cell: simulation

- Signal-charge (gain):

- Cathode current integration:

- Charge = $(C_d + C_q) \cdot (V_{\text{bias}} - V_d(t_q))$

Overvoltage (V_{ov})



I. The signal: model of a complete SiPM

- Complete model of SiPM with N_{tot} cells:

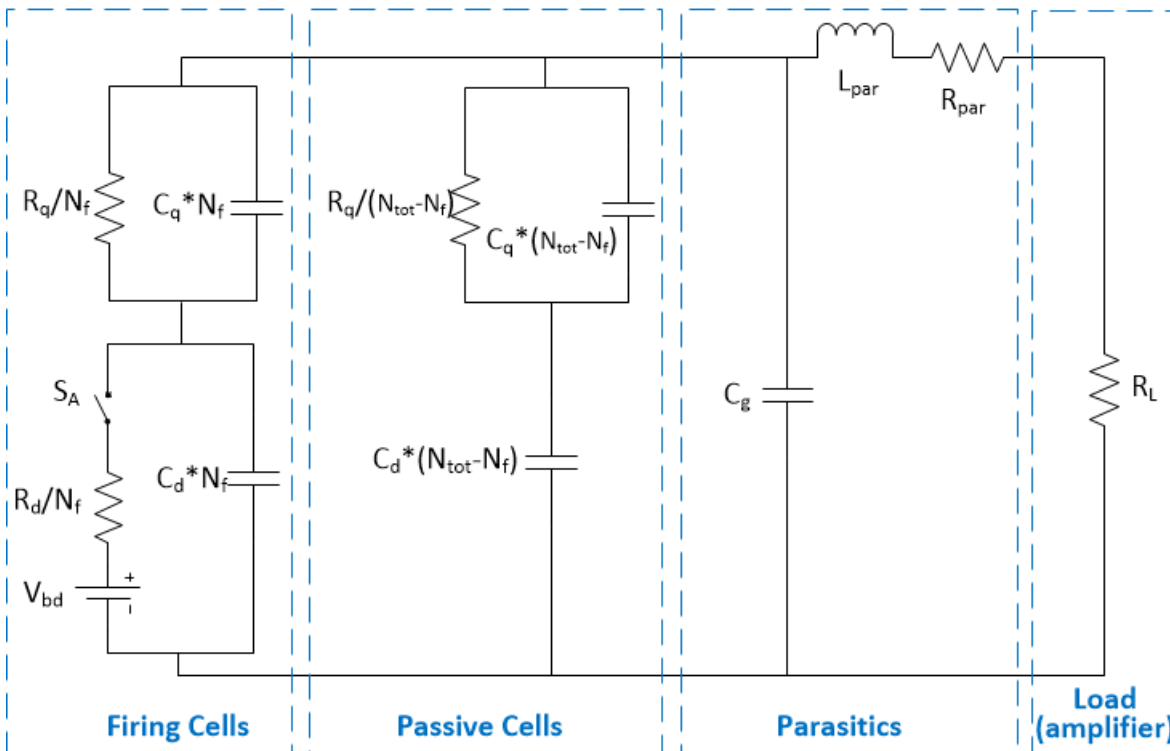
- Active (firing) cells (N_f)
- Passive (not firing) cells ($N_{tot} - N_f$)
- Parasitic components
- Load impedance

- Parasitic elements:

- C_g : interconnection parasitic capacitance
- R_{par} : interconnection parasitic resistance
- L_{par} : interconnection parasitic inductance

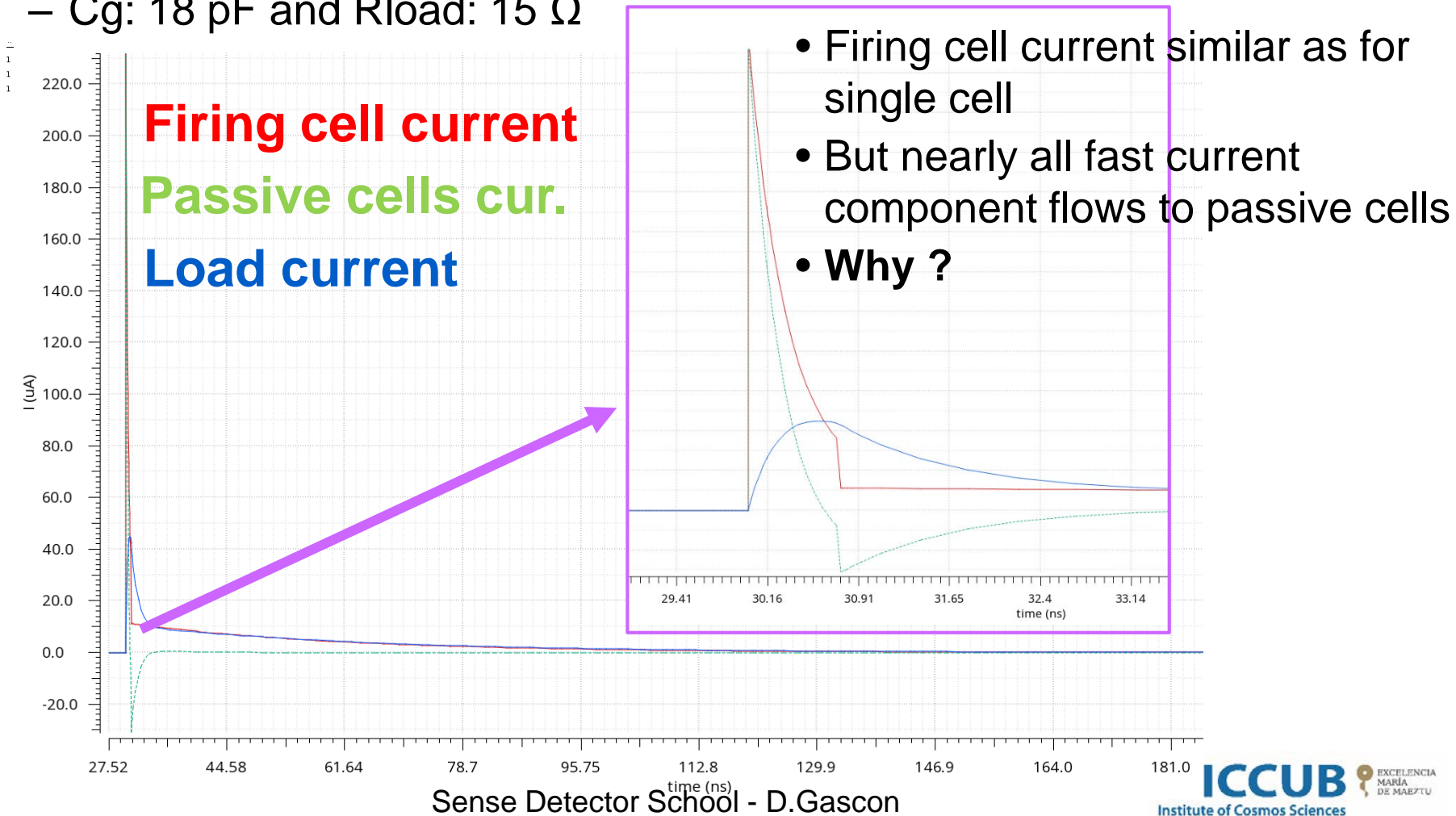
- Load:

- Usually the input impedance of the front end amplifier
- Or just a resistor
 - For instance a 50 Ω scope



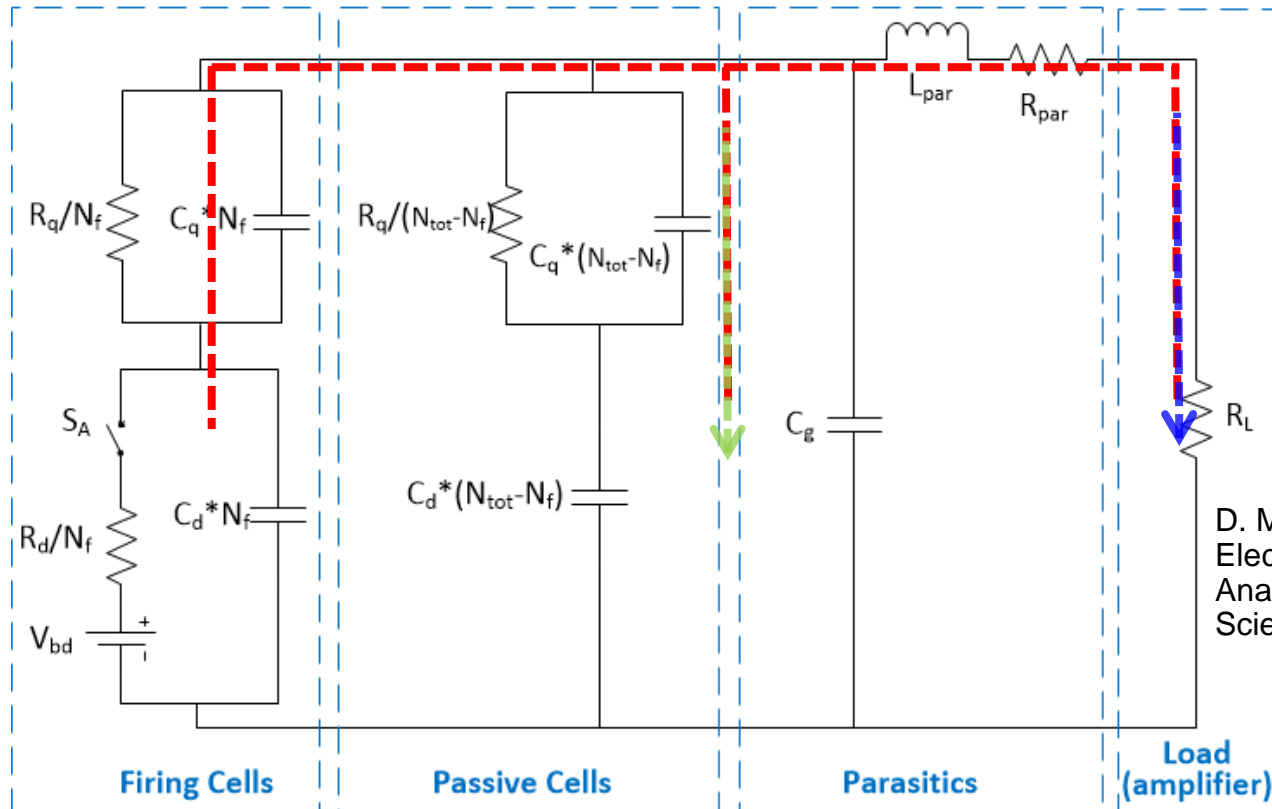
I. The signal: model of a complete SiPM

- Simulation for a typical 3x3 mm² SiPM with 3600 cells:
 - 1 firing cell. Size: 50 μm similar parameters as previous simulations
 - C_g: 18 pF and R_{load}: 15 Ω



I. The signal: model of a complete SiPM

- **Passive cells and load impedance form a low pass filter !**
 - We are sensing the current or the voltage on R_L
 - Passive cells and parasitic capacitances create a current divider with R_L
 - Peak signal goes with C_{par}^{-1}



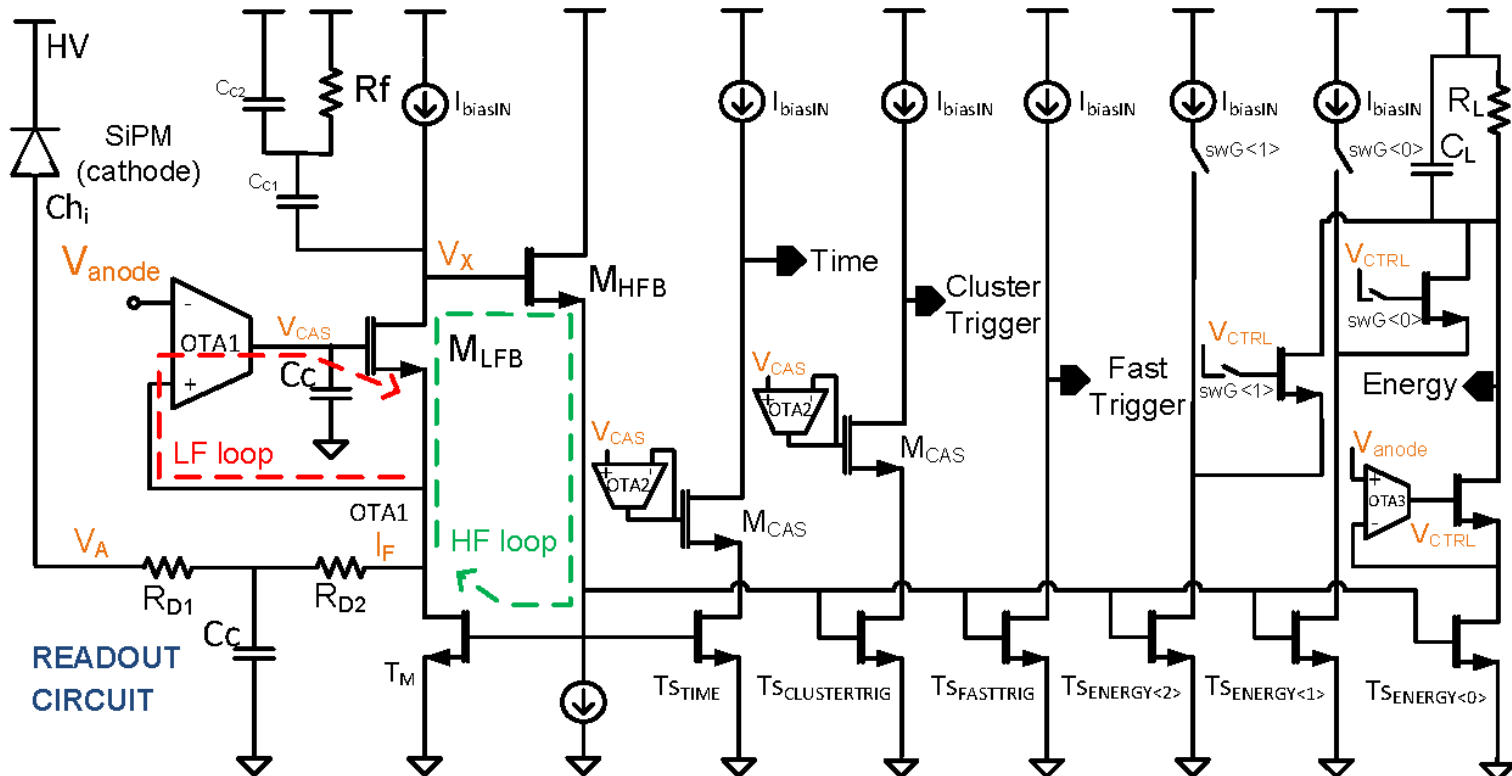
**Low pass filter
time constant:**
 $\tau_L \approx R_L \cdot N_{tot} \cdot (C_q // C_d)$

** Approximate: we should include also C_g here*

D. Marano et al., «Silicon Photomultipliers Electrical Model Extensive Analytical Analysis», IEEE Transactions on Nuclear Science 61(1), 23 (2014).

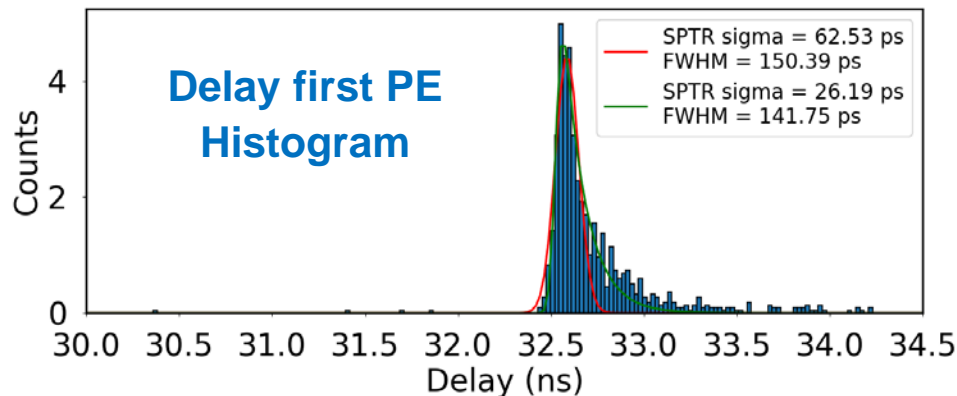
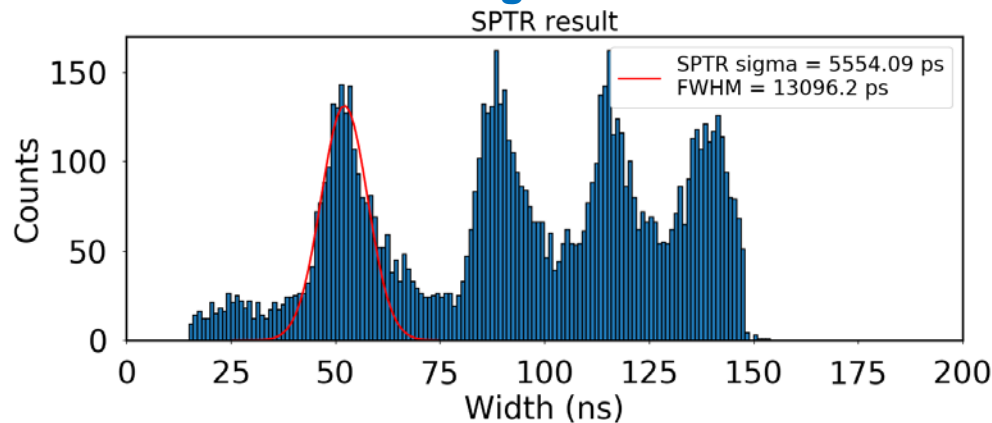
II. MultiCh readout: HRFlexToT: current based

• Input stage design



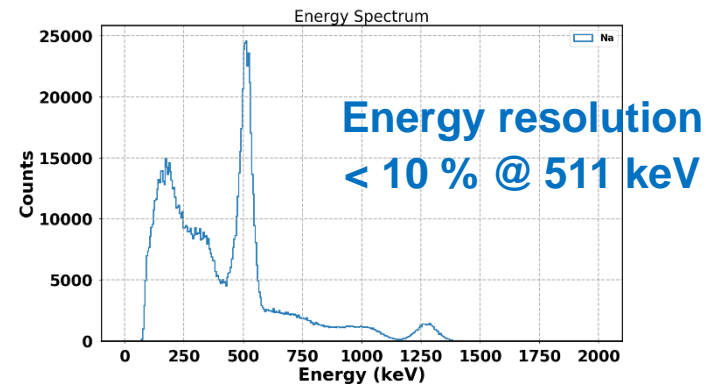
II. MultiCh readout: HRFlexToT

Time width Histogram



• Bias voltage 38V (11.5V of Overvoltage)

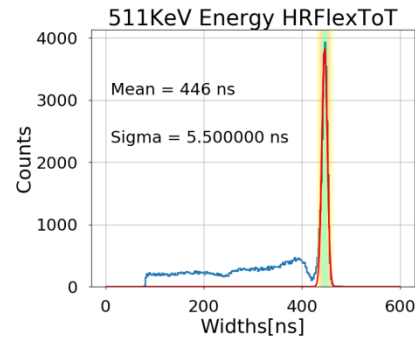
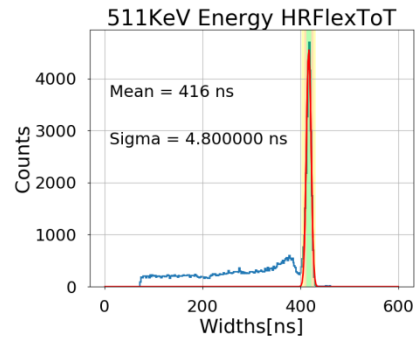
Sensor	NINO	HRFlexToT
HPK S13360 (3x3mm ² , 50μm pixel pitch)	160 (10V of Ov)	167 (10V of Ov)
FBK NUV-HD (4x4mm ² , 40μm pixel pitch)	135 (12V of Ov)	142 (11.5V of Ov)



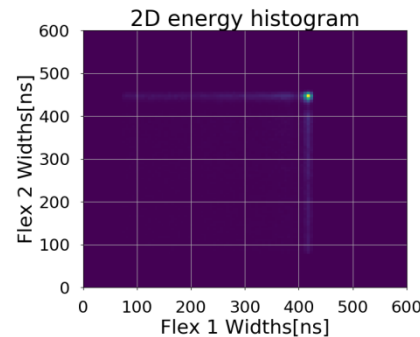
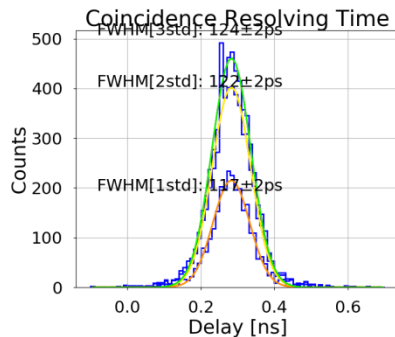
D. Sanchez, S. Gomez, D. Gascon, J. Mauricio,
L. Freixas, J. Marin, J.M. Perez. P. Rato et al.

II. MultiCh readout: HRFlexToT: CTR results using HPKK sensor

- Sensor: HPK S13360-3050PE $3 \times 3 \text{mm}^2$, $50 \mu\text{m}$ pixel pitch.
- Crystal: LSO:Ce Ca 0.2% of $2 \times 2 \times 5 \text{mm}^3$



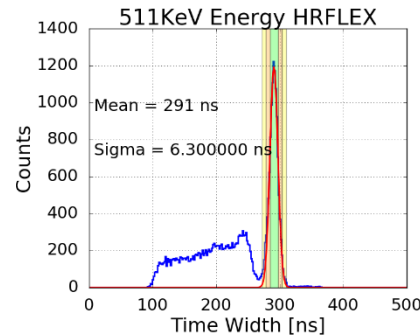
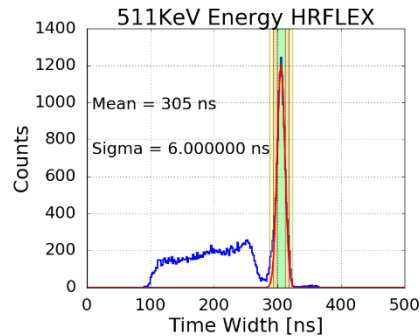
FWHM = 117ps



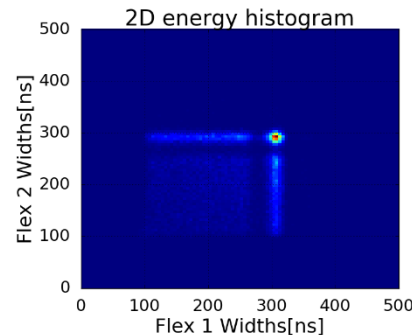
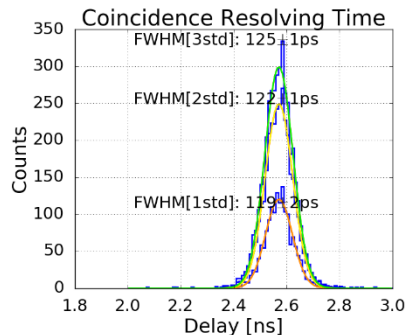
HRFlexToT Bias voltage 61V (8V of Overvoltage)

II. MultiCh readout: HRFlexToT: CTR results using FBK sensor

- Sensor: NUVHD $4 \times 4 \text{mm}^2$, $30 \mu\text{m}$ pixel pitch.
- Crystal: LSO:Ce Ca 0.2% of $2 \times 2 \times 5 \text{mm}^3$



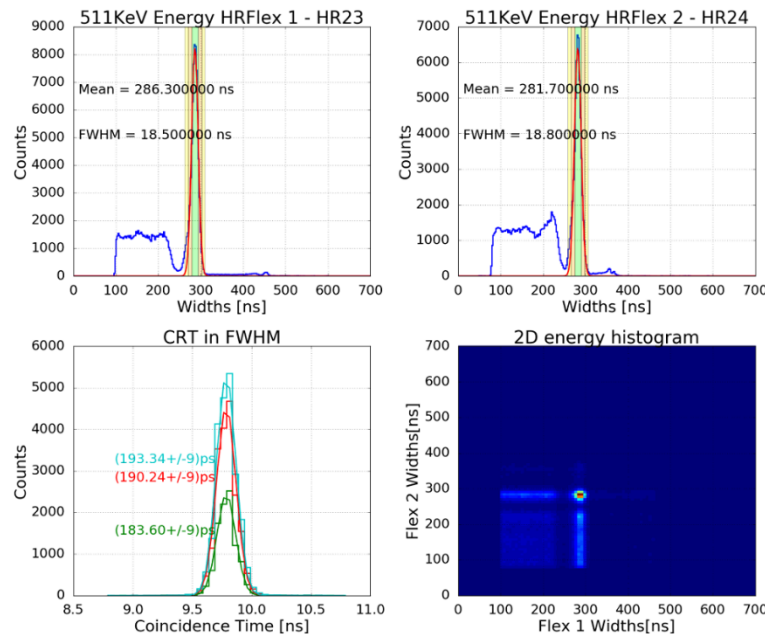
FWHM = 119ps



HRFlexToT Bias voltage 39V (12V of Overvoltage)

II. MultiCh readout: HRFlexToT: CTR results using FBK sensor

- Sensor ref: HPK S13360-3050PE $3 \times 3 \text{mm}^2$, $50 \mu\text{m}$ pixel pitch. Crystal: LSO:Ce Ca 0.2% of $2 \times 2 \times 5 \text{mm}^3$
- Sensor measuring: HPK S13360-6050CS $6 \times 6 \text{mm}^2$, $50 \mu\text{m}$ pixel pitch. Crystal: LFS of $3 \times 3 \times 20 \text{mm}^3$



FWHM = 190ps

HRFlexToT Bias voltage 58V (6V of Overvoltage)

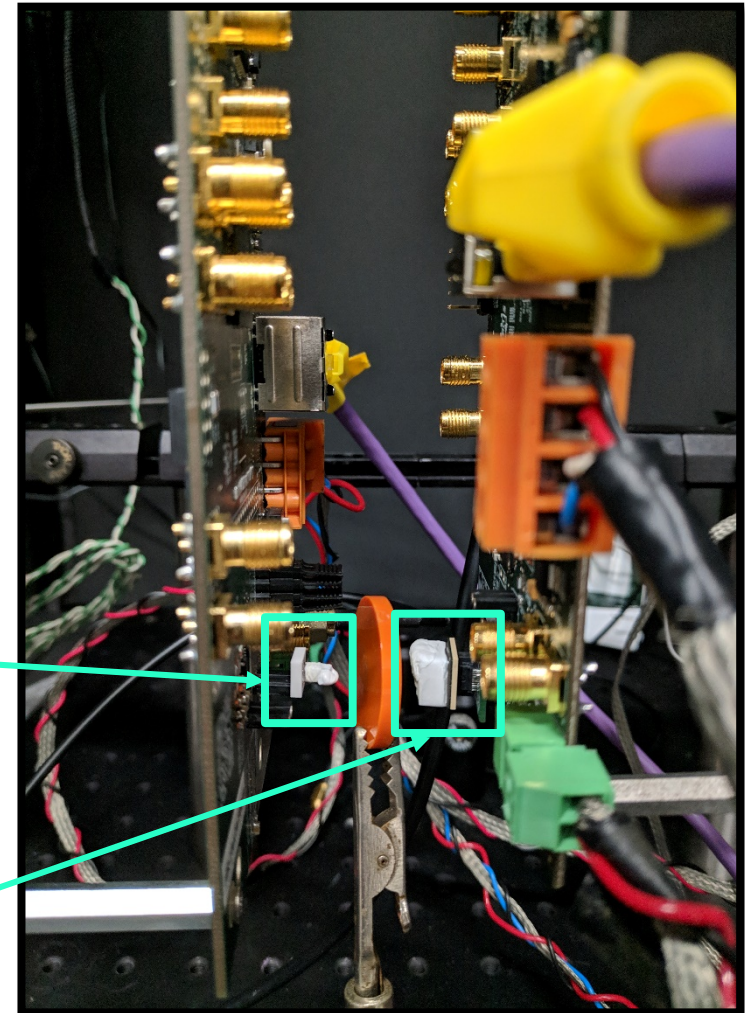
II. MultiCh readout: HRFlexToT: monolithic crystals

- Coincidence Time Resolution (CTR)
- Sensors and crystals involved:
 - Reference detector:
 - LSO:Ce 0.2% Ca $2 \times 2 \times 5 \text{ mm}^3$
 - S13360-3050CS (1 channel, $3 \times 3 \text{ mm}^2$)
 - Jitter = 90 ps FWHM
 - Monolithic module:
 - LSO:Ce 0.2% Ca $8 \times 8 \times 5 \text{ mm}^3$
 - S13361-2050PE-04 (16 channel, $2 \times 2 \text{ mm}^2$)



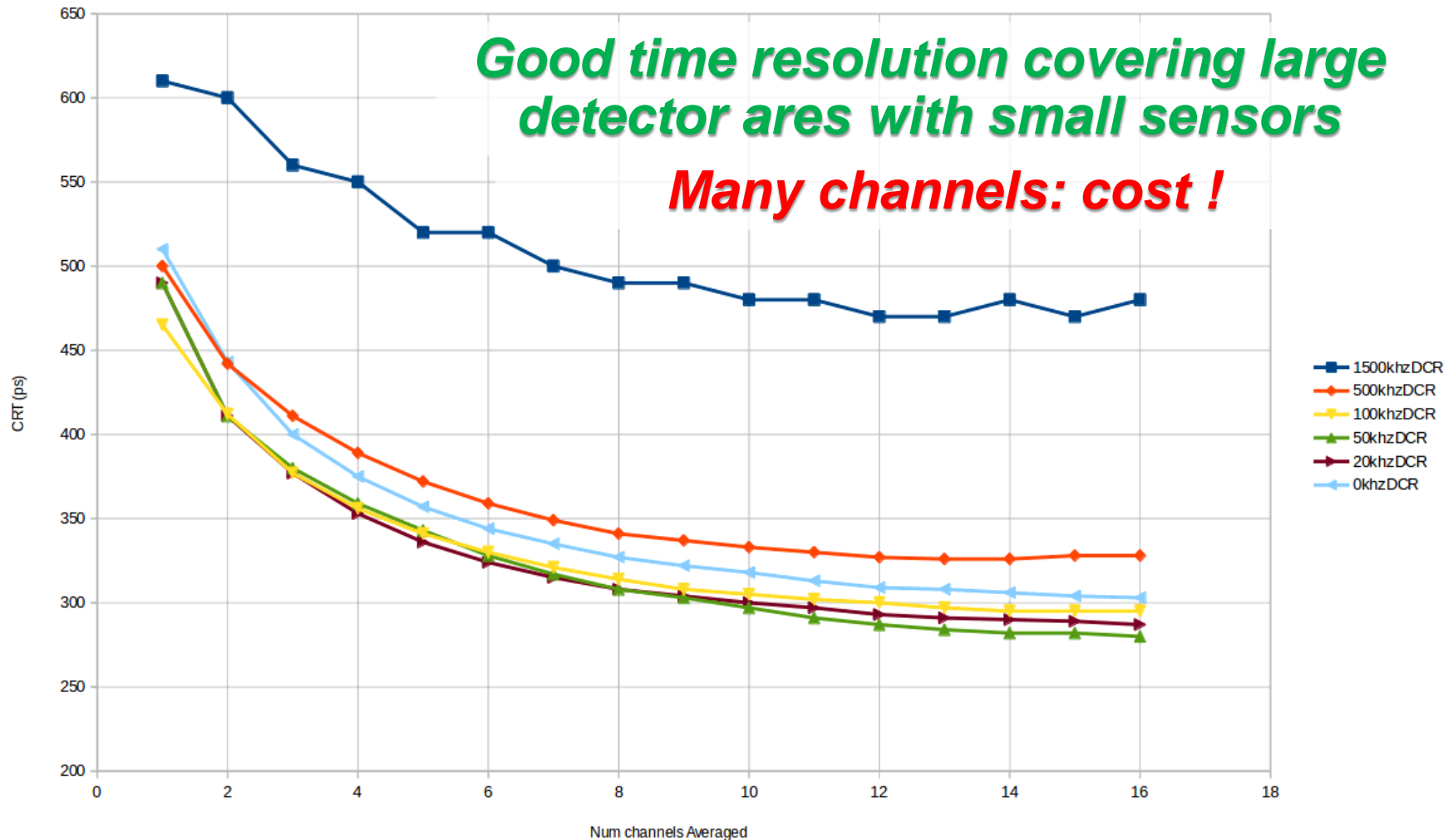
Reference
detector

Monolithic
detector



II. HRFlexToT: Time-walk correction + averaging timestamps (MONOLITHIC WITH TEFLON)

- Time-walk correction + averaging of the N fastest channels.

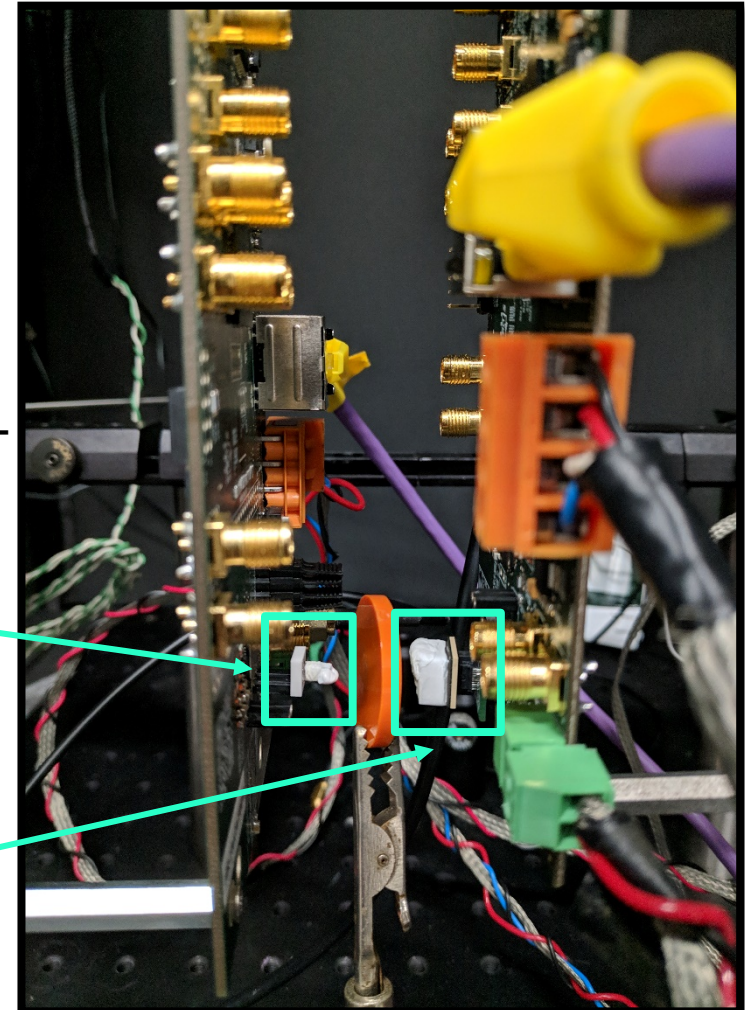


II. MultiCh readout: HRFlexToT: monolithic crystals

- Sensors and crystals involved:
 - Reference detector:
 - S13360-3050CS (1 channel, $3 \times 3 \text{ mm}^2$) + LSO:Ce 0.2% Ca $2 \times 2 \times 5 \text{ mm}^3$.
 - Jitter = 90 ps FWHM
 - Monolithic module:
 - S13361-2050PE-04 (16 channel, $2 \times 2 \text{ mm}^2$) + LSO:Ce 0.2% Ca $8 \times 8 \times 5 \text{ mm}^3$

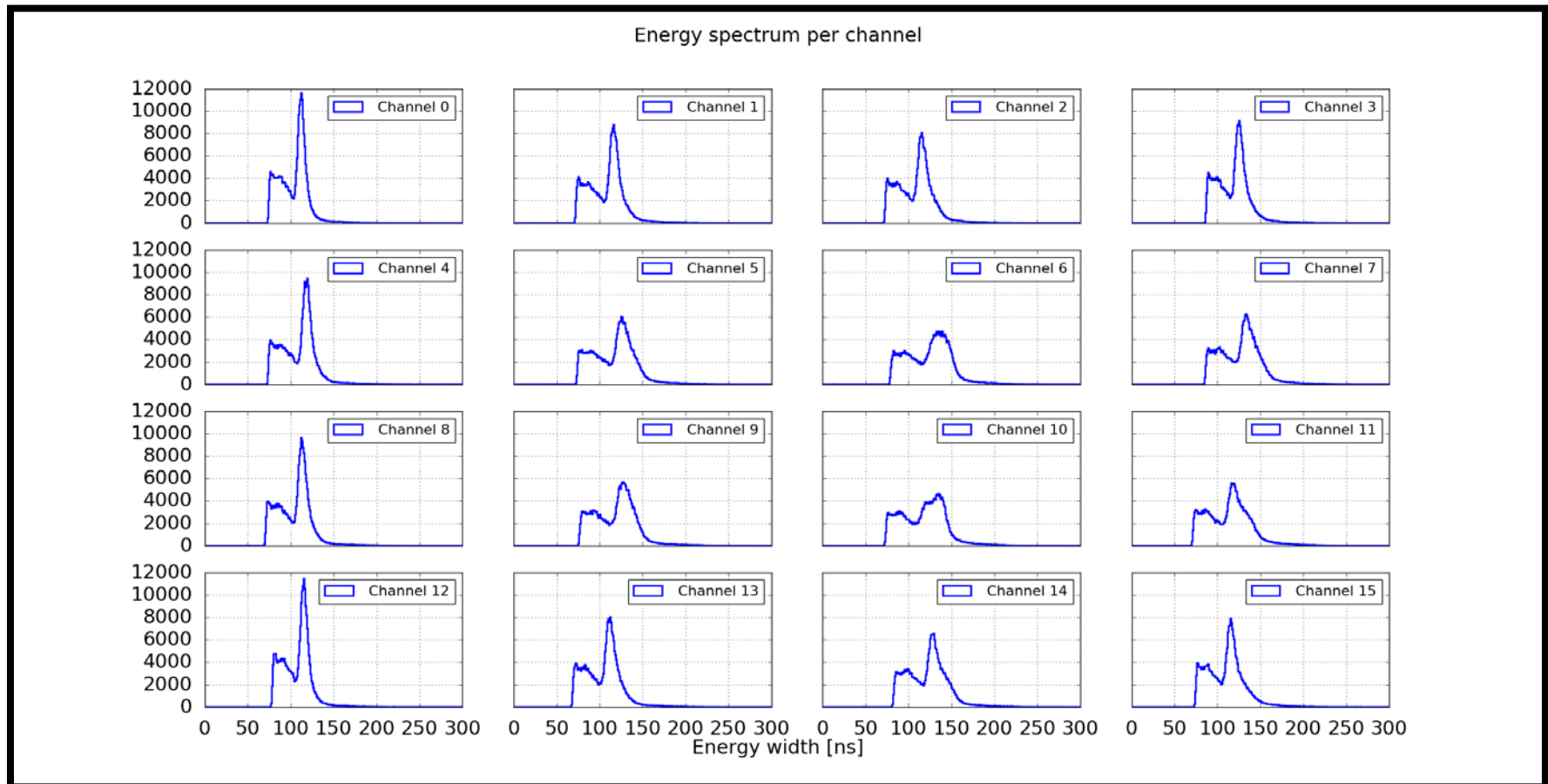
Reference detector

Monolithic detector



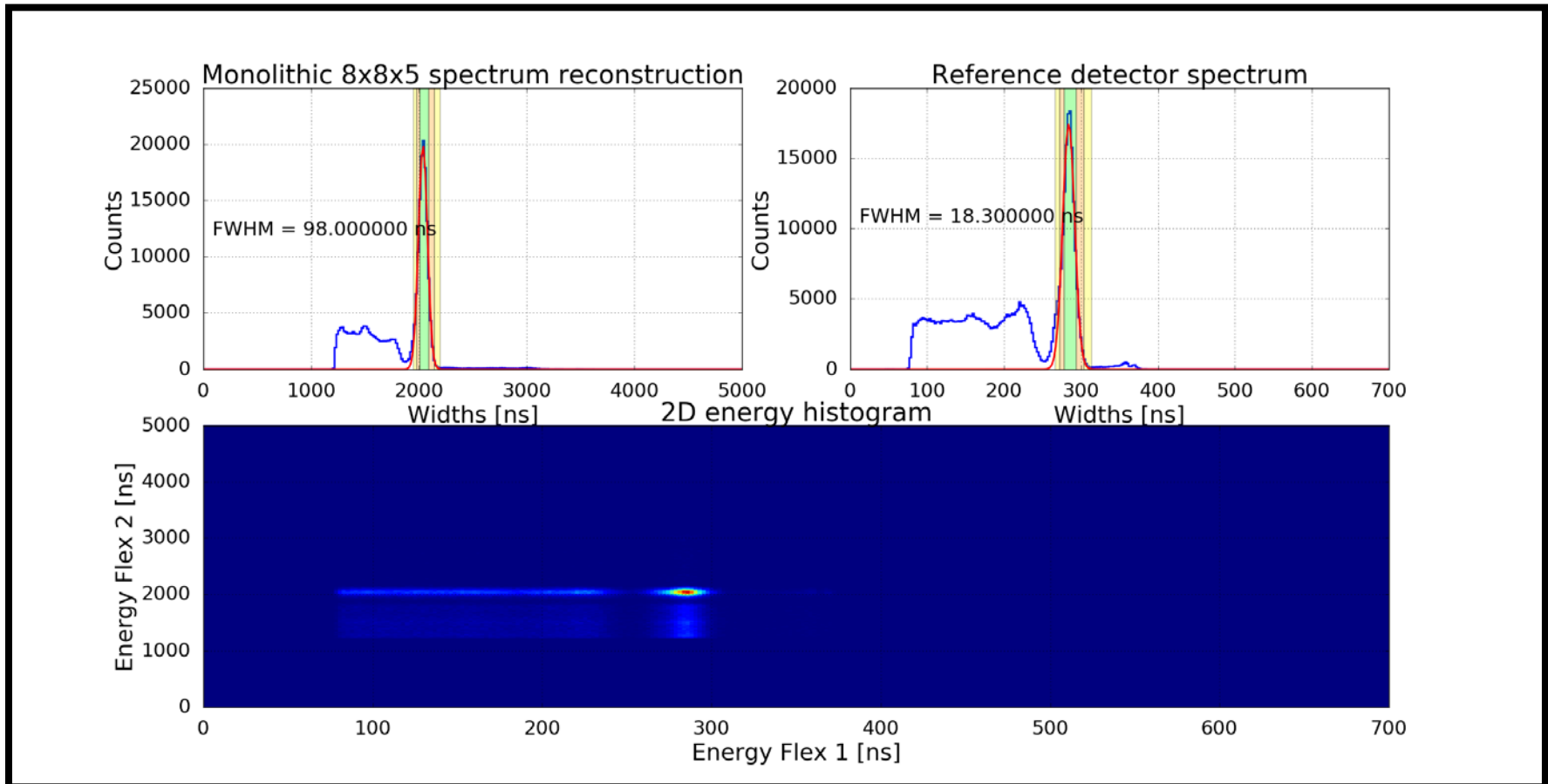
II. MultiCh readout: HRFlexToT: monolithic crystals

- Histogram showing the energy deposited in each of the 16 channels (FPGA based readout including TDC):



II. MultiCh readout: HRFlexToT: monolithic crystals

- Energy reconstruction on the monolithic and energy deposited on the reference detector:



IV. Towards a hybrid photosensor Architecture

• Intelligence:

- Bank of comparators and TDCs
- The time of arrival of multiple photons is computed by an embedded (via 3D integration) massive parallel signal processing network
- An alternative signal processing by on-chip fast analog waveform sampling also integrated

