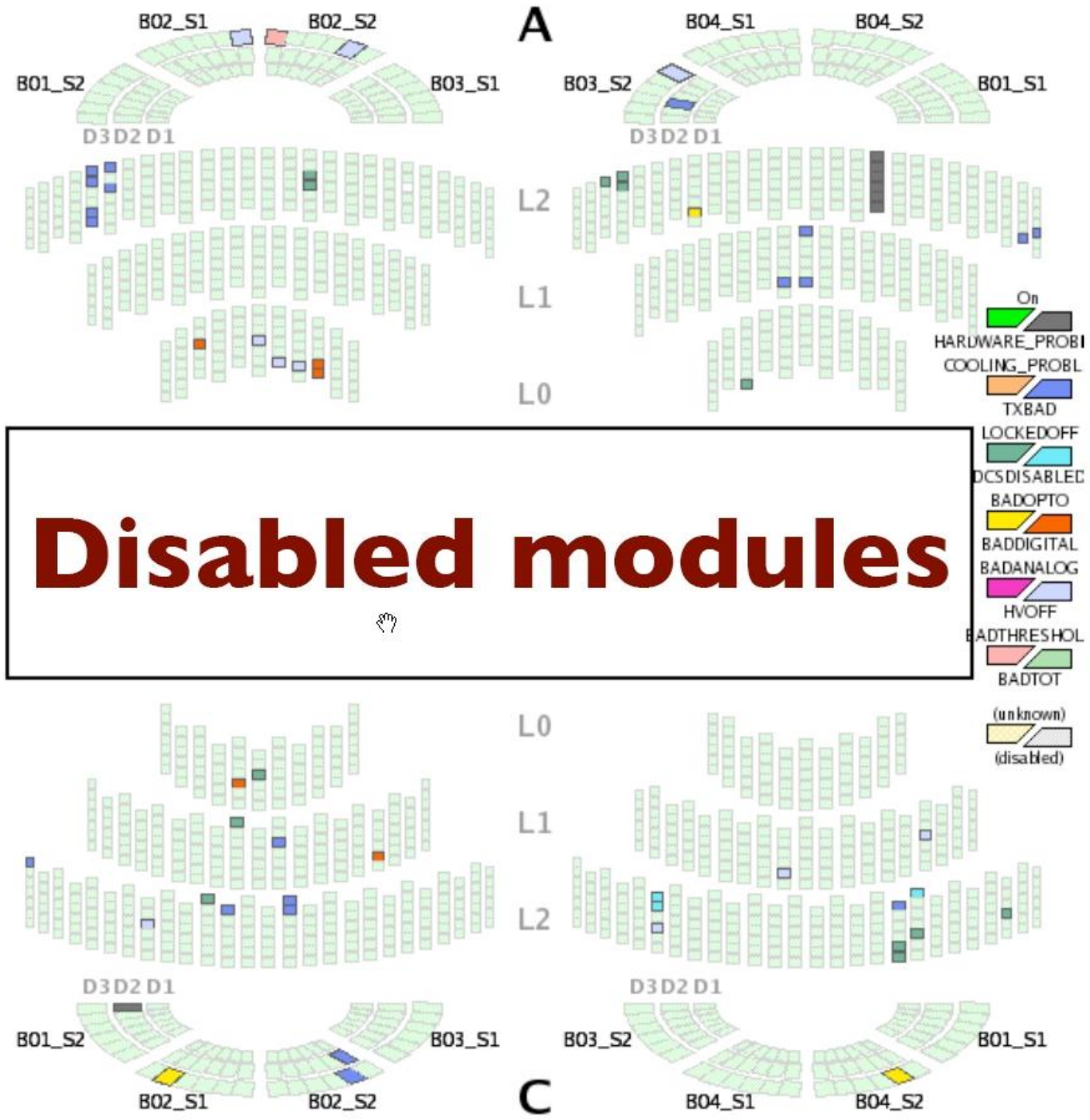
All cooling loops enabled;
62 currently disabled modules in Data taking
(26 temporary: 20 bad TX, one PP0s of due to services)

As a reference:
There were 72 Modules disabled in ID Nov run

- 36 on loops temp off
- ~10 HV OFF
- ~6 data not sent in/out
- ~20 DCSDISABLED



Summary of Failures

	Type	Integration	April 08	Dec 08	June 09
Cooling loops (88)	Leakage	-	-	3(*)	-
Optoboards (272)	Any	-	-	-	-
Module (1744)	Data IN	-	-	3	3 + 7
	Data OUT	-	-	4	6
	HV	1	**7**	13 (<20)	12 (ma uno nuovo..)
	LV	-	-	4	4
	Others	1 + *1(*)*	1 + *1(*)*	3 + *1(*)*	4 + *1(*)*
FE (1744x16)	Shorts	2	2(**)	8	9 (-1!)
	Others	1	1(**)	18	**

(*) means that they could be recovered

(**) Not checked at the time

Shorts on FEs:

- Found 3 digital shorts and 4 analog shorts. Current is extremely high (>2A vs normal ~1A). Special individual settings have to be used to operate the modules involved; possibility to be implemented in DCS.

Module	shorted FE	Current Uncong (A)	Current config (A)	Comments
L2_B15_S1_A6_M6A	chip 12	I_Vdd=1.5/I_Avdd=0.12	I_Vdd=1.8/I_Avdd=1.2	
L0_B03_S2_C6_M3C	chip 7	I_Vdd=1.7/I_Avdd=0.31	I_Vdd=2.0/I_Avdd=1.4	
L1_B10_S2_C6_M2C	chip 4	I_Vdd=1.8/I_Avdd=0.24	I_Vdd=2.1/I_Avdd=1.3	Spontaneously Recovered after ~30 h operation
L2_B08_S2_A7_M3A	chip 6	I_Vdd=0.35/I_Avdd=1.2	I_Vdd=0.7/I_Avdd=2.2	
L2_B08_S2_A7_M4A	chip 0	I_Vdd=0.32/I_Avdd=1.4	I_Vdd=0.7/I_Avdd=2.4	
L2_B15_S2_A7_M5A	chip 5	I_Vdd=0.35/I_Avdd=0.43	I_Vdd=0.7/I_Avdd=1.6	
L2_B15_S2_A7_M6A	chip 10	I_Vdd=0.36/I_Avdd=0.48	I_Vdd=0.7/I_Avdd=1.7	New in 2009

- Running with TRT trigger over the weekend- since yesterday night in ATLAS combined.
- the noise level is down to 0.12 pixels per BC per detector after the new noise masking – yesterday night.