Preflight Detector Characterization of BLAST-TNG



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LTD-18 July 26, 2019





The Next-Generation Balloon-Borne Large-Aperture Submillimeter Telescope

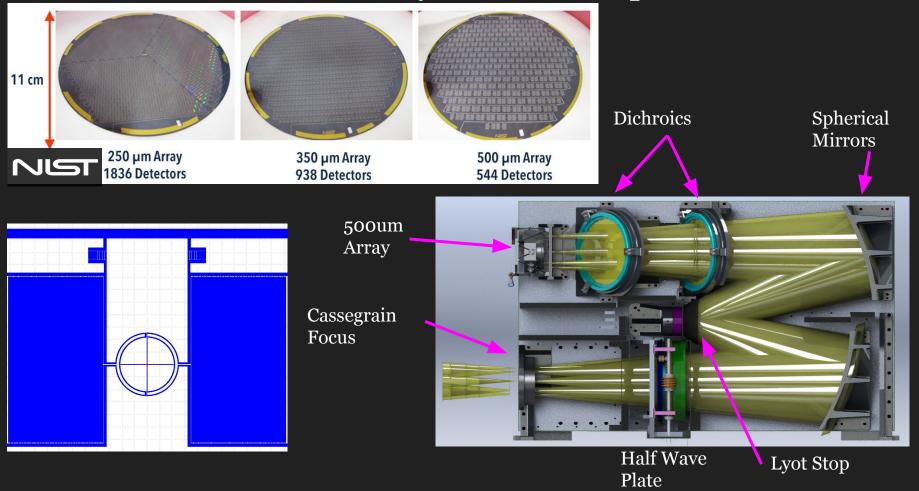
100N-BORN

28 day stratospheric balloon flight
2.5 m primary mirror
> 3000 dual-polarization sensitive LEKIDs across
3 sub-mm bands

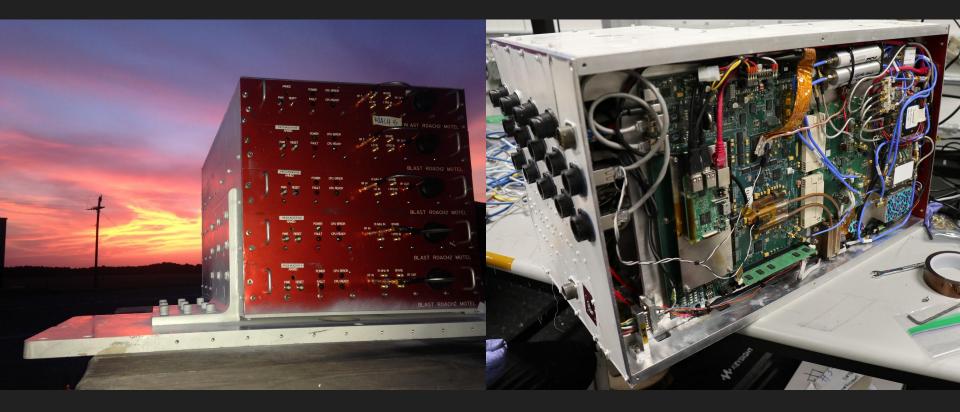
- 250, 350, 500 μm
- ~0.1 pc resolution in nearby MCs (d ~ 1 kpc)
 Second Antarctic campaign

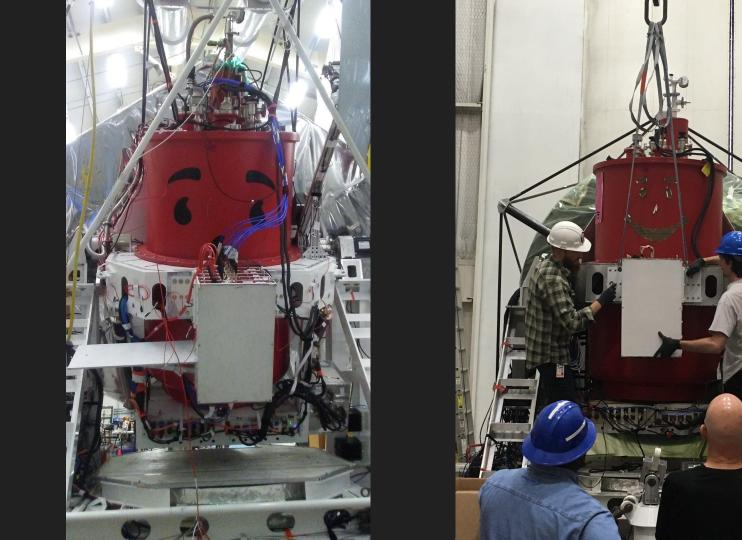
Second Antarctic campaign in summer 2019/2020

Focal Plane Arrays and Cold Optics



BLAST-TNG 'ROACH2 MOTEL' 5 slices = 5000 channels



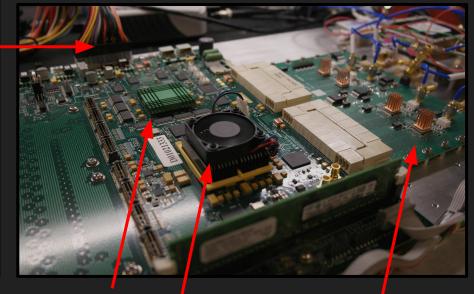


Key Readout System Requirements

Parameter	Requirement
	-
RF bandwidth (MHz)	512
S_{ϕ} / chan (dBc/Hz)	$\lesssim -95$
$1/f f_c (Hz)$	$\lesssim 0.5$
Data rate (Hz)	200-500
Power dissipation (W)	$\lesssim 60$
Time stamp precision (ms)	$\lesssim 1$
DAC Tone Resolution (Hz)	≤ 1000
Multiplexing Factor	$\gtrsim 500$

Passively cool FPGA, PPC to < 80° C Power/pixel ~ 60 mW

CASPER ROACH2



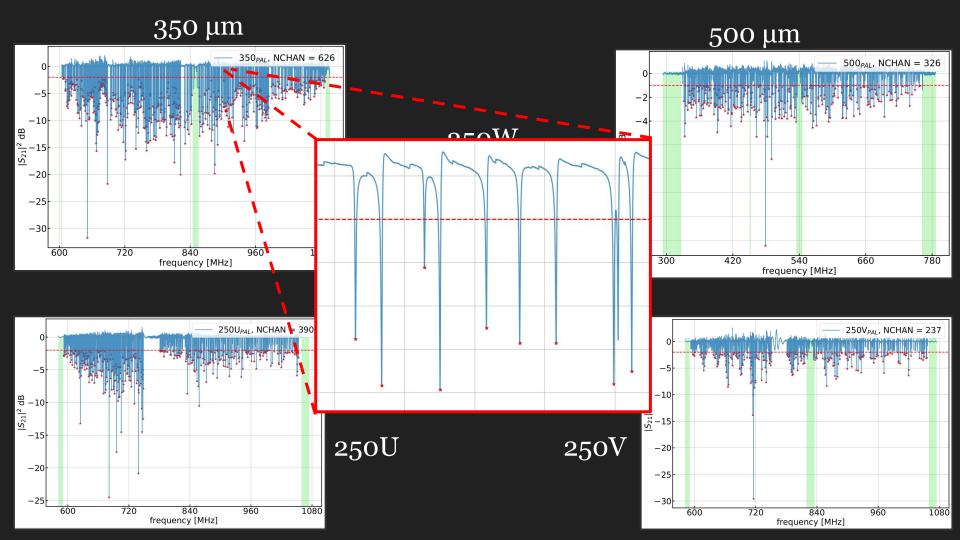
PPC FPGA

MUSIC DAC/ADC

In-Flight Readout: Software Challenges

- *Channel frequency mapping* ('KID finding')
 - Keep it consistent over different optical loadings, base temps
- *Resonant frequency tracking* (tone tracking)
 - How often will re-tunes be necessary?
 - Automatically check for retune condition during flight? (auto tune)
 - Schedule returnes on regular intervals (no automatic check)
 - What retune condition do we use (cal lamp? LO chop? Noise?)
- *Tone power calibration* global (RF attenuators) and local (digital I/Q)
 - Required to ensure majority of channels stay photon-noise limited
 - Is auto-cal required?
 - Is there a safe window where tone powers can be left for entire flight?
- Software/hardware interface limitations
 - How many buttons do we need? Do they work reliably?
 - Can one of us just fly with the payload?

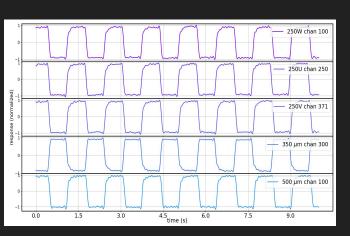
ROACH1 (SNOOKS)		ROACH2 (PIECES)		ROACH3 (MERLIN)		ROACH5 (RUBBLE)		TEMPERATURES		
IS_STREAMING			Cl.	IS_STREAMING	Classic State	IS_STREAMING	-	R1 FPGA	20.33	
	treaming	STATE	Streaming	STATE	Streaming	STATE	Streaming	R2 FPGA	24.90	
HAS_FIRMWARE	1	HAS_FIRMWARE	1	HAS_FIRMWARE	1	HAS_FIRMWARE	1	R3 FPGA	19.98	THE REAL PROPERTY AND A DESCRIPTION OF A
DATA_STREAM_ERROR	0	DATA_STREAM_ERROR	0	DATA_STREAM_ERROR	0	DATA_STREAM_ERROR	0	R4 FPGA	16.79	100
ALLOW_AUTO_TRNAROUND	1	ALLOW_AUTO_TRNAROUND	1	ALLOW_AUTO_TRNAROUND	1	ALLOW_AUTO_TRNAROUND	1	R5 FPGA	22.04	FLIGHT COMPUTERS
ALLOW CHOP LO	1	ALLOW CHOP LO	1	ALLOW CHOP LO	1	ALLOW CHOP LO	1	R1 PPC	19.49	FC1 CMD COUNT 257
CURRENT N TONES	260	CURRENT N TONES	524	CURRENT N TONES	622	CURRENT N TONES	486	R2 PPC	25.49	FC2 CMD COUNT 257
N CHANNELS	260	N CHANNELS	524	N CHANNELS	622	N CHANNELS	486	R3 PPC	24.34	FC IN CHARGE 1
PREV N CHANNELS	252	PREV N CHANNELS	393	PREV N CHANNELS	625	PREV_N_CHANNELS	385	R4 PPC	25.17	
HAS VNA TONES	0	HAS VNA TONES	0	HAS VNA TONES	0	HAS VNA TONES	0	R5 PPC	21.47	
HAS TARG TONES	1	HAS TARG TONES	1	HAS TARG TONES	1	HAS TARG TONES	1	KJFFC	21.41	
POWER PER TONE [dBm]	-47.00	POWER PER TONE [dBm]	-47.00	POWER PER TONE [dBm]	-47.00	POWER PER TONE [dBm]	-47.00		CDV	O CTATUS
SET ATTEN OUT [dB]	6	SET ATTEN OUT [dB]	3	SET ATTEN OUT [dB]	2	SET ATTEN OUT [dB]	3	CRYO STATUS		
READ ATTEN OUT [dB]	6	READ ATTEN OUT [dB]	3	READ ATTEN OUT [db]	2	READ ATTEN OUT [dB]	3	TR_HE3_FR		0.314276
SET ATTEN IN [dB]	17	SET ATTEN IN [dB]	20	SET ATTEN IN [dB]	21	SET ATTEN IN [dB]	20	350 ARRAY	TEMP	0.323442
								250 LNA		ON
READ_ATTEN_IN [dB]	17	READ_ATTEN_IN [dB]	20	READ_ATTEN_IN [dB]	21	READ_ATTEN_IN [dB]	20	350 LNA		ON
ADC_RMS_I [mV]	142.58	ADC_RMS_I [mV]	106.80	ADC_RMS_I [mV]	97.86	ADC_RMS_I [mV]	100.05	500 LNA		ON
ADC_RMS_Q [mV]	142.70	ADC_RMS_Q [mV]	107.59	ADC_RMS_Q [mV]	102.82	ADC_RMS_Q [mV]	107.10	CAL LAMP		-0.0474713
LO_CENTER_FREQ_MHZ	540.000	LO_CENTER_FREQ_MHZ	827.000	LO_CENTER_FREQ_MHZ	850.000	LO_CENTER_FREQ_MHZ	828.000	FRIDGE CY	CLE STAT	FE 0
LO_FREQ_READ_MHZ	540.000	LO_FREQ_READ_MHZ	827.000	LO_FREQ_READ_MHZ	850.000	LO_FREQ_READ_MHZ	828.000	POT VALVE		CLOSED
DOING_FULL_LOOP	0	DOING_FULL_LOOP	0	DOING_FULL_LOOP	0	DOING_FULL_LOOP	0	ROACH LA	MP NOW	0
DOING_FIND_KIDS_LOOP	0	DOING_FIND_KIDS_LOOP	0	DOING_FIND_KIDS_LOOP	0	DOING_FIND_KIDS_LOOP	0	ROACH EN		
DOING_TURNAROUND_LOOP 0 DOING_TURNAROUND_LOOP 0		0	DOING_TURNAROUND_LOOP	0	DOING_TURNAROUND_LOOP	NOACH_EN				
IS_WRITING	0	IS_WRITING	0	IS_WRITING	0	IS_WRITING	0			
IS SWEEPING	0	IS SWEEPING	0	IS SWEEPING	0	IS SWEEPING	0			
IS FINDING KIDS	0	IS FINDING KIDS	0	IS FINDING KIDS	0	IS FINDING KIDS	0			
TONE FINDING ERROR	0	TONE FINDING ERROR	0	TONE FINDING ERROR	0	TONE FINDING ERROR	0			
FPGA CLOCK FREQ MHZ	257.75	FPGA CLOCK FREQ MHZ	257.47	FPGA CLOCK FREQ MHZ	257.28	FPGA CLOCK FREQ MHZ	257.45			ITING
HAS ODR CAL	1	HAS ODR CAL	1	HAS ODR CAL	1	HAS ODR CAL	1	ELEVATION		
ODR CAL FAIL	0	QDR CAL FAIL	0	QDR CAL FAIL	0	ODR CAL FAIL	0	AZIMUTH	184.6	
HAS VNA SWEEP	1	HAS VNA SWEEP	1	HAS VNA SWEEP	1	HAS VNA SWEEP	1	SHUTTER	CLOS	
HAS TARG SWEEP	1	HAS TARG SWEEP	1	HAS TARG SWEEP	1	HAS TARG SWEEP	1	RA	18.81	78
HAS REF PARAMS	1	HAS REF PARAMS	1	HAS REF PARAMS	1	HAS REF PARAMS	1	DEC	-56.98	892
SWEEP FAIL	0	SWEEP FAIL	0	SWEEP FAIL	0	SWEEP FAIL	0			
FIRMWARE UPLOAD FAIL	0	FIRMWARE UPLOAD FAIL	õ	FIRMWARE UPLOAD FAIL	ő	FIRMWARE UPLOAD FAIL	0		TIM	ling
KATCP CONNECT ERROR	0	KATCP CONNECT ERROR	0	KATCP CONNECT ERROR	0	KATCP CONNECT ERROR	0	R1 PPS CO	UNT	4842
PI ERROR COUNT	0	PI ERROR COUNT	0	PI ERROR COUNT	0	PI ERROR COUNT	0	R2 PPS CO		4842
	0		0		0	PI REBOOT	1277	R3 PPS CO		4842
PI_REBOOT		PI_REBOOT		PI_REBOOT			0	R5 PPS CO		4842
LAST_DF_RESPONSE [Hz]	0.00	LAST_DF_RESPONSE [Hz]	0.00	LAST_DF_RESPONSE [Hz]	0.00	LAST_DF_RESPONSE [Hz]	0.00			2.35817e+06
DF_RETUNE_THRESH	100000	DF_RETUNE_THRESH	100000	DF_RETUNE_THRESH	100000	DF_RETUNE_THRESH	100000			2.35831e+06
N_OUT_OF_RANGE_THRESH	300	N_OUT_OF_RANGE_THRESH	300	N_OUT_OF_RANGE_THRESH	300	N_OUT_OF_RANGE_THRESH	300			2.35831e+00 2.35844e+06
N_OUTOFRANGE	0	N_OUTOFRANGE	0	N_OUTOFRANGE	0	N_OUTOFRANGE	0			
IS_CHOPPING_LO	0	IS_CHOPPING_LO	0	IS_CHOPPING_LO	0	IS_CHOPPING_LO	0			2.35813e+06
HAS_LAMP_CONTROL	0	HAS_LAMP_CONTROL	0	HAS_LAMP_CONTROL	0	HAS_LAMP_CONTROL	1	R1_UTC_TIM		668577531
WAITING_FOR_LAMP	0	WAITING_FOR_LAMP	0	WAITING_FOR_LAMP	0	WAITING_FOR_LAMP	0	R2_UTC_TIM		668577528
FULL_LOOP_FAIL	0	FULL_LOOP_FAIL	0	FULL_LOOP_FAIL	0	FULL_LOOP_FAIL	0	R3_UTC_TIM		668577518
TURN_AROUND_LOOP_FAIL	0	TURN_AROUND_LOOP_FAIL	0	TURN_AROUND_LOOP_FAIL	0	TURN_AROUND_LOOP_FAIL	0	R4_UTC_TIM	٨E	668577539
EXT_REF	1	EXT_REF	1	EXT_REF	1	EXT_REF	1			
IS_COMPRESSING	0	IS_COMPRESSING	0	IS_COMPRESSING	0	IS_COMPRESSING	0			
PI_TEMP	34.8	PITEMP	34.8	PITEMP	36.4	PI_TEMP	34.3			



Timestreams - real-time conversion from I/Q to df with IQ gradient method

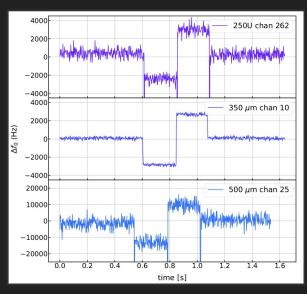
1000

Cal lamp chops (every full loop)



⁰ -1000 2000 -2000 -2000 -2000 -2000 -2000 -10000 -2000 -10000 -200 -2000 -

LO chops (azimuth turnarounds)



Filtered timestreams with NDF 30 Hz

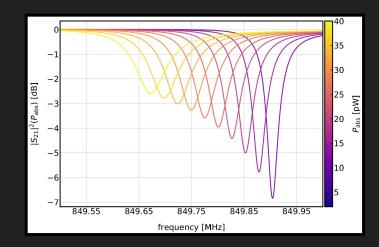
No NDF (single channels, ice) 244 Hz

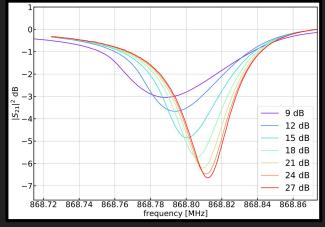
250W chan 261

Optical and Temperature Responsivities

Verified using LEKID software model which estimates:

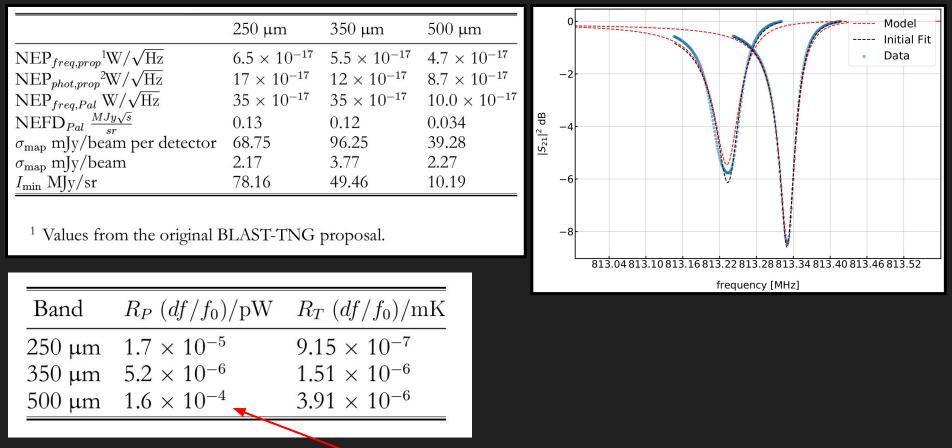
- Quality factors (Q_r, Q_c, Q_i)
- Frequency response to dT and dP_{abs} : df/dT, df/dP
- Quasiparticle number densities
- Sensitivity/Noise-equivalent power
- Photon-noise
- Detector-noise
- Amplifier-noise





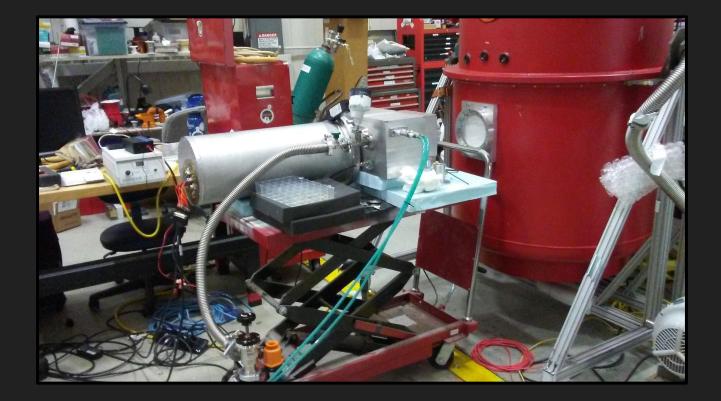
Instrumental optical efficiency estimated to be $\sim 30\%$

Pre-flight characterization in Palestine - readout power not optimized → sensitivities are upper limits

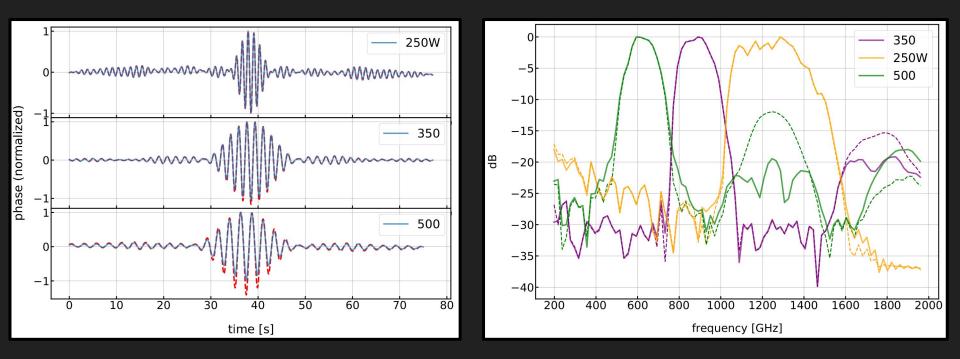


Avg values provided by NIST

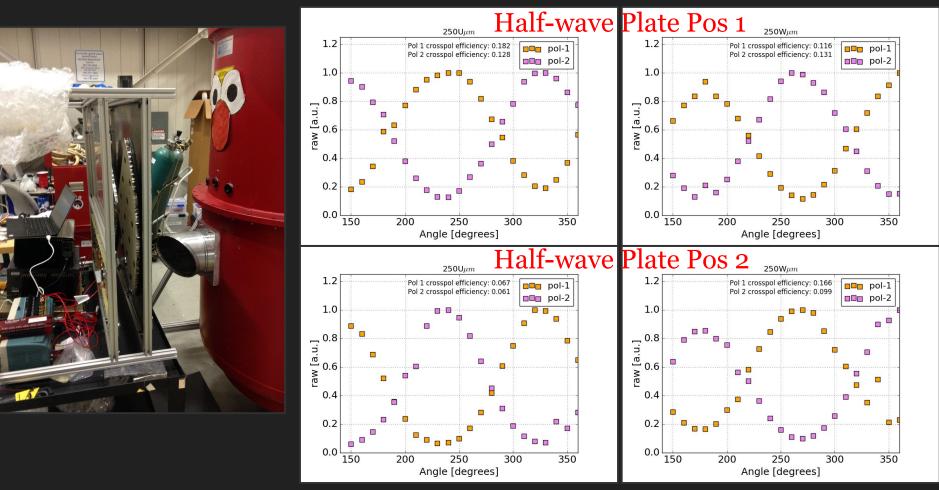
Optical Passband Measurement



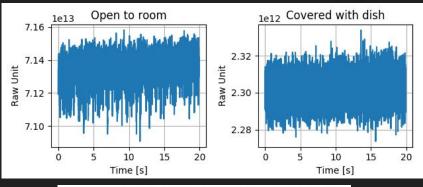
Optical Passband Measurement



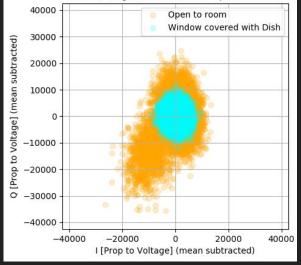
Polarization Efficiency Measurements

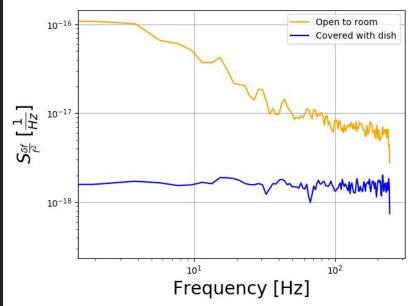


Optical tests in Antarctica - without NDF



Wessel diagram for IQ noise open detector





Tone powers are optimized over Palestine tests

Discovered WiFi pick up in cryostat

Planning a couple of days of ground tests pre-flight

The Next-Generation Balloon-Borne Large-Aperture Submillimeter Telescope

Instrument fully integrated

Spending the winter in the high bay on a snow berm at LDB

First in line this year!



Earth and Space Science Fellowship (Astrophy NNX16AO91H

Next steps for BLAST-TNG type readout systems:



Image: Phil Mauskopf

IF single board integration = SWAP-c reduction



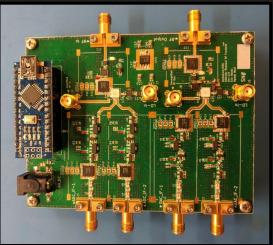


Image: Eric Weeks



Xilinx RF System on a Chip (RFSoC) = 8X Bandwidth, ¹/₃ power dissipation



Thanks!





Earth and Space Science Fellowship (Astrophysics) NNX16AO91H