Tracciatori in 4D

Day 1:

- Why timing?
- A Time tagging system
- Gain in Silicon detectors
- Signal formation, go thin!

Day 2:

- Radiation damage
- Read-out
- From a pixel to a tracker

Bonus: design your own detector \rightarrow prepare for tomorrow

4D tracking with ultra-fast silicon detectors

http://iopscience.iop.org/article/10.1088/1361-6633/aa94d3/pdf https://arxiv.org/abs/1704.08666

Preamble: Timing layers, 4D- and 5D-tracking

Besides a few indirect signals of new physics, particle physics today faces a discovery desert.

We need to cross an **energy**- **cross section** desert to reach the El-dorado of new physics.

Very little help in the direction of this path is coming from nature, the burden is on the accelerator and experimental physicists to provide the means for this crossing.

Timing is one of the enabling technologies to cross the desert





The effect of timing information

The inclusion of track-timing in the event information has the capability of changing radically how we design experiments.

Timing can be available at different levels of the event reconstruction, in increasing order of complexity:

1) Timing in the event reconstruction → Timing layers

- this is the easiest implementation, a layer ONLY for timing
- 2) Timing at each point along the track \rightarrow 4D tracking
 - tracking-timing
- 3) Timing at each point along the track at high rate \rightarrow 5D tracking
 - Very high rate represents an additional step in complication, very different read-out chip and data output organization

Preamble: simulator Weightfield2

Available at:

http://personalpages.to.infn.it/~cartigli/Weightfield2/Main.html

It requires Root build from source, it is for Linux and Mac.

It will not replace TCAD, but it helps in understanding the sensors response



Weightfield2

Highlights:

- It is completely open source
- it's fast
- It generates the signal from several sources (MIP, alpha, lasers..)
- Runs in batch mode writing output files
- It loads/save configurations
- It has basics electronics simulation

It crashes occasionally

How to use it:

Obtain the last version from

http://personalpages.to.infn.it/~cartigli/Weightfield2/Main.html

From the <u>download</u> page, get the latest version
 Unzip it and then type:
 Make or 3-bis) make -f Makefile_MacOS10.10_root6
 /weightfield



Figure 1 Interaction time of many proton-proton vertexes happening in the same bunch crossing in the case of \sim 50 overlapping events. The vertexes are spaced 10's of pico seconds apart.

Is timing really necessary?

The research into 4D tracking is strongly motivated by the HL-LHC experimental conditions:

150-200 events/bunch crossing

According to CMS simulations:

- Time RMS between vertexes: 153 ps
- Average distance between two vertexes: 500 um
- Fraction of overlapping vertexes: 10-20%
 - Of those events, a large fraction will have significant degradation of the quality of reconstruction



At HL-LHC: Timing is equivalent to additional luminosity

One extra dimension: tracking in 4Dimension



Figure 4 Schematic representation of the power of timing information in distinguishing overlapping events using a timing layer.

Timing at each point along the track

→Massive simplification of patter recognition, new tracking algorithms will be faster even in very dense environments
 → Use only "time compatible points"



Where do we place a single timing layer?

The tracking community thinks it is a wonderful idea, clearly to be implemented **outside the tracker volume**, in front of the calorimeter





The calorimeter community thinks it is a wonderful idea, clearly to be implemented far from the calorimeter, in the tracker volume



We are now in contact with the muon community....

State-of-the-art Position Detectors

Extremely good position detectors are currently in use in every major high energy physics experiment:

- Millions of channels
- Very reliable
- Very radiation hard

The timing capability is however limited to ~ 100-150 ps (NA62 @CERN)



σ_t ~ 100-150 ps σ_x ~ 20-30 μm



Time is set when the signal crosses the comparator threshold

The timing capabilities are determined by the characteristics of the signal at the output of the pre-Amplifier and by the TDC binning.

Strong interplay between sensor and electronics

Time resolution



Usual "Jitter" term Here enters everything that is "Noise" and the steepness of the signal

 $\sigma_{t} = \frac{\sigma_{n}}{\left|\frac{dV}{dt}\right|}$

Need large dV/dt

Time walk: Amplitude variation, corrected in electronics

Shape variations: non homogeneous energy



Gain needs E ~ 300kV/cm. How can we do it?

1) Use external bias: assuming a 50 micron silicon detector, we need $V_{bias} = \sim 600 - 700 V$



Difficult to achieve

2) Use Gauss Theorem:

$$\sum q = 2\pi r * E$$



Need to have 10¹⁶/cm³ charges !!

Gain in Silicon detectors

Gain in silicon detectors is commonly achieved in several types of sensors. It's based on the avalanche mechanism that starts in high electric fields: V ~ 300 kV/cm

Charge multiplication

- Gain:
- α = strong E dependance
- $\alpha \sim 0.7$ pair/µm for electrons,
- $\alpha~$ ~0.1 for holes

Concurrent multiplication of electrons and holes generate very high gain

Silicon devices with gain:

- APD: gain 50-500
- SiPM: gain ~ 10⁴

$$N(l) = N_0 \cdot e^{\mathbf{O} \cdot l}$$

$$\mathbf{G} = \mathbf{e}^{\alpha \cdot l} \quad \alpha_{e,h}(E) = \alpha_{e,h}(\infty) \cdot \exp\left(-\frac{b_{e,h}}{|E|}\right)$$



Low Gain Avalanche Detectors (LGADs)

The LGAD sensors, as proposed and manufactured by CNM (National Center for Micro-electronics, Barcelona): High field obtained by adding an extra doping layer E ~ 300 kV/cm, closed to breakdown voltage



Difference PiN - LGAD



Traditional silicon diode



Low Gain Avalanche Diode

Come di fanno dei sensori?

Collaborazione molto intensa (~ una riunione a settimana per 3 anni...) FBK – Trento Uni – INFN

UFSD1: 300-micron. First LGAD production at FBK. Gain layer study, edges **UFSD2: 50-micron.** Very successful, good gain and overall behavior, excellent time resolution. Gain layer doping: Boron, Gallium, Boron + Carbon, Gallium+Carbon





The key to good timing is the uniformity of signals:

Drift velocity and Weighting field need to be as uniform as possible

Basic rule: parallel plate geometry



Drift Velocity



Highest possible E field to saturate velocity
Highest possible resistivity for velocity uniformity



Weighting Field: coupling the charge to the electrode



Strip: 100 µm pitch, 40 µm width



Pixel: 300 µm pitch, 290 µm width



Good: strong coupling almost all the way to the backplane

The weighting field needs to be as uniform as possible, so that the coupling is always the same, regardless of the position of the charge

Non-Uniform Energy deposition

Landau Fluctuations cause two major effects:

- Amplitude variations, that can be corrected with time walk compensation
- For a given amplitude, the charge deposition is non uniform.

These are 3 examples of this effect:



Signal formation in silicon detectors

We know we need a large signal, but how is the signal formed?

What is controlling the slew rate?

 $\frac{\mathrm{dV}}{\mathrm{dt}} \propto ?$



A particle creates charges, then:

- The charges start moving under the influence of an external field
- The motion of the charges induces a current on the electrodes
- The signal ends when the charges reach the electrodes

What is the signal of one e/h pair?

(Simplified model for pad detectors)

Let's consider **one single electron-hole pair**.

The integral of their currents is equal to the electric charge, q:

$$\int [i_{el}(t) + i_{h}(t)] dt = q$$

24

However **the shape of the signal depends on the thickness** d: thinner detectors have higher slew rate



Large signals from thick detectors?

(Simplified model for pad detectors)

Thick detectors have higher number of charges:

$$Q_{tot} \sim 75 q^*d$$

However each charge contributes to the initial current as:

$$i \propto qv \frac{1}{d}$$



The initial current for a silicon detector does not depend on how thick (d) the sensor is:

$$i = Nq \frac{k}{d} v = (75dq) \frac{k}{d} v = 75kqv \sim 1 - 2*10^{-6} A$$
Number of e/h = 75/micron
Weighting field
Velocity



Thick detectors have longer signals, not higher signals

We need to add gain

How gain shapes the signal

Time [ns]



Gain electron:

absorbed immediately Gain holes: long drift home



Electrons multiply and produce additional electrons and holes.

- Gain electrons have almost no effect
- Gain holes dominate the signal

➔ No holes multiplications

Interplay of gain and detector thickness

The rate of particles produced by the gain does not depend on d (assuming saturated velocity v_{sat})

Gain_





Constant rate of production

However the initial value of the **gain current depends on d** (via the weighing field)

$$di_{gain} \propto dN_{Gain} qv_{sat}(\frac{k}{d}) \rightarrow \text{Gain current} \sim 1/d$$

A given value of gain has much more effect on thin detectors



Significant improvements in time resolution require thin detectors





Ultra Fast Silicon Detectors

UFSD are LGAD detectors optimized to achieve the best possible time resolution

Specifically:

- 1. Thin to maximize the slew rate (dV/dt)
- Parallel plate like geometries (pixels..) for most uniform weighting field
- 3. High electric field to maximize the drift velocity
- 4. Highest possible resistivity to have uniform E field
- 5. Small size to keep the capacitance low
- 6. Small volumes to keep the leakage current low (shot noise)



Nicolo Cartiglia, INFN, Torino – Tracciatori 4D

UFSD: HPK time resolution

UFSD from Hamamatsu: 30 ps time resolution,

Value of gain ~ 20



Irradiation effects

Irradiation causes 3 main effects:

- Decrease of charge collection efficiency due to trapping
 - \rightarrow Very small in thin sensor
- Increased leakage current, shot noise → back up slides
- Gain layer disappearance → following slides

Gain layer de-activation

Unfortunate fact: irradiation de-activate p-dopin(removing Boron from the reticle

 $N(\emptyset) = N(\mathbf{0}) * e^{-c\emptyset}$





Boron Radiation creates interstitial defects that inactivate the Boron: Si_i + B_s → Si_s + B_i

Two possible solutions: 1) use Gallium, 2) Add Carbon



Gallium

From literature, Gallium has a lower possibility to become interstitial

Carbon

Interstitial defects filled with Carbon instead of with Boron and Gallium

Is the Boron still there?



Yes, **the Boron is still there**, but it is not active any more... **Instead of being "substitutional"** (i.e. in the place of a Silicon atom) **is "interstitial"** (i.e. In the middle of the lattice, not electrically active)
UFSD2: studio della resistenza alle radiazioni



- 1) Gallium is actually is less rad-hard than Boron
- 2) Carbon addition works really well, increasing by a factor of 2 the radiation hardness

Gain vs gain layer doping

Unfortunately, the gain is very sensitive to the doping level



Gain in irradiated sensors



the gain layer disappearance is compensated by external bias

UFSD: Time resolution after irradiation

FBK production of UFSD sensors: time resolution for different fluence



Very complex irradiation campaign, lot's of samples and fluences **Achieved:**

- Unchanged time resolution of ~ 35 ps, up to ~ 2E15 n/cm2
- Time resolution of 50 ps up to 3E15 n/cm2

Why noise in LGAD does not degrade time resolution?

Time resolution in LGAD is determined by jitter and charge non uniformity:

$$\sigma_t^2 = \left(\frac{N}{dV/dt}\right)^2 + \sigma_{Landau Noise}^2$$

The jitter term contains electronic noise and shot noise:

$$\mathsf{Jitter} = \frac{\sqrt{N_{el}^2 + N_{Shot Noise}^2}}{\frac{dV}{dt}}$$

Noise Aide memoire



Shot noise in LGAD - APD



Noise increase as a function of fluence and gain



10

100

1000

Gain

Goal: the noise from Silicon current should stay below that of the electronics

The role of the excess noise factor



Excess noise factor: noise of the multiplication process



$$F = Gk + \left(2 - \frac{1}{G}\right) * (1 - k)$$



Shot noise is actually dominated by the excess noise factor: at gain = 20 the excess noise factor more than doubles the shot noise without it

Shot noise and Jitter



The Jitter, instead of decreasing, is becoming constant due to the contribution of the shot noise.

Why noise is not a problem yet?

- Noise increase is driven by bulk current shot noise
- The calculated noise values are within 20% of the measured values
- The shot noise is large only when the gain and the bulk leakage current are

high \rightarrow not very common

- After irradiation, the gain decreases, and as a unintended consequence, the shot noise stays small.
- The values of shot noise are below the Landau noise

Electronics

To fully exploit UFSDs, dedicated electronics needs to be designed.

The signal from UFSDs is different from that of traditional sensors





Electronics: What is the best pre-amp choice?



The players: signal, noise and slope



There are 3 quantities determining the output rise time after the amplifier:

- 1. The signal rise time (t_{Cur})
- 2. The RC circuit formed by the detector capacitance and the amplifier input impedance (t_{\rm RC})
- 3. The amplifier rise time (t_{Amp})

Integrator or current amplifier?



- integrators work best with signals that are of the same length of their integration time
 - Current amplifiers work best with very fast signals

From one pad to a tracker

We have produced thousands of UFSDs, with many shapes, thicknesses, gains etc.. We know very well how a single pads and small array work, however....

Are we able to produce a full large tracker

- Uniformity
- Fill factor





Multi-pad sensors: TDCpix & FBK-UFSD

Bump-bonded NA62 TDCpix ROC to FBK-UFSD sensor NA62 ROC: 40x45 pads, each 300x300 μ m² (1800 pads)

- More than 99% of pads working
- Same voltage behavior as single pad: breakdown above 280 V
- First example of 4D tracking!



Hit map UFSD2 UFSD2 Bias = 280V run = 3

Fill factor

The fill factor is mainly determined by the inactive gap between sensors.



Current measured gap size:

- ~ 70 micron for CNM
- ~ 100 micron for HPK
- ~ 70 micron for FBK



This gap affects directly the detector acceptance as we have only one layer: a 70 micron gap corresponds to a 91% fill factor

Goal: 30 micron gap = 96% fill factor

Currently under study, looks possible...

Fill factor: optimization of current design

The gap is due to two components:

1) Adjacent gain layers need to be isolated (JTE & p-stop)

2) Bending of the E field lines in the region around the JTE area

Both under optimization Different junction termination/p-stop design

> CMS Goal: 30 micron gap = 96% fill factor



Fill factor solution 1: trenches

Trenches (the same technique used in SiPM):

- No pstop,
- No JTE \rightarrow no extra electrode bending the field lines



JTE + p-stop design

Trench design

Trench isolation technology

- Typical trench width < 1 um
- Max Aspect ratio: 1:20
- Trench filling with: SiO₂, Si₃N₄, PolySi

CMM CENTRE FOR MATERIALS AND MICROSYSTEMS





Fill factor solution 2: Resistive electrode



And now we need to build the detector..

CMS and ATLAS have now in their upgrade proposal the combined construction of ~ 20 m2 of UFSD sensors.

Main challenges:

- Sensor: moving from one pad to large area
- Electronics: need to design an appropriate chip



And: backend electronics, mechanics, cooling, HV & LV distribution, High precision clock, data transmission

ETL: Endcap Timing Layer

The chosen sensors for ETL are UFSD

one layer per side, each made of two disks

~ 1800 sensors:

microns



SENSORS: state of the art

UFSD: 35 ps resolution for fluences up to 5 - 6e14 neq/cm²

State-of-the-art sensors guarantee 80% of the ETL coverage

Low radiation : radius 55 – 110 cm ~2.8 m2 , ~80% High radiation: radius 25 – 55 cm ~0.7 m2, ~20%



There are several options to cover the additional 20%

- R&D will demonstrate new solutions \rightarrow next 18 months
- Allow degradation of resolution (~ 60 ps after 3000 fb-1)
- Have two layers



R&D in 5 years: space-time tracking at high rate

Imagine tracking with ~ 1000-2000 tracks @ 40 MHz crossing This situation is the pinnacle of complications..



Laboratory measurements

What do you do in a lab to characterize an LGAD?

CV LGAD vs PiN

Perché sono diverse?

C-V (LGAD) - WF6



LGAD Current with radiation



PiN - LGAD Current (Wafer14-Gallium 1.04)





UFSD – Summary

We are just starting to understand the timing capability of UFSD

- Low-gain avalanche diodes offer silicon sensors with an enhanced signal amplitude
- The internal gain makes them ideal for accurate timing studies
- We developed a program, Weightfield2 to simulate the behaviors of LGAD and optimized them for fast timing (available at <u>http://personalpages.to.infn.it/~cartigli/Weightfield2.0/</u>)
- Thin detectors enhance the effect of gain, several productions in progress

Sensor and electronics



Figure 5 Basic operational principles of a silicon detector: an external bias voltage polarizes the p-n junction inversely, creating a large depleted volume. When an incident charged particle crosses the sensor, it creates electron-hole pairs whose drift generates an induced current in the electronics.

particle crosses the sensor, it creates by ionization along its path electron-hole pairs, on average 73 pairs in each micron.

LGADs Pads, Pixels and Strips The LGAD approach can be extended to any silicon structure, not just pads. This is an example of LGAD strips + multiplication ostop AC3 implant metal 24µm p+ implant AC6 pstop 48µm 10um AC9 62µm

Energy deposition in Silicon Detectors

A charged particle looses energy in silicon, with a mean energy of ~ 80 keV in 300 micron.

The energy to create an e/h pair is 3.6 eV

80 keV/3.6 = 22,000 e/h pair

→ 75 e/h pair per micron



Time Resolution and slew rate

Using the expressions in the previous page, we can write

$$\sigma_t^2 = ([\frac{V_{th}}{S/t_r}]_{RMS})^2 + (\frac{N}{S/t_r})^2 + (\frac{TDC_{bin}}{\sqrt{12}})^2$$

where:

-
$$S/t_r = dV/dt = slew rate$$

- N = system noise

$$V_{th} = 10 N$$

Assuming constant noise, to minimize time resolution **we need to maximize the S/t_r term** (i.e. the slew rate dV/dt of the signal)

→ We need large and short signals

LGAD - Ultra-Fast Silicon Detector



Traditional Silicon Detector

Ultra-Fast Silicon Detector

Adding a highly doped, thin layer of of p-implant near the p-n junction creates a high electric field that accelerates the electrons enough to start multiplication. Same principle of APD, but with much lower gain.

Gain changes very smoothly with bias voltage.

Easy to set the value of gain requested.


The "Low-Gain Avalanche Detector" project

Is it possible to manufacture a silicon detector that looks like a normal pixel or strip sensor, but with a much larger signal (RD50)?

- 750 e/h pair per micron instead of 75 e/h?
- Finely Segmented
- Radiation hard
- No dead time
- Very low noise (low shot noise)
- No cross talk
- Insensitive to single, low-energy photon

Many applications:

- Low material budget (30 micron == 300 micron)
- Excellent immunity to charge trapping (larger signal, shorter drift path)
- Very good S/N: 5-10 times better than current detectors
- Good timing capability (large signal, short drift time)

Compensation with Vbias

The necessary field can be recovered by increasing the external Vbias: proven to work up to 5 10¹⁵ n^{eq}/cm²

Collected Charge [e]



How to use UFSD up to $5\sim 10^{15} n_{eq}/cm^2$

As the gain layer density decreases, we need to increase the external voltages to create the Efield needed for multiplications. In so doing, the gain moves from the gain layer to the bulk

Bias voltage to obtain Gain ~ 10 as a function of fluence



CNM W5 - 50 micron

Fluence [1e14 neq/cm2]

Sensor thickness and slim edge

Rule: when the depletion volume reaches the edge, you have electrical breakdown.

It's customary to assume that the field extends on the side by $\sim 1/3$ of the thickness.

edge = k* thickness

- k = 1 very safe
- k = 0.5 quite safe
- K = 0.3 limit



By construction, thin detectors (~ 100 micron) might have therefore slim edge