Welcome to ESC17

the Ninth INFN International Continental School on

"Architectures, tools ad methodologies for developing efficient large scale scientific computing applications"

Oct. 23-28 2017

Mauro Morandin - INFN-Padua

On behalf of:

- **◆ The Italian Institute for Nuclear Physics (INFN)**
 - x Bologna, CNAF, Padova sites
 - x providing most of the support
- ◆ The University of Bologna, Department of Physics
- **◆ The lecturers, the tutors**
 - x and their Institutions/Companies

The site

Bertinoro and hospitality

Bertinoro is a nice medieval village, famous for its **hospitality** and therefore quite well suited as a location for a center like CeUB

* the name most likely comes from "Castrum Brittinori" (XIth century) and it is probably due to the frequent stops pilgrims coming from Britain, in their way to Rome, used to take in the quiet surroundings of Bertinoro.

- * one of the monument in Bertinoro known as Colonna delle Anelle ("Column of the Rings" or "Column of hospitality") is a column in white stone with 12 rings erected in 1300 by the noble families to express their commitment to hospitality.
 - ◆ Each one of the rings corresponded to one family;
 foreigners arriving in town, could select the family to be hosted, by tying the horse bridles to the correspongind ring

but still today...

- * Bertinoro still hosts a "Hospitality Festival". Held in the first weekend of September, it includes an entire night of music, dances and events (between Friday and Saturday), some historic commemorations and the final Hospitality Rite (Sunday late morning).
- In this ceremony anyone can be hosted for lunch by a family in the town simply taking one of the envelopes tied to the rings of the Hospitality column (which inside has the name of the hosting family).
- you will not get this opportunity this week, but I'm sure you will at least enjoy the good food and wine that Bertinoro will offer to you while you are staying here



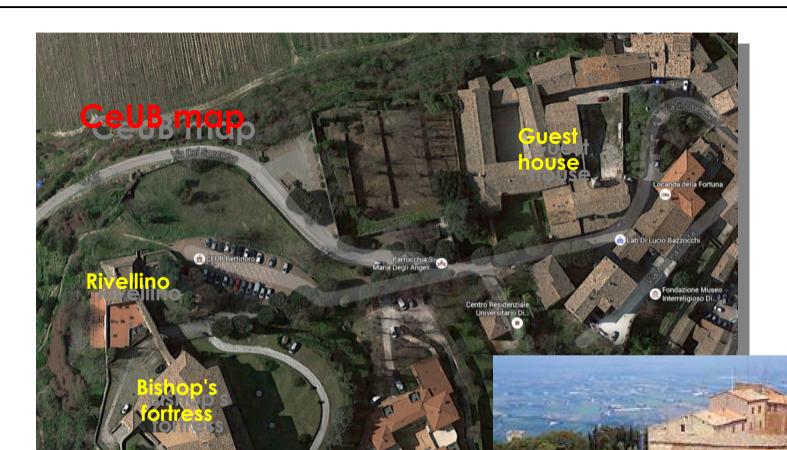


BERTINORO
BERTINORO, FESTA
DELL'OSPITALITÀ
2017

ITALITÀ

mbre 2016

CeUB: University Residential Center of Bertinoro



Visit of the fortress and the Interfaith museum Wednesday at 2:30 pm

CeUB: the facilities

- the Center was brought back in use with renovation work that started in 1991 in the fortress and in the guest house, followed by integration of additional buildings
- today the Center offers:
 - x 14 lecturing/meeting rooms inside the Bishop's Fortress, the Rivellino, St. Sylvester church and the Theatre;
 - x 2 computer labs, 20 and 50 seats;
 - **x 86 bedrooms** (single, twin, double), for a total amount of 130 beds;
 - x a canteen with 200 seats for breakfast, lunch and gala dinners.
- ◆ 120+ events per year
- up to 30.000 daily presences per year

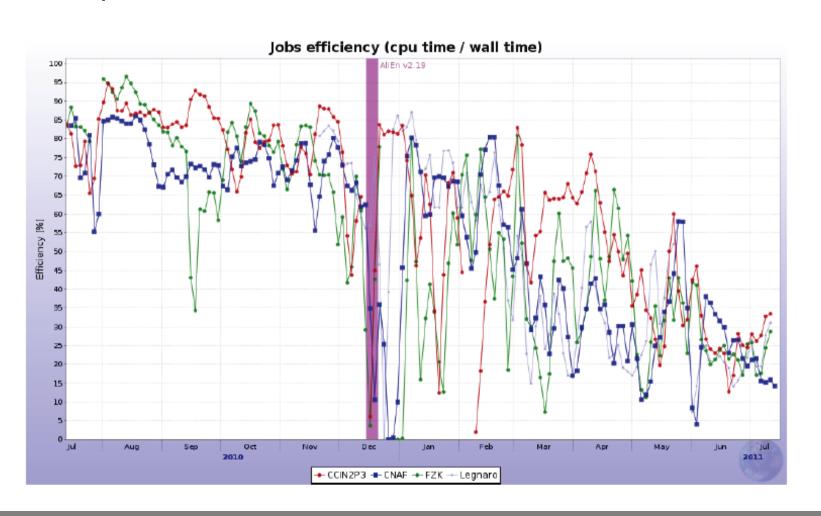
Why this School

The context

- High Energy Physics has been heavily relying on computing since long
 - * for many years the scale of resources needed by HEP experiments was such that the computing centers of the hosting lab were copying well with the core computing data processing needs
 - x the UA1/2 experiments that discovered the Z and W bosons at CERN were good examples
- the model started to break at the end of the last century when experiments, like the BaBar experiment at the SLAC B-factory, had to deal with a huge amount of data and the computing power had to be scaled up by more than one order of magnitude w.r.t. the initial estimates
 - x the investment needed started to grow very significant
 - x for the first time it was felt necessary to distribute the processing of the data stored on tape to en external center

more recent experience

x an example from a LHC experiment of how CPU efficiency may drop when real data come in



Why a School about "efficiency"

- ◆ The conception of this school was motivated by the awareness that efficient usage of computing resources in our field:
 - x had to be taken seriously, given the level of computing investment now required
 - x in the past was not always well understood and taken into proper consideration
 - was becoming more and more challenging due in particular to the physical constraints in increasing scalar performance and the attempts to exploit anyway Moore's law with new processor architectures

 Chip Maximum
 Power in watts/cm²

 Chip Maximum
 Power in watts/cm²

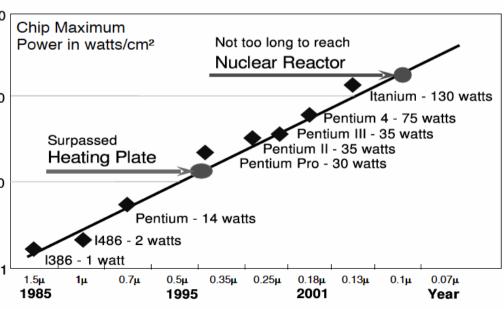
 Not too long to reach
 Nuclear Reactor

 Pentium 4

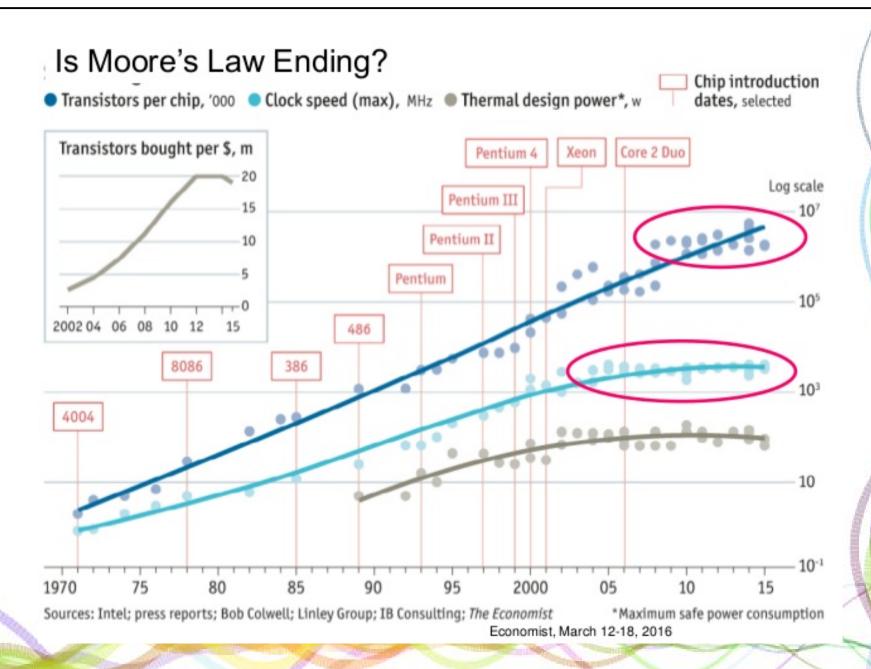
 Pentium 4

 Pentium 4

 Pentium 111 38
 - ◆ ... many cores, co-processors, 10
 GPU, vector units, etc
 - memory access getting more and more critical



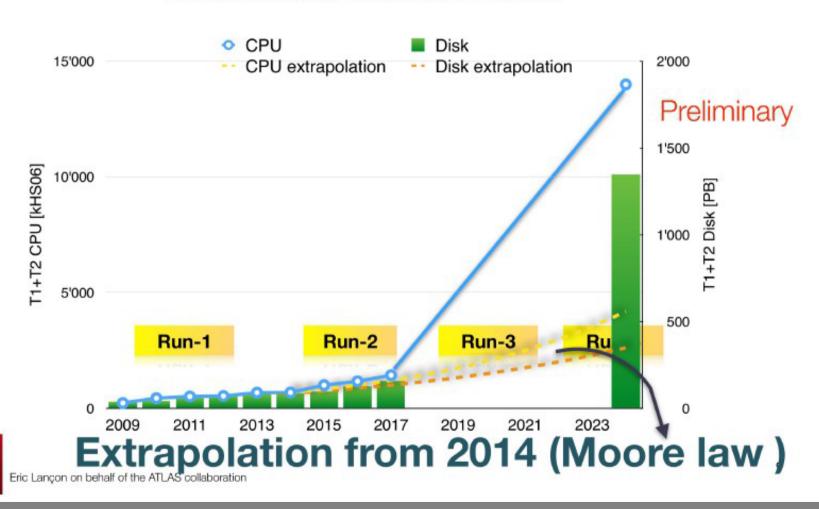
Why a School about "efficiency" (II)



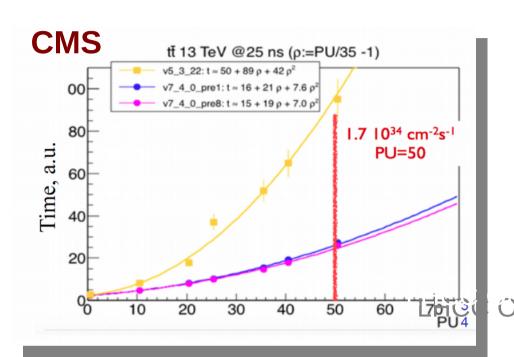
Future may be even more challenging

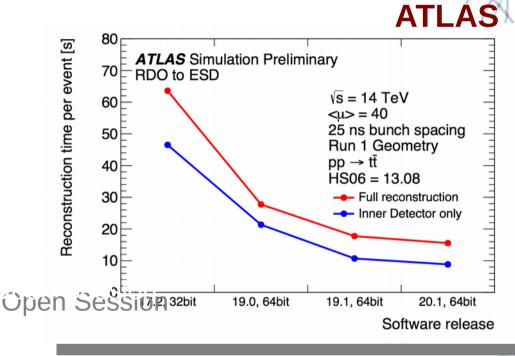
cea

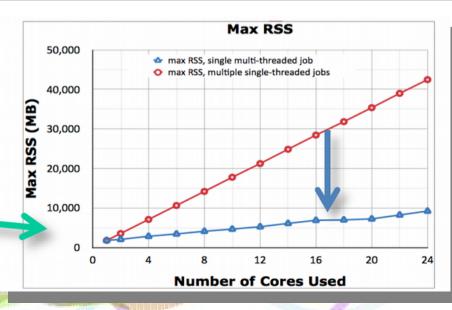
ATLAS resource needs at T1s & T2s



the pay off: recent achievements at LHC







lan Bird LHCC Open Session sep. 15

a look at HPC computing

- the next big milestones for HPC systems is Exascale computing
 - x 1000 PFlops, 1000 PBytes
- we are progressing towards the goal, although at reduced pace
- however, no supercomputer runs real applications faster than five percent of its design speed
- the natural trend is towards
 poorer and poorer efficiencies as systems scale out to Exascale
- "it is not power or reliability that are the exascale challenges: it's programmability of complex memory hierarchies"



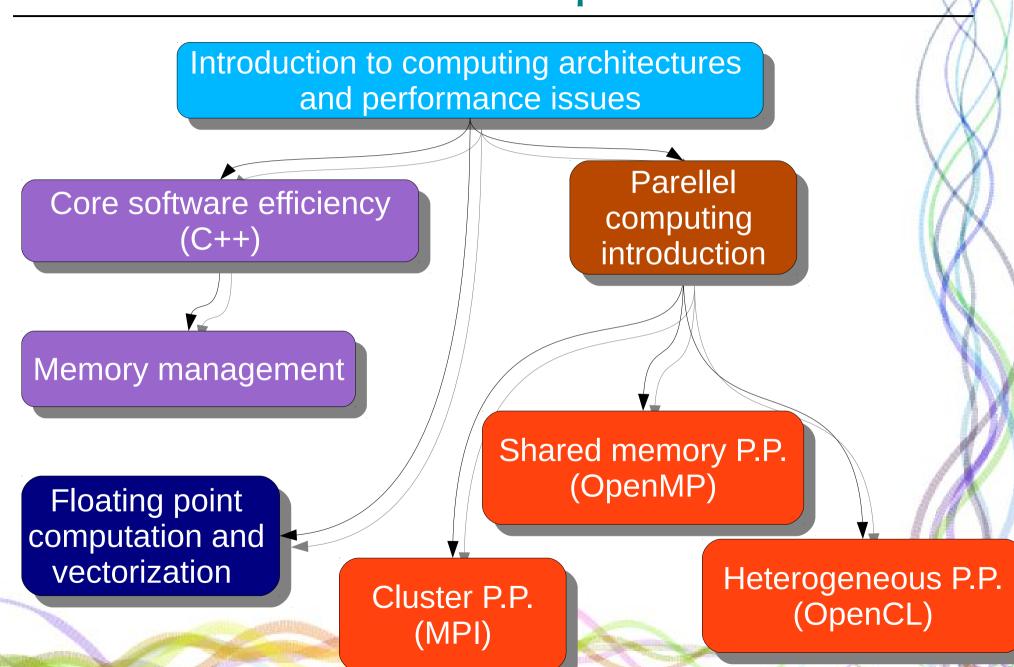
Supinski - ISC HPC '15 - FrankfurtConference and Exhibition in Frankfurt in July.

The School evolution

- ◆ In the first editions of the School we tried to embrace several key aspects related to the efficient usage of computing resources in scientific applications, from exploitation of modern CPUs to I/O related issues
- however we realized that the scope was too vast for a one week long School
- ◆ a few years ago we decided therefore to focus the School on the area where developments looked most disruptive and challenging: memory management and parallel processing

The 2017 edition

ESC16 School lecture plan



Improving the School

- we have been striving to improve the School year after year
- the feedback we got from the students has always been very useful
- on Saturday morning we will give you an opportunity to evaluate:
 - x the perceived quality of various aspects of the School
 - ◆ feedback questionnaire
 - x the competences you have acquired
 - ◆ final test
 - ◆ a piece of information that is of course valuable to us too

Consolidation time

 to accommodate the need of students had to have some time for assimilating the material presented in the lectures there are some consolidation time slots for a total of 9h30', i.e.:

x Mo.: 30¹

x Tu.: 1h30'

x Wed., Fri.: 2h30'

x Thu.: 2h45'

Evening lectures

- ◆ Thursday:
 - x Piero Altoè Nvidia:

Exploitation of parallel processing in AI applications

- ◆ Friday:
 - x Tim Mattson Intel

Surviving the Red Queen's Race: A guide for the perplexed programmer

The week together

- thank you for preparing the lightning talks and bringing here your posters!
 - x we have received slides from most participants (21)
 - ◆ please send us the the missing slides by 2 pm today
- ◆ after the lectures... you can stay here until dinner time
- dinners: on Mon., Tue., Wed. you can organize yourself and enjoy what local restaurants have to offer
 - x a list of restaurants has been distributed together with the vouchers worth 25 €
 - check which restaurant are closed on Mondays!
 - x if you tell the restaurant that you have the voucher they will usually propose a fixed-cost menu
- there is a large room in the Guest House that is available if you want to stay there together after dinner

Special events

On Thursday night

- special dinner at the "Casa Artusi"* restaurant in Forlimpopoli
 - x two menus available: **meat** or **fish**
 - * please make your choice by today at noon and report any special food requirement

On Wednesday afternoon:

Visit to the Bertinoro Fortress and the InterFaith museum

On Friday night:

social dinner "da Serafina"

*Pellegrino Artusi: "The Science of Cooking and the Art of Fine dining - 1891"

School computing infrastructure

- we believe that:
 - x learning-by-doing is an effective way of learning
 - * but it is also an effective way of teaching
 - guiding students to discover by themselves new notions and concepts
- therefore a computing infrastructure suitable for supporting hands-on activities has been setup for you
 - x located at the Padova INFN site
- this year:
 - * each of you will get exclusive access, through a public login gateway (esc-gw.pd.infn.it), to a couple of Intel dual processors (Intel E5520) Linux servers
 - you will share the access to a server equipped with 8 Nvidia
 GPU (Tesla \$2050) for the OpenCL exercise

Wireless networks

- we have here two infrastructures:
 - ◆ INFN wireless network, available only in this room, accessed
 - x using Eduroam credentials [eduroam SSID]
 - x or via captive portal [INFN-Captive SSID]
 - you should already have the credentials
 - ◆ Bertinoro wireless network, available available in all rooms including the guest house [CeUB SSID]
 - x credentials should have been given at registration time, otherwise ask Rossana

Last but not least



We wish you a very pleasant and "efficient" week in Bertinoro