

**EcoScale - ExaNeSt training
workshop on FPGA
implementation of OpenCL**

Report of Contributions

Contribution ID: 0

Type: **not specified**

INFN Application (Brain Simulation)

Friday, 17 March 2017 14:30 (15 minutes)

Primary author: PASTORELLI, Elena (ROMA1)

Presenter: Dr PASTORELLI, Elena (INFN - Rome)

Contribution ID: 1

Type: **not specified**

INAF Applications (Astrophysics)

Friday, 17 March 2017 14:45 (15 minutes)

Primary author: Dr GOZ, David (OATS-INAF Trieste)

Presenter: Dr GOZ, David (OATS-INAF Trieste)

Contribution ID: 2

Type: **not specified**

Exact Lab Applications (Material and Climate Science)

Friday, 17 March 2017 15:00 (15 minutes)

Primary author: Dr BRANDINO, Giuseppe Piero (Exact-Lab)

Presenter: Dr BRANDINO, Giuseppe Piero (ExaCt-Lab)

Contribution ID: 3

Type: **not specified**

EnginSoft Applications (Engineering) TBC

Primary author: Dr PERNA, Gino

Contribution ID: 6

Type: **not specified**

ECOSCALE architecture and OpenCL tasks acceleration in hardware (I. Mavroidis)

Contribution ID: 7

Type: **not specified**

ECOSCALE architecture and OpenCL tasks acceleration in hardware (I. Mavroidis)

Friday, 17 March 2017 09:00 (1h 50m)

Primary author: Dr MAVROIDIS, Iakovos (TSI-FORTH)

Presenter: MAVROIDIS, Iakovos

Contribution ID: 8

Type: **not specified**

Hands-on: programming TRENZ system (K. Georgopoulos)

Contribution ID: 9

Type: **not specified**

Hands-on: programming TRENZ system (K. Georgopoulos)

Friday, 17 March 2017 11:10 (1h 50m)

Primary author: Dr GEORGOPOULOS, Kostas

Presenter: KOSTAS, Georgopoulos (FORTH)

Contribution ID: **10**

Type: **not specified**

Open discussion and brainstorming on EcoScale OpenCL-generated blocks in ExaNeSt infrastructure

Friday, 17 March 2017 15:15 (1h 15m)

Contribution ID: 11

Type: **not specified**

OpenCL and Xilinx methods tutorial (1)

Thursday, 16 March 2017 09:05 (1h 15m)

Primary author: Prof. LAVAGNO, Luciano (Politecnico di Torino)

Presenter: Prof. LAVAGNO, Luciano (PoliTO)

Contribution ID: 12

Type: **not specified**

OpenCL and Xilinx methods tutorial (2)

Thursday, 16 March 2017 10:40 (2h 20m)

Primary author: Prof. LAVAGNO, Luciano (Politecnico di Torino)

Presenter: Prof. LAVAGNO, Luciano (PoliTO)

Contribution ID: 13

Type: **not specified**

OpenCL and Xilinx methods hands-on session (Lab1)

Thursday, 16 March 2017 14:30 (1h 30m)

Presenter: Prof. LAVAGNO, Luciano (PoliTO)

Contribution ID: 14

Type: **not specified**

OpenCL and Xilinx methods hands-on session (Lab2)

Thursday, 16 March 2017 16:20 (1h 40m)

Presenter: Prof. LAVAGNO, Luciano (PoliTO)

Contribution ID: 15

Type: **not specified**

Opening

Thursday, 16 March 2017 09:00 (5 minutes)