

JUNO Electronics Workshop 2017

Thursday, 20 April 2017

Afternoon session - Aula Emiciclo (14:30 - 16:00)

time	[id] title	presenter
14:30	[15] Base design for integration test and signal clamping	Dr LUO, Fengjiao
14:55	[16] Thermal test and potting	Dr XU, Meihang
15:10	[17] Trigger status report	Prof. GONG, Guanghua
15:40	[18] Clock distribution	Prof. GONG, Guanghua