Ideas and proposals from Melbourne

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Aims & proposals of Melbourne group

- To contribute to miximise the likelihood of on-time delivery of the AM08 (and eventually 2020) for the phase-II track trigger in VHDL programing, digital simulation and characterisation
 - Will streamline whole VHDL related steps (programming (static timing analysis) – digital simulation)
 - "Specification based" approach
 - Jafar will continue to take major role in the characterisation
 - We plan to facilitate a lab with some graduate students
- To set up a collaboration of interdisciplinary application of AM envisioning to obtain Australian fund
 - Explore needs and resources in Australia
 - Primarily targeting Australian Research Council (ARC) funds,
 which will open during October 2017 March 2018 (next page)

Core funding opportunities

- ARC Future Fellowship (open mid October):
 - 4 year fellowship for mid career researcher
 - \$800,000 for personnel (including CI salary), travel and small facility
 - Success rate ~15%
- ARC Discovery Project (open early December):
 - 1-5 year project with PI (including international PI)
 - \$350,000 (typical) for personnel, travel and small facility
 - Success rate ~18%
- ARC DECRA (open mid January):
 - 3 year fellowship for early career researcher
 - \$350,000 for personnel (including CI salary), travel and small facility
 - Success rate ~15%