

cherenkov telescope array



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# Innovative design of a camera for IACT telescopes

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#### The context

- The Cherenkov Telescope Array will be composed of 3 different sizes of telescopes: Large (LST), Medium (MST) and Small (SST)
- Different optics and a camera designs are proposed for these telescopes
- The Geneva group is the leader of one of the proposed SST telescopes using a Davies-Cotton single mirror design and proposing an innovative fully digital camera based on SiPM
- The camera presented here is the design proposed for this Single mirror Small Size telescope (SST-1M)







Large scale ma	iss production High yield	No Ageing Low Op	Lightweight . Voltage
Higher PDE over larger spectral range			
Robust	<b>Insensitive to Magnetic Field</b>		
Compact	Very goo	od Single Phot	on Response
Small variation sample-by sample			



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#### Why SiPM

- High Quantum Efficiency
- Single Photon sensitivity
- Fast pulses
- Low noise
- High Fill factor
- Robustness
- Uniformity
- High dynamic range
- · Large area to be covered
- Linear response



#### Why SiPM

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#### 40 mm





#### Why SiPM

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- High dynamic range
- · Large area to be covered
- Linear response (optical cross talk, pile up)
- · Lower voltage and easier cooling
- Lightweight
- High potential for performance improvement and cost decrease
- Characteristics depend on operation temperature



#### 40 mm







#### Improve



- Fast pulses
- · Low noise (dark count, afterpulses)

- Large area to be covered
- Linear response (optical cross talk, pile up)

Are these parameters worrisome for gamma ray astronomy ?

Characteristics depend on operation temperature



### Why SiPM in gamma-ray Astronomy

- Excellent single PE sensitivity
- Lightweight and robust cameras
- No evidence of ageing after 18 months
- Night Sky Background (NSB) rate dominates wrt Dark noise (MHz)
- Current Photo-Detection Efficiency > 40%.
- Operation during Moonlight: ~30% larger duty cycle
- As demonstrated by FACT, SiPM work on the field and with moonlight!)

## New approach, use fully digital SiPM-based camera on a Davies-Cotton telescope.



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#### **Telescope Design Drivers**



#### **PSF** OF A 4M DAVIES-COTTON



![](_page_8_Picture_4.jpeg)

#### **Telescope Design Drivers**

#### Too big for a SiPM!!

![](_page_9_Figure_2.jpeg)

![](_page_9_Figure_3.jpeg)

![](_page_9_Picture_4.jpeg)

#### Winston cones

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#### We need an Hollow cone to improve the response in the UV

![](_page_10_Figure_2.jpeg)

#### Prototypes and first measurements

![](_page_11_Figure_1.jpeg)

![](_page_11_Picture_2.jpeg)

- Measurement done for different wavelengths
- Simulation of the set-up to validate the coating
- Very good agreement between simulation and measurement
- Coating qualified also with 40 thermal cycles (from -15° to +30°) - no measurable effect
- The collection efficiency shown here does not take into account the effect of the entrance window

#### Related paper:

J.A. Aguilar et al., Design, optimization and characterization of the light concentrators of the single mirror small size telescopes of the Cherenkov Telescope Array, Astroparticle Physics, doi:10.1016/j.astropartphys.2014.05.010

#### Prototypes and first measurements

![](_page_12_Figure_1.jpeg)

#### The S12516-050 sensor

![](_page_13_Picture_1.jpeg)

![](_page_13_Picture_2.jpeg)

![](_page_13_Picture_3.jpeg)

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### The Hexagonal Sensor

- Despite the use of the light concentrators, the pixel size remains large compared to common devices,
- Result of a collaboration between DPNC University of Geneva and Hamamatsu, the sensors are large hexagonal arrays of G-APD.

![](_page_14_Figure_3.jpeg)

![](_page_14_Picture_4.jpeg)

mm

### The Camera concept

![](_page_15_Figure_1.jpeg)

- Separation of PDP and Digital Readout
  - Separate mechanics and power supplies
  - Analogue signals over CAT6/RJ45
  - DC coupling for NSB monitoring
- Window and chassis sealed, IP65
- Water cooled Heat pipes on Digital board
- Compact, robust, lightweight and selfcontained

![](_page_15_Picture_9.jpeg)

![](_page_15_Picture_10.jpeg)

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![](_page_16_Picture_9.jpeg)

![](_page_16_Picture_10.jpeg)

### **Photo Detection Plane**

- 1296 pixels, 108 modules (12 pixels each)
- Power consumption ~ 500 W
- Total weight 35 kg
- Borofloat entrance window 3.3 mm coated with AR filter (Cut-off at 540 nm)
- Aluminum backplate (6 mm) as backbone and cold plate for Cooling
- Sensor bias automatically adjusted according to temperature (reso. of 0.17 deg)

![](_page_17_Picture_7.jpeg)

![](_page_17_Picture_8.jpeg)

#### Borofloat window

![](_page_17_Picture_10.jpeg)

#### **Assembly - Photo Detection Plane**

1 day of work to assemble full PDP

![](_page_18_Picture_2.jpeg)

![](_page_18_Picture_3.jpeg)

4

#### **Assembly - Photo Detection Plane**

1 day of work to assemble full PDP

![](_page_19_Picture_2.jpeg)

![](_page_19_Picture_3.jpeg)

4

### The Front-end electronics

The pre-amp boards

- read and pre-amplify the analog signals
- routes out signals, the HV and the output of the NTC probe, present on the sensor package.

#### SlowControl Board

- route pixel signals to Digicam via the RJ45 connector
- distributes the power and the HV
- regulate HV for each pixel
- Use a micro controller to change HV of each pixel to compensate temperature variations measured by the NTC probe in sensor package
- Board accessed via CAN-bus

![](_page_20_Picture_10.jpeg)

![](_page_20_Picture_11.jpeg)

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![](_page_21_Picture_10.jpeg)

![](_page_21_Picture_11.jpeg)

#### Slow control Board

![](_page_22_Picture_1.jpeg)

![](_page_22_Picture_2.jpeg)

- The slow control board has the following task
  - route pixel signals to Digicam via the RJ45 connector
  - provide the bias and
  - change the bias to compensate for temperature variations
- a micro controller supervises the temperature compensation
  - look-up table is loaded with correction
  - each pixel has its own correction
  - temperature reading
- Board accessed via CAN-bus

![](_page_22_Picture_12.jpeg)

### Commissioning - Design validation

![](_page_23_Figure_1.jpeg)

#### Control:

- ✓ Enable boost for bias
- ✓ Activate bias voltage
- ✓ Reference voltage and temperature setting
- Temperature variation factor

#### Monitoring of:

- Temperature and bias voltage
- ✓ Compensation loop

![](_page_23_Picture_11.jpeg)

### **Digital readout and trigger / DigiCam**

![](_page_24_Figure_1.jpeg)

![](_page_24_Picture_2.jpeg)

- Sampling rate 250 MHz
- Readout rate: 32 kHz @ 80 ns readout window, no dead-time
- Fully digital trigger and readout (High-speed/High-throughput)
- Serial architecture based on multi-Gigabit links (trigger and ADC readout)
- Trigger path with reconfigurable algorithms and signal preprocessing

![](_page_24_Picture_8.jpeg)

- Based on last generation of High performance Xilinx FPGA  $\Rightarrow$  Flexibility, High
  - Speed, Low power consumption
    - ➡ up to **56 GTX** transceivers, max 12.5Gbit/s
    - ➡ up to 96 GTH transceivers, max 13.1Gbit/s
- Using DDR expandable RAM for data buffering  $\Rightarrow$  Upgradable, handle high

![](_page_25_Figure_6.jpeg)

- 36 high speed 8Gbit/s for crate trigger data
- 18 low speed 1Gbit/s for readout and slow control

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![](_page_26_Picture_6.jpeg)

#### **BackPlane with 54 differential pairs**

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![](_page_28_Figure_6.jpeg)

• 18 - low speed 1Gbit/s for readout and slow control

![](_page_28_Picture_8.jpeg)

### Assembly - Water Cooling system

![](_page_29_Picture_1.jpeg)

![](_page_29_Picture_2.jpeg)

![](_page_29_Picture_3.jpeg)

- Cooling pipes installed prior to any electrical system
- Water tightness checked
- Epoxy resin applied on joints for safety

![](_page_29_Picture_7.jpeg)

![](_page_29_Picture_8.jpeg)

#### Assembly - Water Cooling evetors fast connectors for inlet/outlet camera

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![](_page_30_Picture_2.jpeg)

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#### Assembly - Water Cooling system fast connectors for inlet/outlet camera

### Sector manifold

Camera manifold

Water tightness checked
Epoxy resin applied on joints for safety

![](_page_31_Picture_3.jpeg)

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hiller

#### **Digicam Cooling**

![](_page_32_Picture_1.jpeg)

2 Heat pipes (25W each) per board coupled to a water cooled plate

![](_page_32_Picture_3.jpeg)

![](_page_32_Picture_4.jpeg)

 Board can be removed from the crate with limited intervention on the cooling system

Micro-crate mounted at 45° to have always optimal heat-pipes efficiency

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#### Camera Chassis IP65 Test

- Camera tested against IP65 Specs
  - to check even tiny leaks the internal surface of the camera was covered with a special paper which change color in case it comes in contact to water
- No water leak but on 2 points on the patch panel
  - Due to a wrong machining of the holes
  - · cured with a small joint
- Another test is scheduled next week to qualify the new solution

![](_page_34_Figure_7.jpeg)

![](_page_34_Picture_8.jpeg)

![](_page_34_Picture_9.jpeg)

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![](_page_35_Figure_7.jpeg)

![](_page_35_Picture_8.jpeg)

![](_page_35_Picture_9.jpeg)

#### Camera Chassis IPX5 Test

- Camera tested against IP65 Specs
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![](_page_36_Figure_7.jpeg)

![](_page_36_Picture_8.jpeg)

![](_page_36_Picture_9.jpeg)

### Camera Safety - Housekeeping Board

- to have as many possible probes but not having lots of cables from camera to the PLC we designed an Housekeeping board
- It can host 3 different type of sensors
  - Temp sensors
  - Humidity sensors
  - Temp+hum integrated sensor

![](_page_37_Figure_6.jpeg)

![](_page_37_Picture_7.jpeg)

- It can be connected to the PLC with 2 different protocols:
  - RS485 or CanBus
- it can trigger alarm to PLC in case of problems
- it will also monitor the power supply

![](_page_37_Picture_12.jpeg)

![](_page_38_Figure_1.jpeg)

#### Charge resolution measurement:

- Key parameter to assess quality of image reconstruction (Hillas parameters)
- Measurement performed injecting both pulsed and continuous light
- Different NSB level from dark night to half-moon (45° off-axis)

![](_page_38_Picture_6.jpeg)

Linear

![](_page_39_Figure_1.jpeg)

25

![](_page_40_Figure_1.jpeg)

![](_page_40_Figure_2.jpeg)

![](_page_41_Figure_1.jpeg)

26

![](_page_42_Figure_1.jpeg)

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![](_page_43_Figure_1.jpeg)

charge resolution

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![](_page_44_Figure_1.jpeg)

charge resolution

![](_page_45_Figure_1.jpeg)

![](_page_46_Figure_1.jpeg)

#### First operations of the camera

![](_page_47_Figure_1.jpeg)

#### First operations of the camera

- A camera test set-up has been realised.
- 1 sector (1/3 of the camera) + the centre of the camera can be tested at the same time
- For each pixel, 2 LEDs are present: one flashing (AC) to emulate Cherenkov flashes and one DC to emulate NSB.
- Cherenkov shower-like shapes can be produced to verify and validate trigger algs and efficiency
- The set-up is very flexible and allow to check the cabling/ mapping, the working pixels, the flat fielding, charge resolution for each individual pixel, calibrations, trigger algs

![](_page_48_Figure_6.jpeg)

### Pixels scan to check mapping and working pixels

![](_page_48_Picture_8.jpeg)

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![](_page_49_Figure_6.jpeg)

### Pixels scan to check mapping and working pixels

![](_page_49_Picture_8.jpeg)

#### Conclusions

- SiPM use is spreading in many fields given their advantages.
- SiPM are particularly fit for gamma-ray astronomy,
  - Operation during Moonlight ~ 30% larger duty cycle
  - No evidence of ageing
  - Lightweight and robust cameras
  - Excellent single PE sensitivity
  - High Photo-Detection Efficiency at ~ 40%
- SST-1M camera goes in this direction but tried to open a new road towards large area devices.
  - Custom designed hexagonal device in collaboration with Hamamatsu
  - Large Area devices are complicated to handle but can be done!
- Many Lessons learned
  - SiPM parameter spread verified and validated
  - · Large capacitance can be mastered
- The camera is under test . Very soon result on the field with real data.... stay tuned
- The solution worked out for CTA can be exploit without major modification both in larger CTA telescope and LHAASO WFCTA.
- Now under evaluation last generation of FBK sensor to see if they can work in our approach at room temperature.

![](_page_50_Picture_17.jpeg)

![](_page_51_Figure_0.jpeg)

Looking for seeing real shower!!!

![](_page_52_Figure_0.jpeg)

Looking for seeing real shower!!!