
Detecting Signals into the Noise: Franco Manfredi in memoriam

5 December 2016

Aula Volta, University of Pavia , Pavia, Italy

Toward the Ultimate Low-Noise Front-End Electronics for X- γ Ray Detectors

The Heritage of *Franco Manfredi* to Current Research

Giuseppe Bertuccio

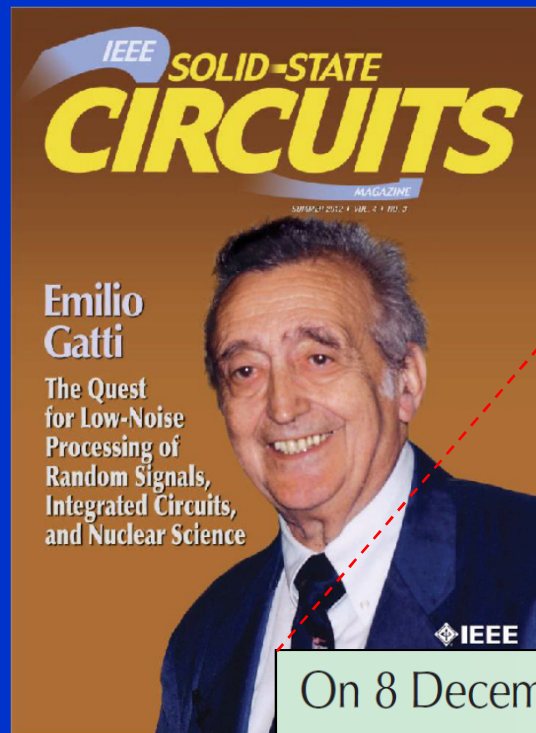
Politecnico di Milano - Italy

Department of Electronics, Information and Bioengineering

and

National Institute of Nuclear Physics - Milano

2012 : Franco Manfredi Last Letter



A DECISION OF WHICH I AM STILL PROUD

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So, shortly after graduation, I asked Prof. Gatti for an appointment, and I went to Milan to meet him. I intended to ask him about a possible job opening in his group. He offered me a position of junior assistant, which I accepted with enthusiasm. I began working at the Polytechnic of Milan in the final days of December 1959. I soon realized what an extraordinary leader Emilio Gatti was. Reporting to him was, indeed, very stimulating. He taught me how to analyze the behavior of a circuit, and at that time I realized that there is something adding in the study of a circuit. That

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Prof. Gatti had a noticeable influence on the research directions I've pursued, for instance by transferring his passion for the noise problems to me. My career continued at the Polytechnic of Milan, where in 1976 I became full professor. Two years afterwards I left the Polytechnic and moved to the University of Pavia as a professor of electronics. However, Emilio and I continued our scientific cooperation. We have worked together on several subjects. After I had moved to Pavia, we used to meet whenever possible, and weekends, holidays and evenings were good opportunities for working. Our collaboration kept going on even when I moved to Lawrence Berkeley National Laboratory. All I can say is that I am very proud of the decision I made when I was still a student to join

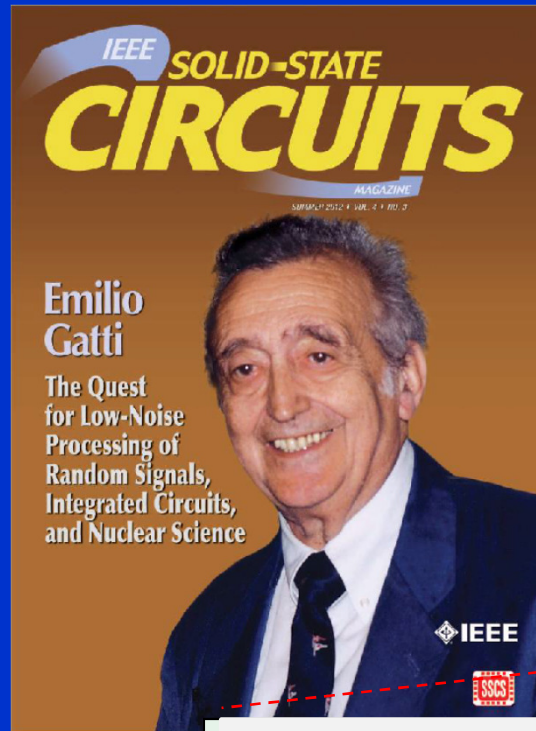
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joined by the faculty to switch from the courses in nuclear electronics, which he had been teaching for years, to a course in physics, which was much more demanding in terms of workload for a professor. He asked me to teach one of the courses in nuclear electronics. I was too young to be fully independent in such a task, and because of this, Prof. Gatti had to assure the faculty that he would maintain responsibility for the courses in nuclear electronics. He did this, in my case, by attending nearly all the

my university career at the Polytechnic of Milan where, in 1976, he became a professor of nuclear electronics. He was professor in two more Italian universities, Milan and Pavia, until 1997, when he joined the Lawrence Berkeley National Laboratory in Berkeley, California, as a senior staff scientist. His primary research interests include low noise circuits and systems and front-end circuits based on low-noise monolithic technologies. Part of his activity was done in collaboration with Brookhaven National Laboratory of Upton, New York, and Max Planck Institute of Munich, Germany. He has published more than 100 papers in international journals.

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Date of publication: 13 August 2012

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—Pier F. (Franco) Manfredi

Emilio Gatti Research Group in '90

at Politecnico di Milano

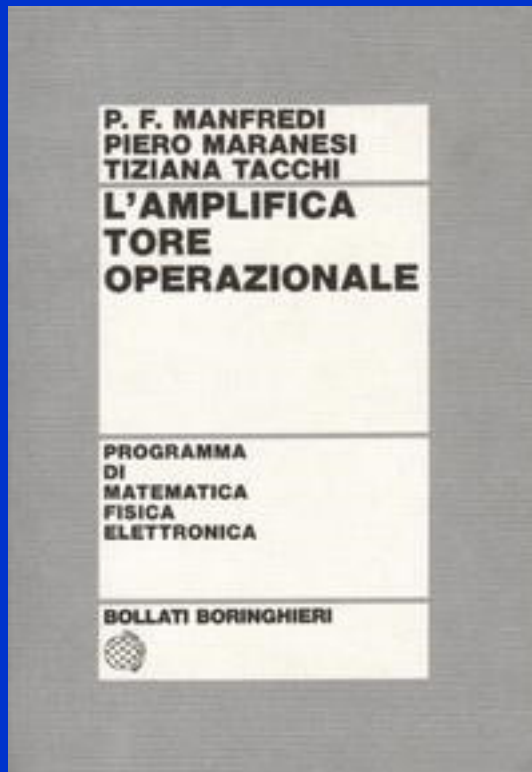


at a Conference...



1983: My first knowledge of Prof. Franco Manfredi

as student of nuclear engineering at Politecnico di Milano



June 1994

coming back from Isola d'Elba
Conference “Frontier Detectors for Frontier Physics”

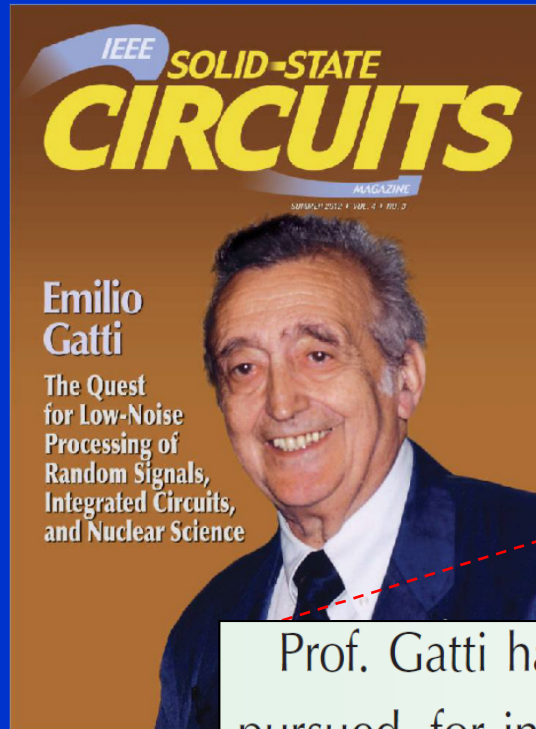


F. Manfredi

Z. Li (BNL)

G. Bertuccio

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Toward the Ultimate Low-Noise Front-End Electronics for X- γ Ray Detectors

the Heritage of *Franco Manfredi*
to Current Research

Franco Manfredi and the electronics for X- γ ray spectroscopy

1978: Preamplifier for X-Ray

NUCLEAR INSTRUMENTS AND METHODS 156 (1978) 553-557 ; © NORTH-HOLLAND PUBLISHING CO.

X-RAY PREAMPLIFIER EMPLOYING PULSED DRAIN FEEDBACK

C. BUSSOLATI, P. F. MANFREDI, D. MARIOLI

Politecnico di Milano, Sez. di Brescia, I.N.F.N., Sez. di Milano, Italy

and

R. KRASOWSKI

Academy of Mining and Metallurgy, Cracow, Poland

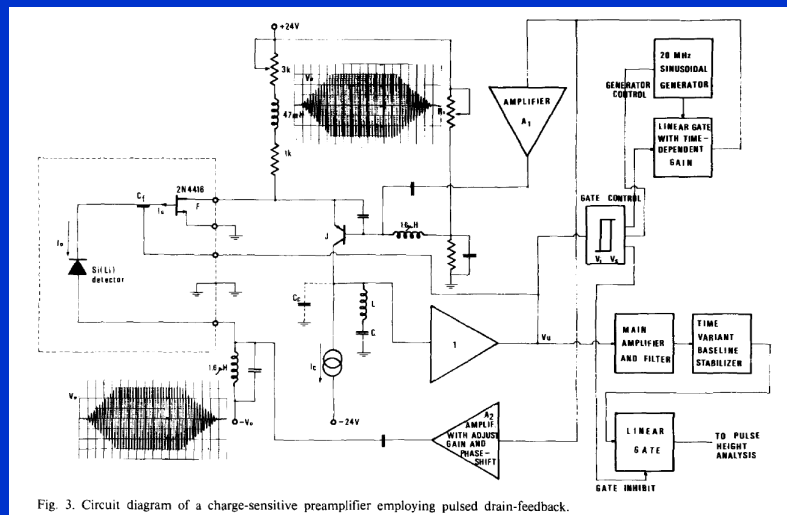


Fig. 3. Circuit diagram of a charge-sensitive preamplifier employing pulsed drain-feedback.

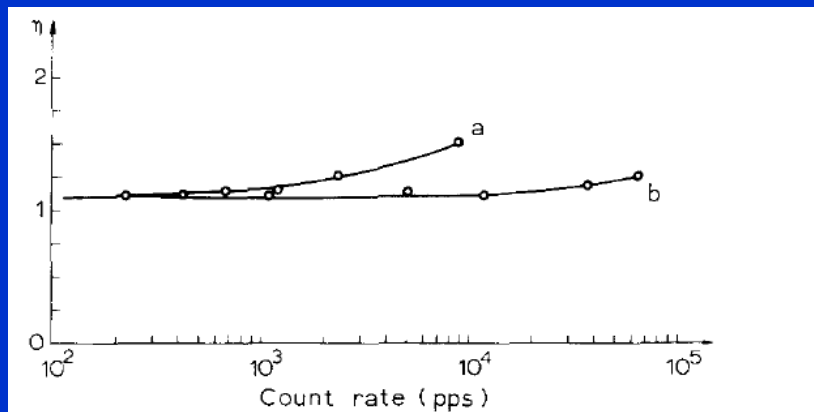
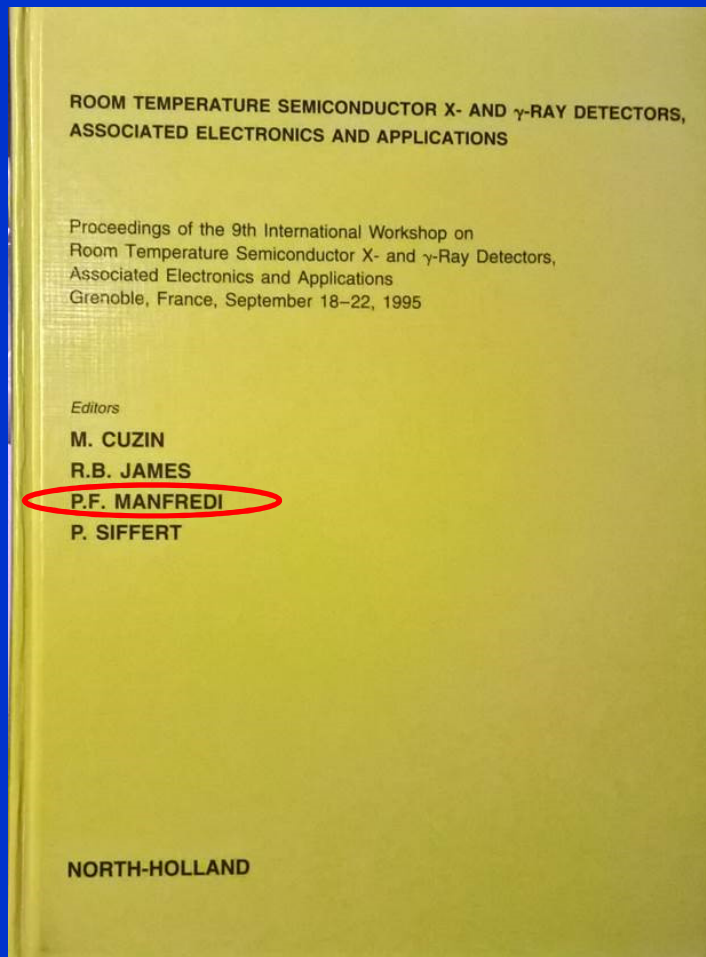


Fig. 4. Resolution versus counting rate characteristics for the drain-feedback preamplifier. a) continuous mode; b) pulsed feedback. The vertical axis represents the ratio

1995: RTSD Conference Scientific Committee

Room Temperature Semiconductor X- and γ -Ray Detectors, Associated Electronics and Applications



Nuclear Instruments and Methods in Physics Research A 380 (1996) 308–311

NUCLEAR
INSTRUMENTS
& METHODS
IN PHYSICS
RESEARCH
Section A

JFET-based monolithic preamplifiers for spectrometry applications¹

P.F. Manfredi*, V. Re, V. Speziali

Dipartimento di Elettronica, Università di Pavia, via Ferrata 1, 27100, Pavia, Italy²

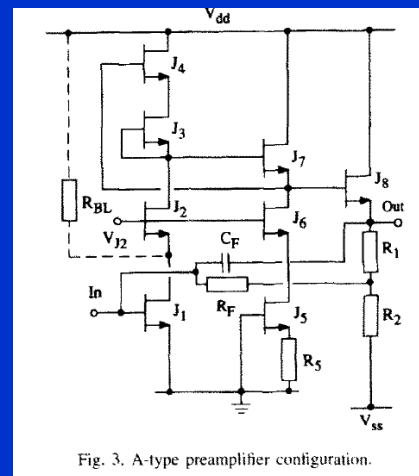
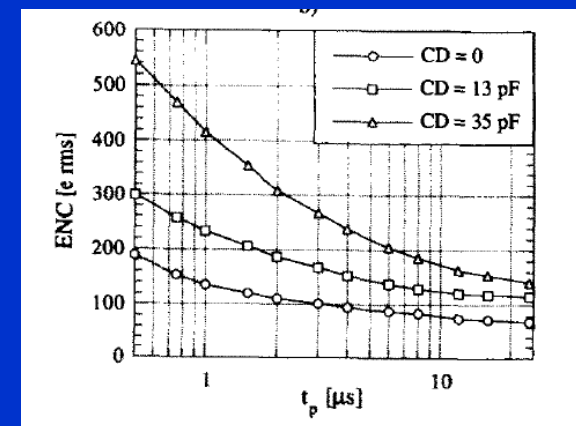


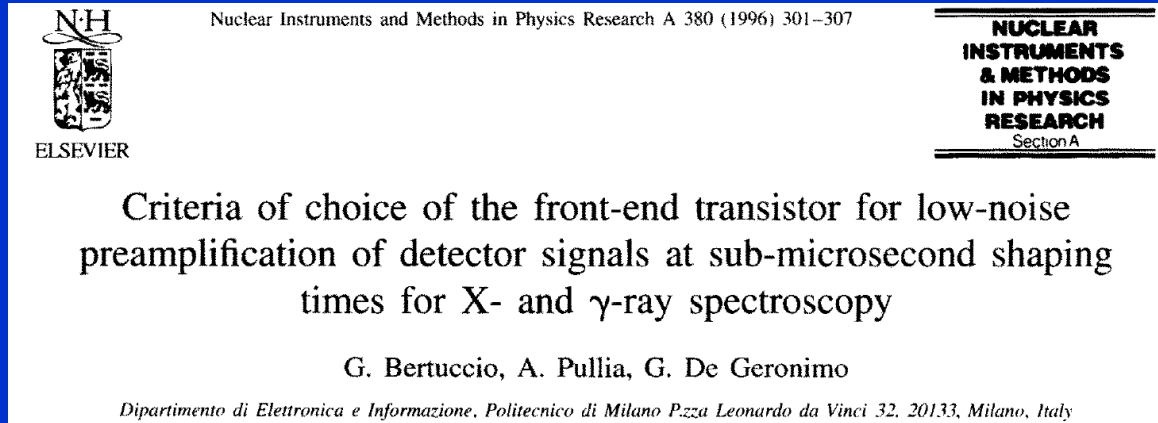
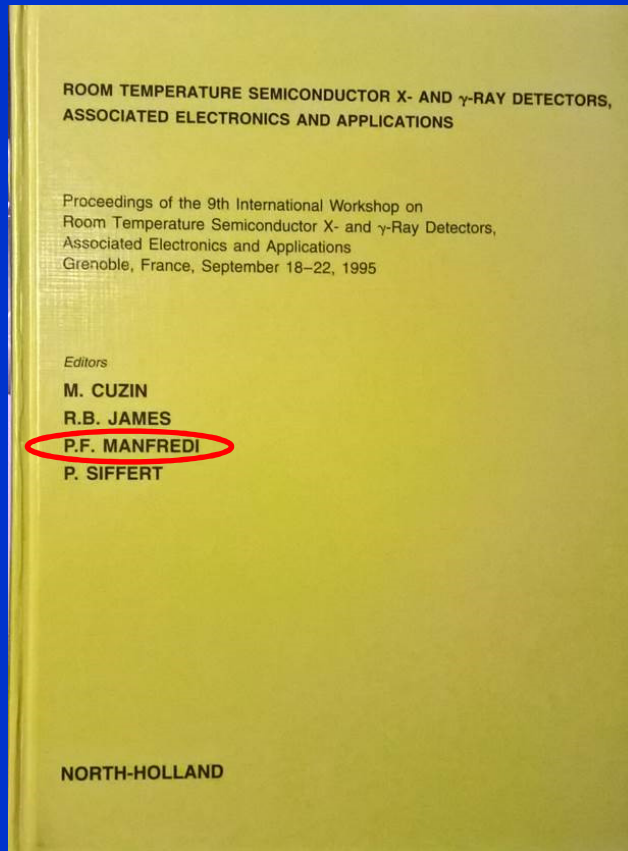
Fig. 3. A-type preamplifier configuration.



$ENC \approx 100$ electrons r.m.s. for $C_D \approx 10$ pF

1995: RTSD Conference

Invited talk asked by Franco Manfredi



Comparative Study of Noise Performace of JFET, MOSFET, MESFET, HEMT and BJT

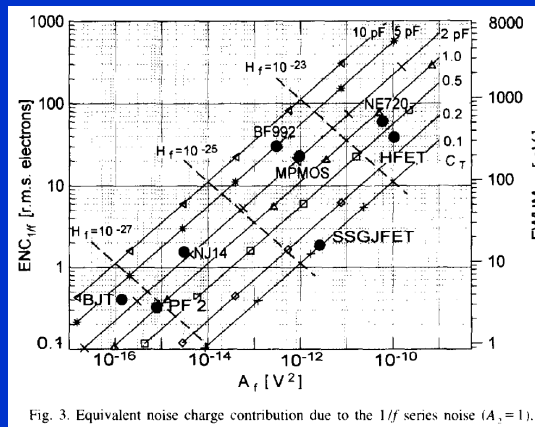
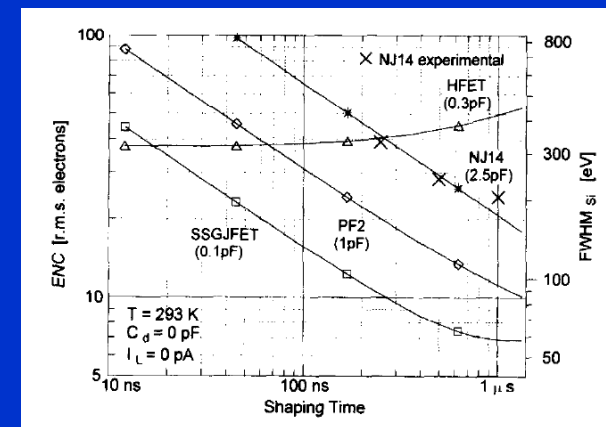


Fig. 3. Equivalent noise charge contribution due to the $1/f$ series noise ($A_s = 1$).



AToM CMOS ASIC applied to X-Ray Detectors

Presented at
1999 RTSD
Conference
in Vienna



ELSEVIER

Nuclear Instruments and Methods in Physics Research A 458 (2001) 382–391

**NUCLEAR
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& METHODS
IN PHYSICS
RESEARCH**
Section A

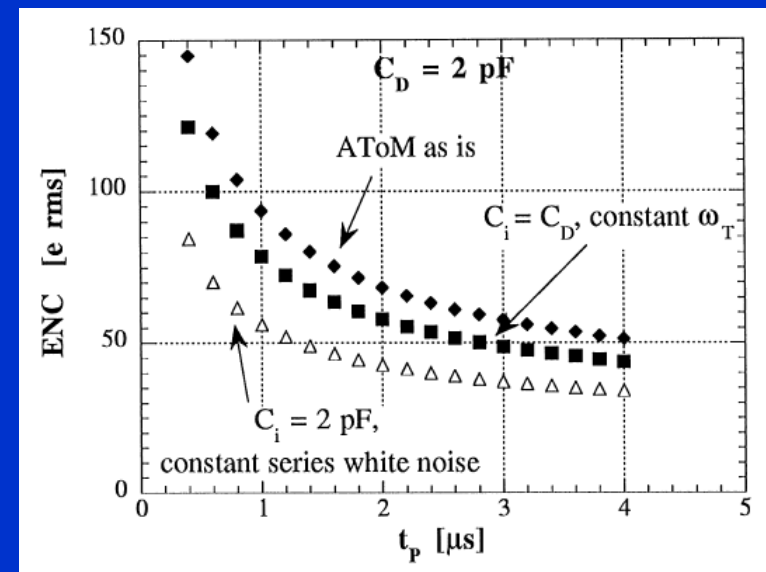
www.elsevier.nl/locate/nima

Noise limits of AToM, a 128 channel CMOS readout chip in applications with room temperature high granularity detectors

P.F. Manfredi^{a,b}, L. Ratti^{c,d}, V. Re^{d,b,*}, N.A. Roe^a, V. Speziali^{c,b}

- CMOS 0.8 μ m
- 128 channels
- analog processing and A/D conversion
- Designed for Si microstrip in HEP application

Study of a design suitable for
X- γ ray spectroscopy detectors

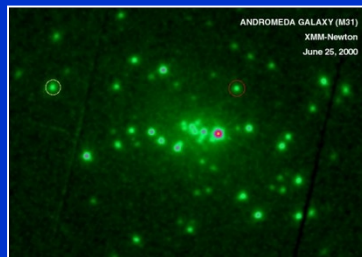


$ENC \approx 50$ electrons r.m.s. for $C_D \approx 2$ pF

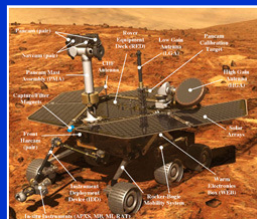
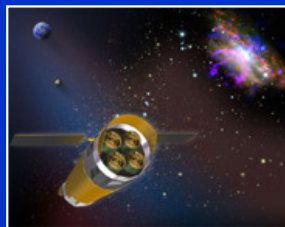
Why X-Ray Detectors are interesting for a Front-End Electronics Designer?

Applications of X-Ray Detectors

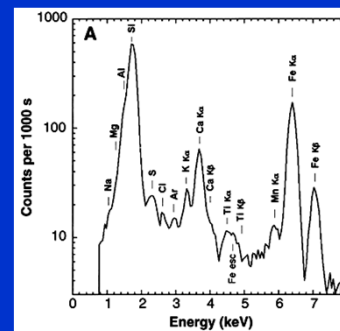
Astrophysics



X-γ ray telescopes
Universe and planetary explorations



Material study

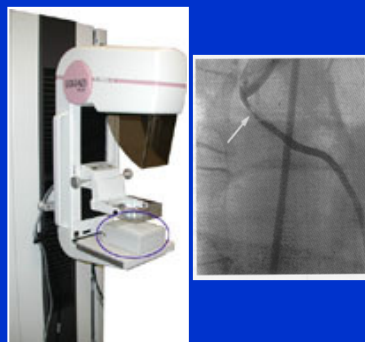


X-Ray fluorescence analysis

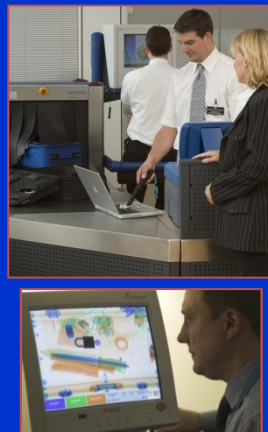
Works of arts analysis



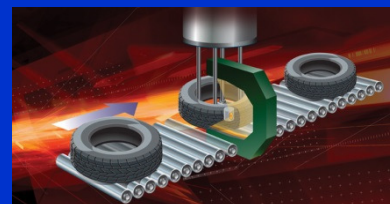
Medical



Security



Quality assurance (Food, Electronics, Pharma, Tire...)

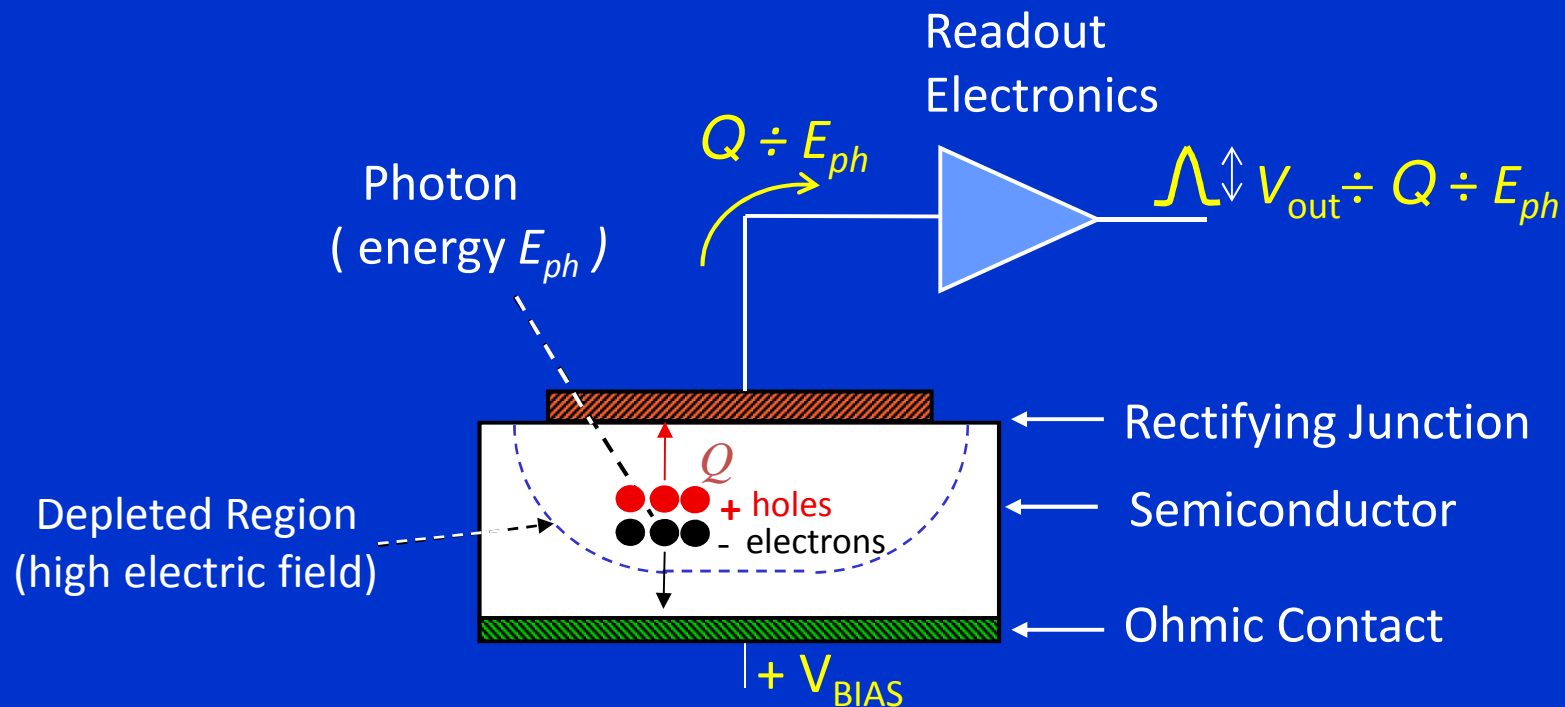


Waste recycling



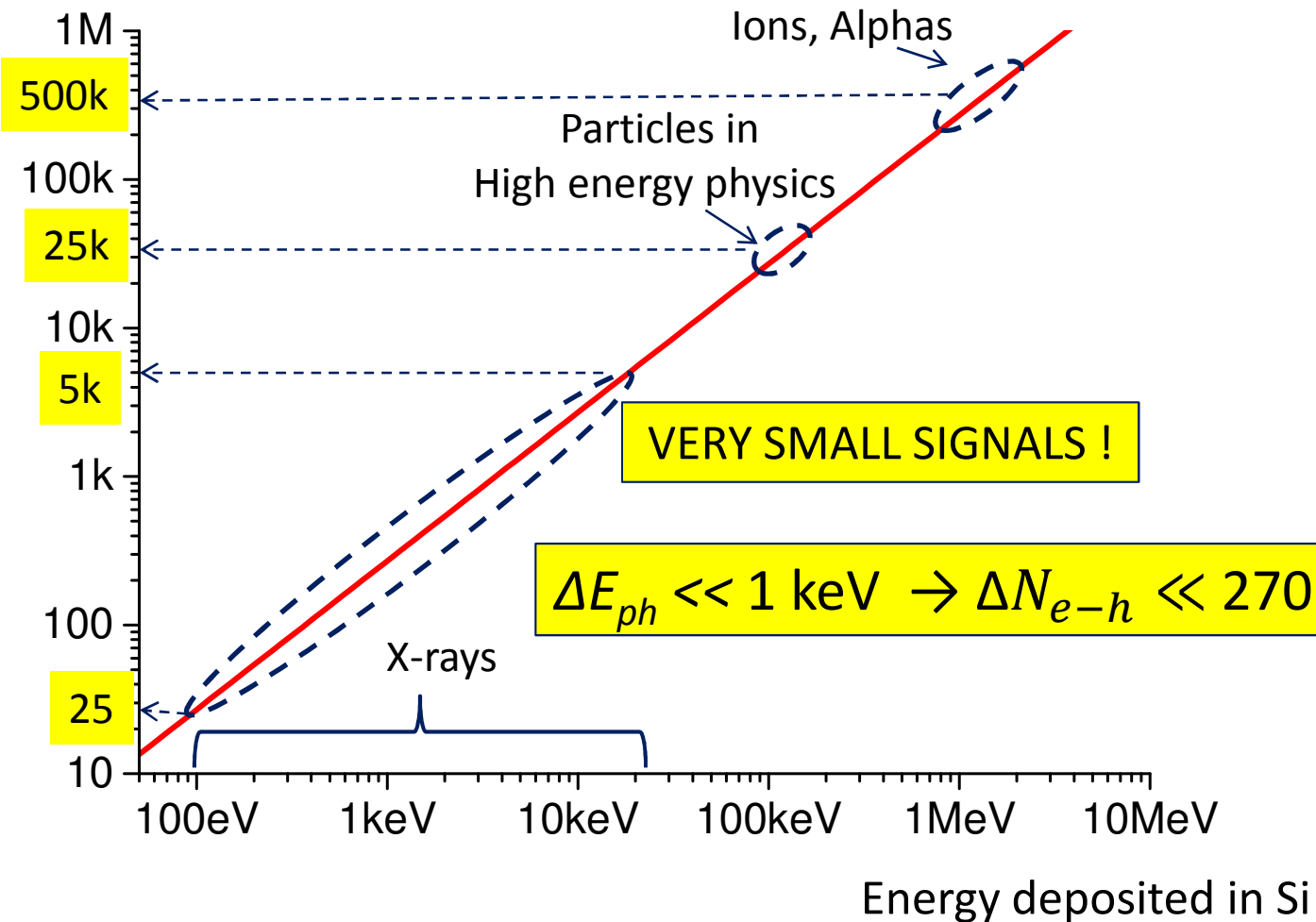
Electronics for X-Ray Detectors

Where is the challenge?



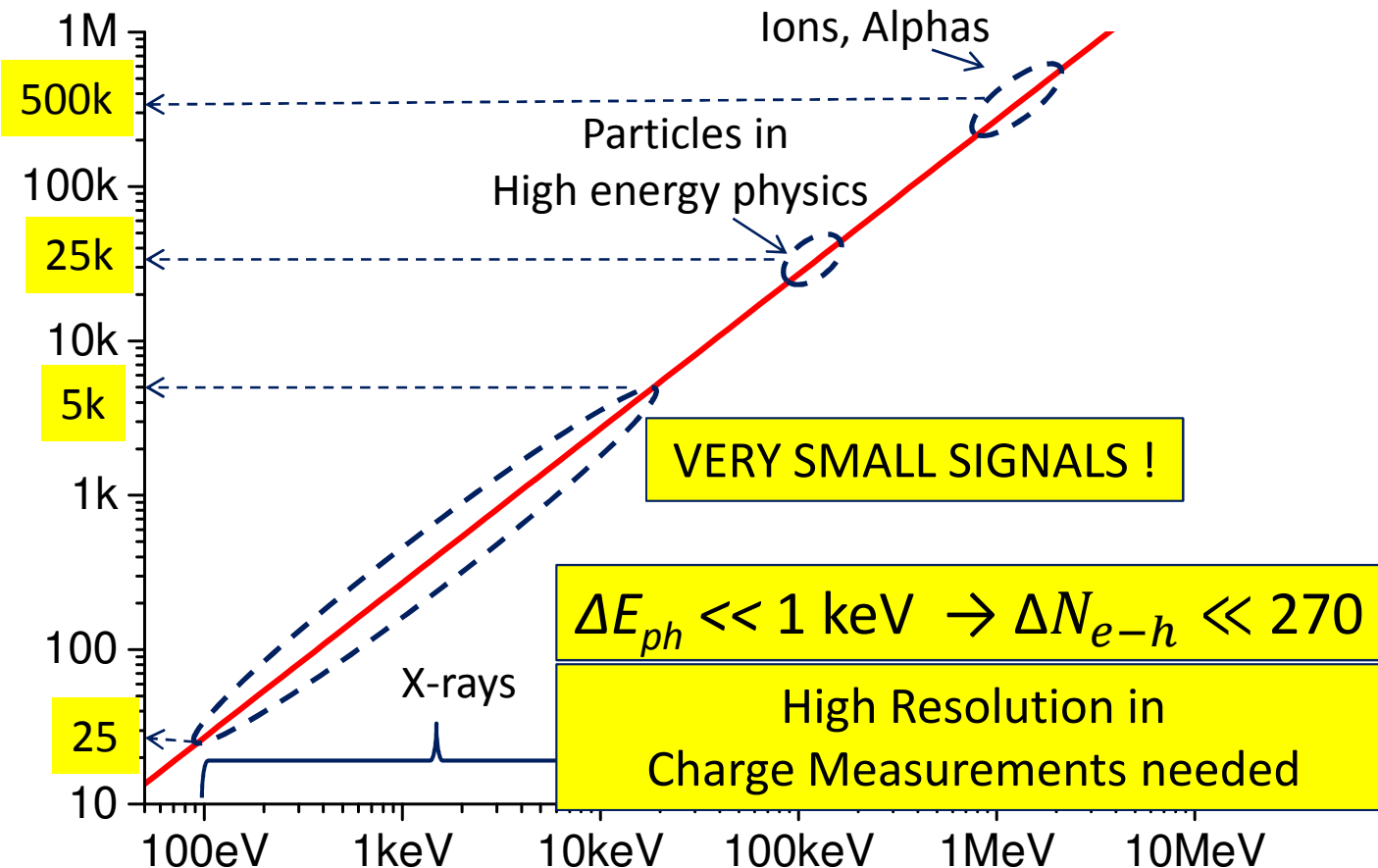
Electronics for X-Ray Detectors: Where is the Challenge?

Number of e-h pairs
generated in Silicon



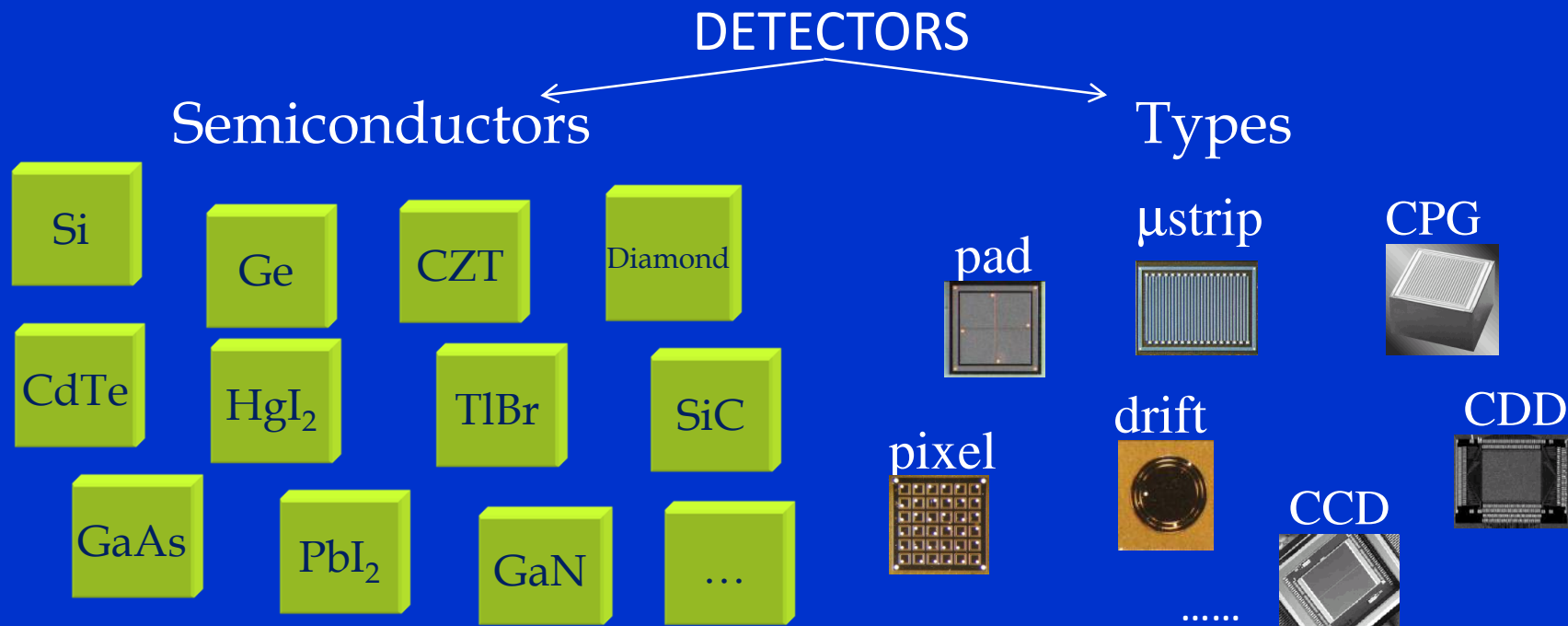
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Number of e-h pairs
generated in Silicon



Readout Electronics is required to have
Equivalent Noise Charge of few tens or even few electrons r.m.s.

Electronics for X-Ray Detectors



DASIC more than *ASIC*

Detector and Application Specific IC

Toward The Ultimate Low-Noise Front-End Electronics

two examples:

- A Complex, High Functionality, Ultra Low-Noise DASIC
- An Ultimate-Low Noise Fully Integrated CMOS Charge Sensitive Preamplifier



STAR-X

a Large Format
High Functionality
Ultra Low Noise
and Low Power
DASIC



**POLITECNICO
DI MILANO**

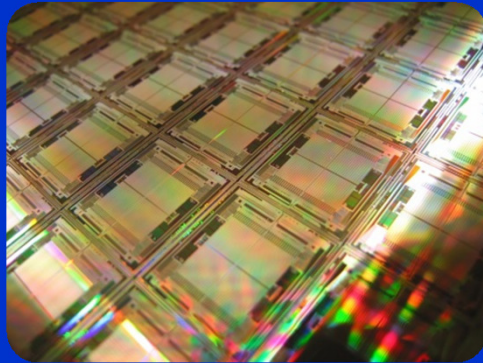
G. Bertuccio, "Toward the Ultimate Low-Noise Front-End Electronics for X-γ Ray Detectors:
the heritage of Franco Manfredi to current research", Aula Volta, University of Pavia, Italy, 5 December 2016

slide 21



STAR-X

A Large Format X-Ray Detector Readout DASIC



Supervision



Project Management
&
Readout System Design

DASIC Designer Groups



Front-End Electronics Design

Giuseppe Bertuccio



UNIVERSITÀ
DEGLI STUDI DI PAVIA

Back-end Electronics Design

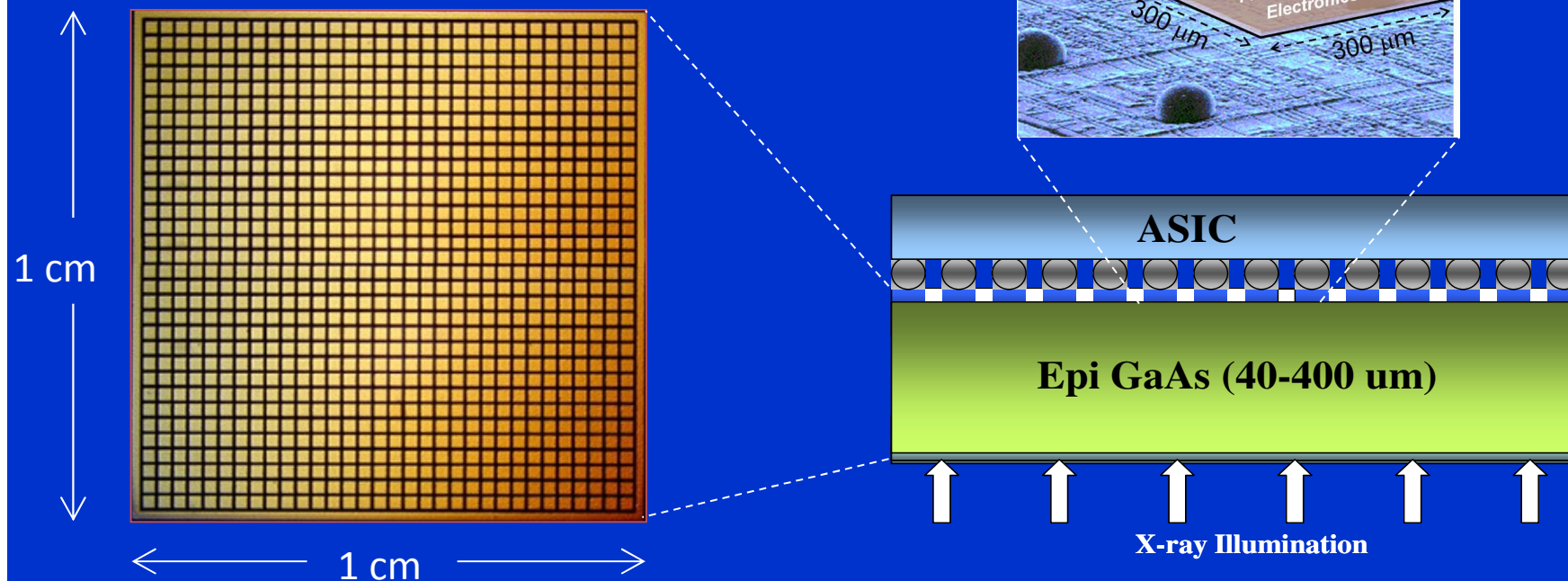
Piero Malcovati

STAR-X DASIC

for GaAs pixel detector for X-ray Imaging with Space Telescopes

GaAs detector 32 x 32 pixels

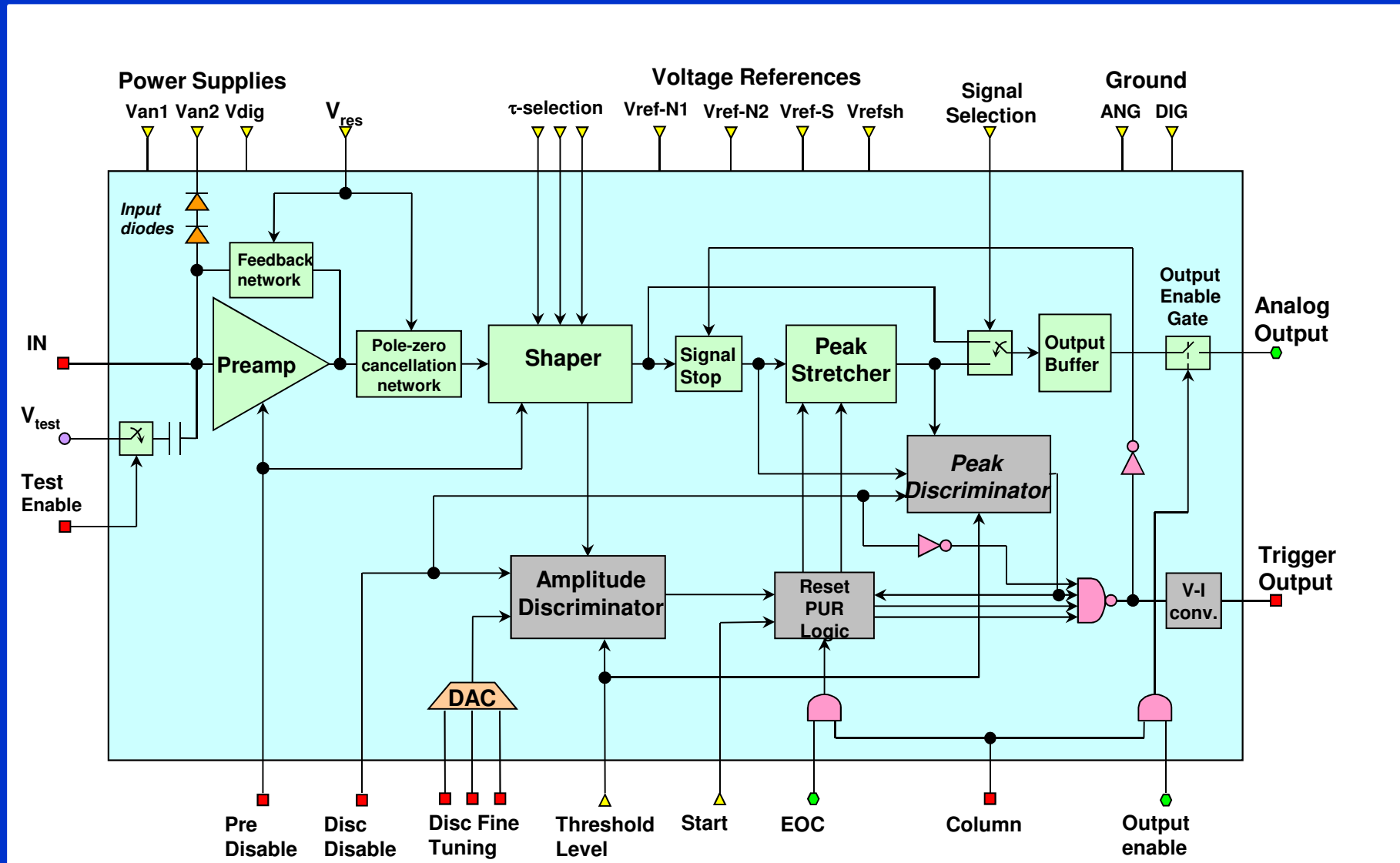
Pixel size: 250 μm X 250 μm



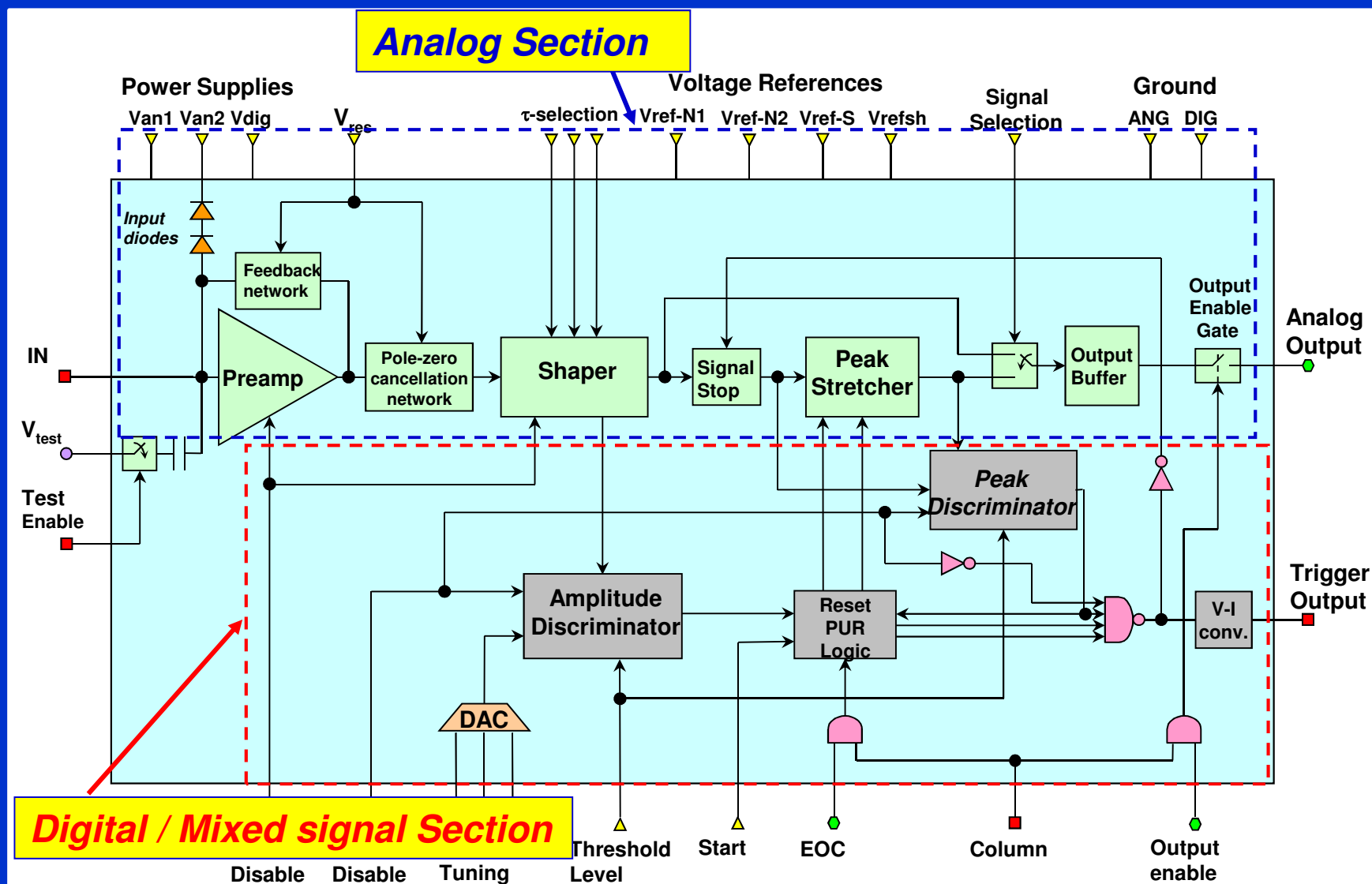
X-Ray Energy Range

500 eV – 50 keV (120 to 12k electrons)

Front-End: The Readout Pixel Cell (RPC)



Front-End: The Readout Pixel Cell (RPC)



Readout Pixel Cell Layout

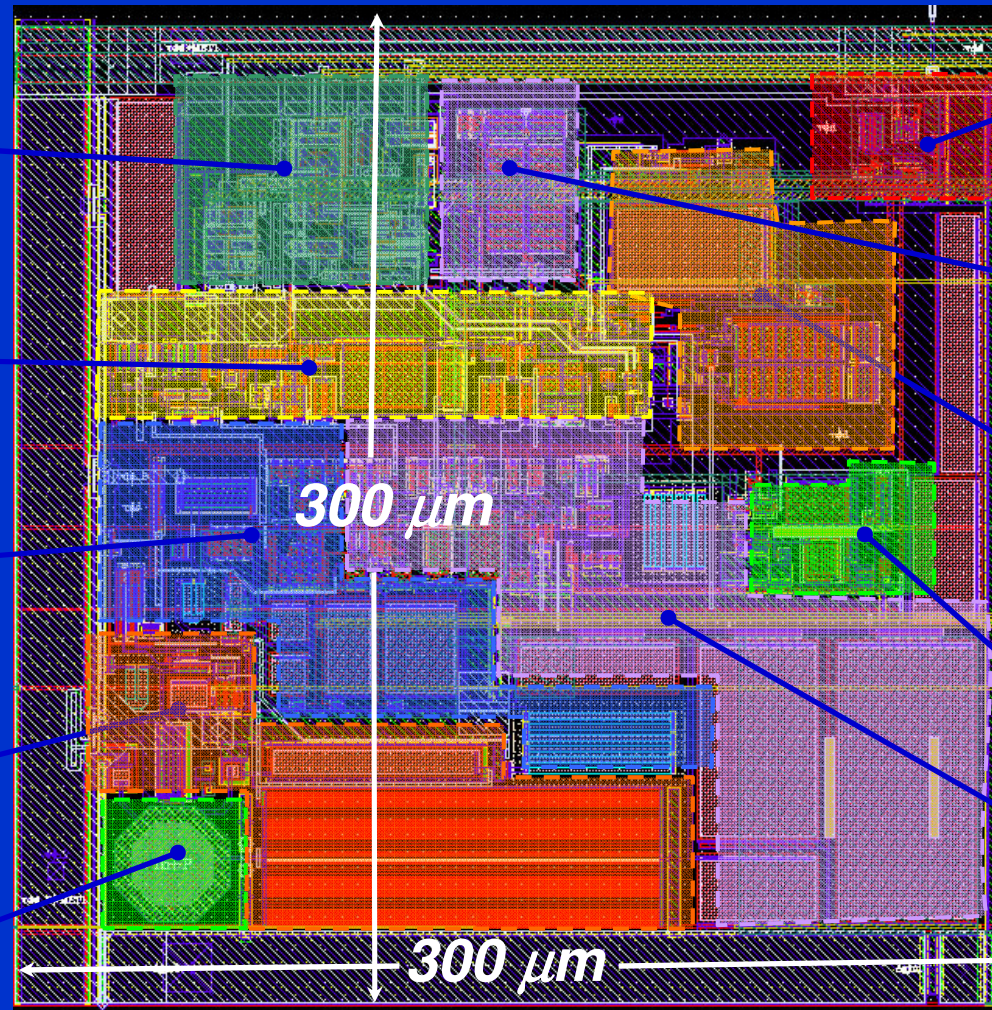
**Reset/PUR Logic
+ Trigger Trasm.
<< 1 μW**

**Amplitude
Discriminator
37 μW**

**Shaper 1st
98 μW**

**Charge
Preamplifier
115 μW**

**Input Pad
 $\varnothing=35 \mu\text{m}$**



**Output Buffer
49 μW**

**Peak
Discriminator
21 μW**

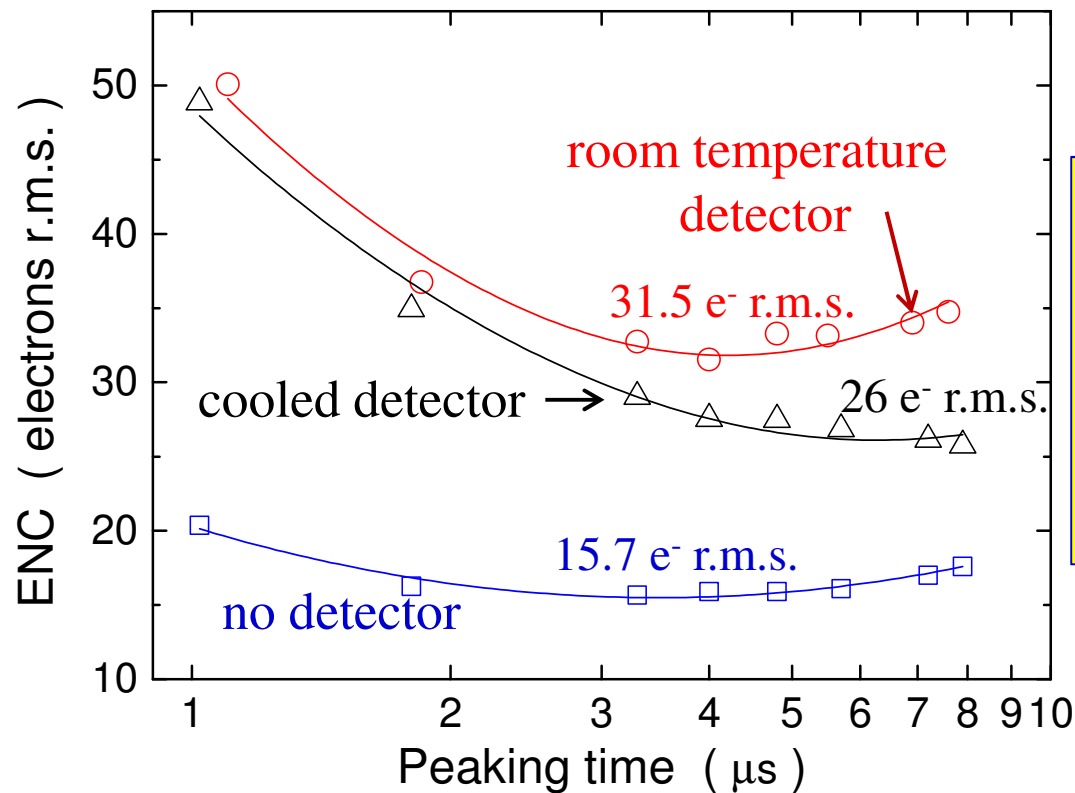
**Peak
Stretcher
35 μW**

DC stabilizer

**Shaper 2nd
71 μW**

Total Power Consumption: 483 μW

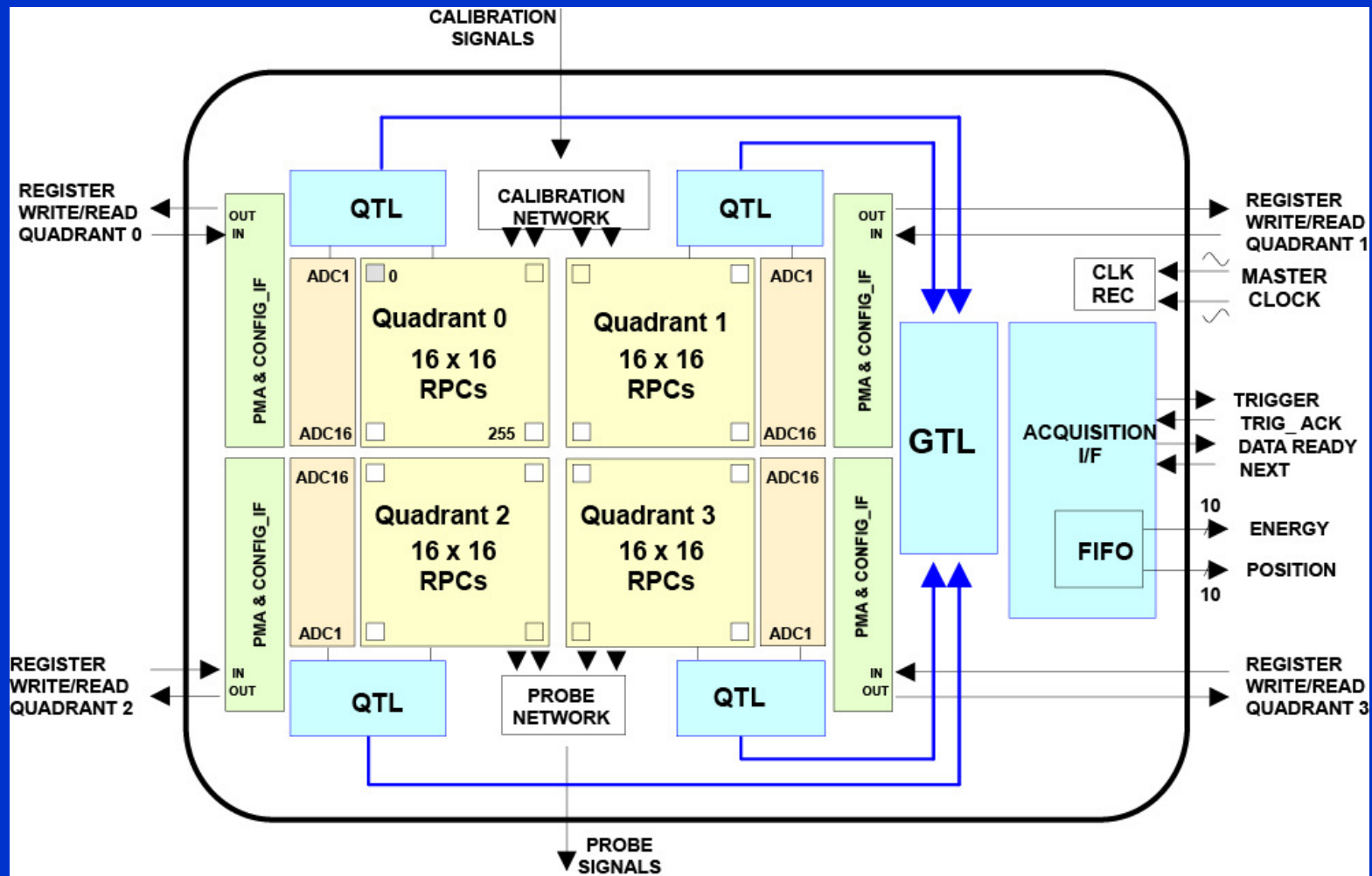
RPC Equivalent Noise Charge



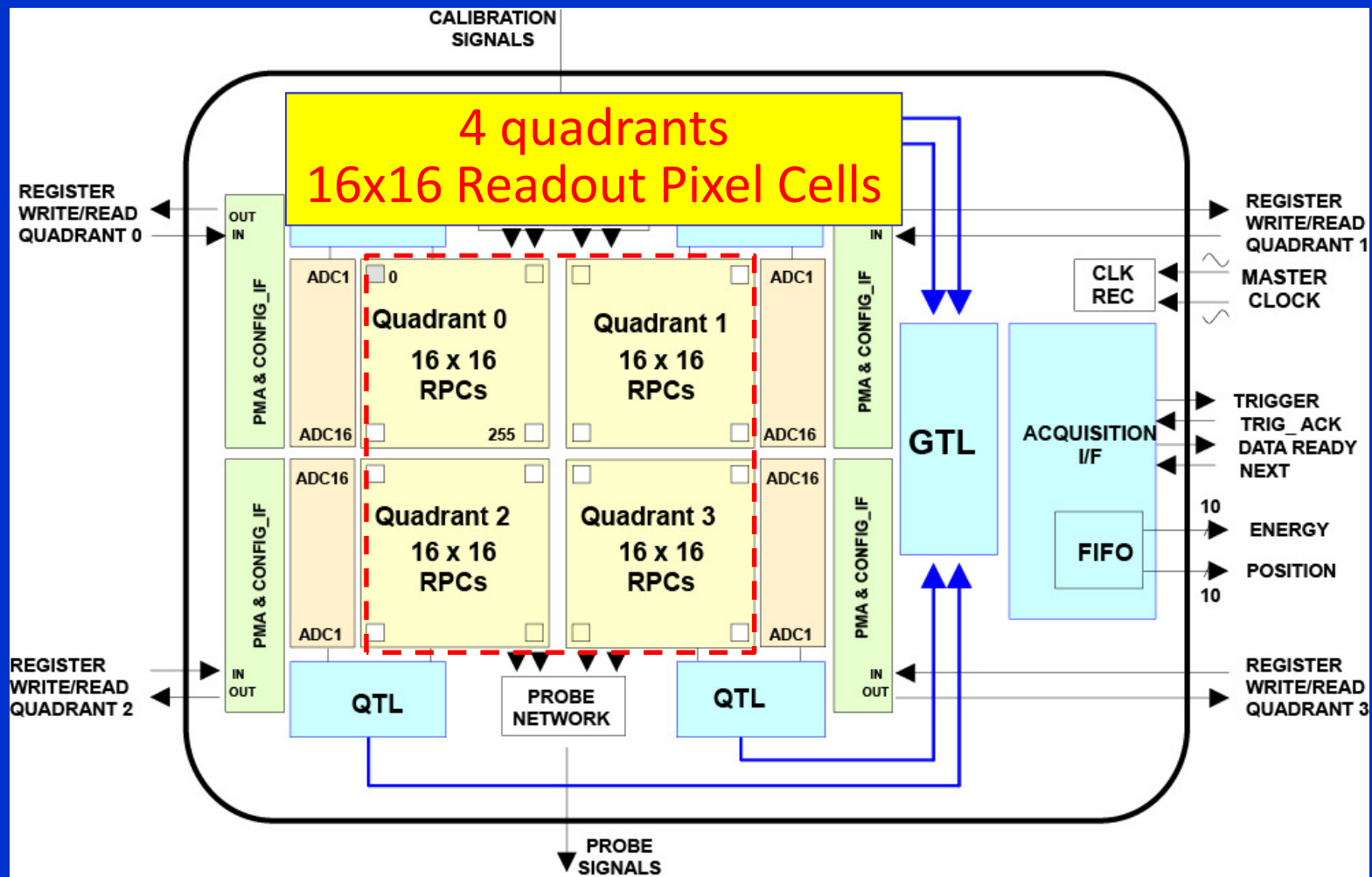
Measurement made on a test chip with a single isolated Readout Pixel Cell

Front-End Electronics Noise
15.7 electrons r.m.s @ 3 us peaking time

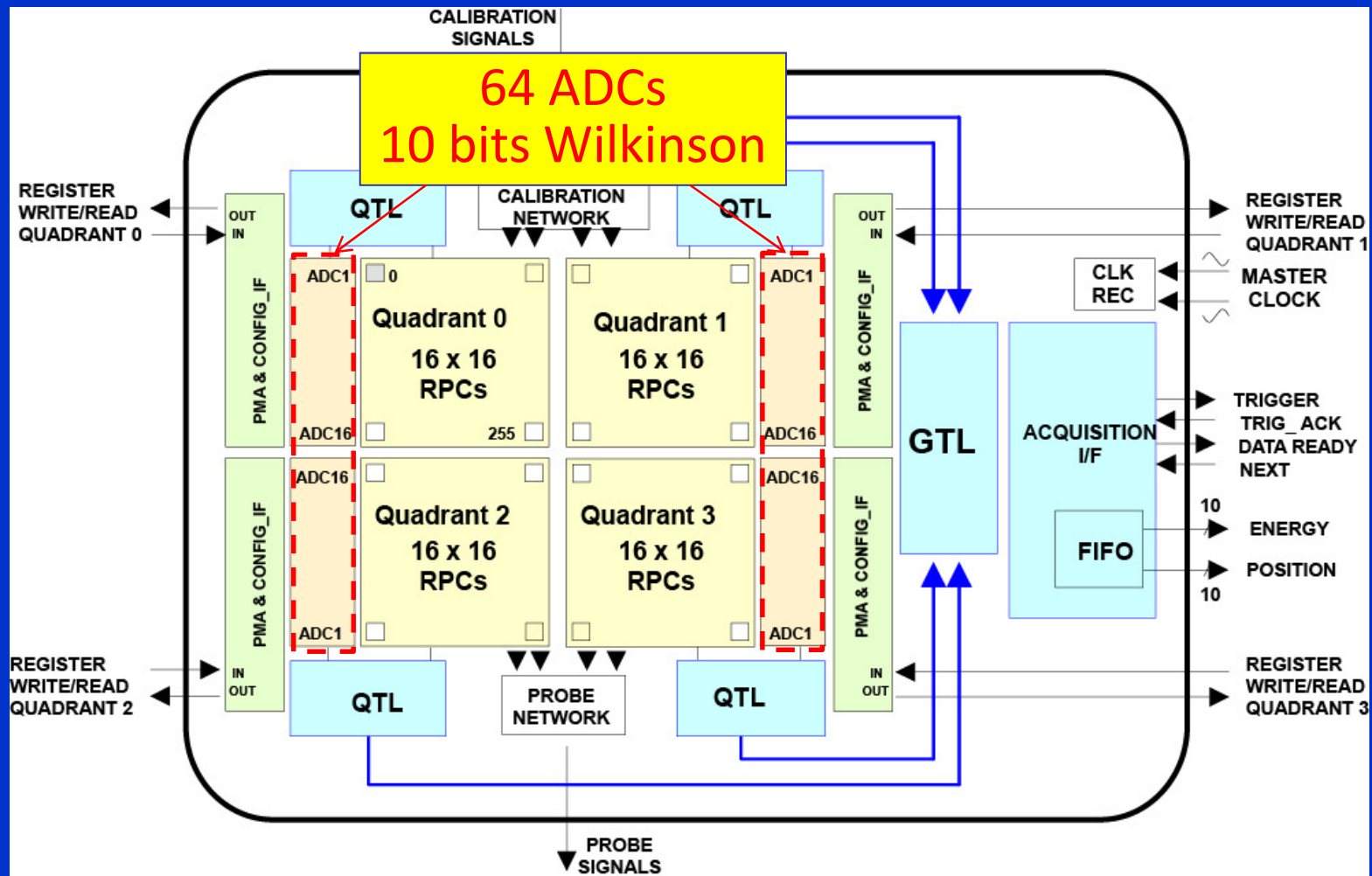
STAR-X Architecture



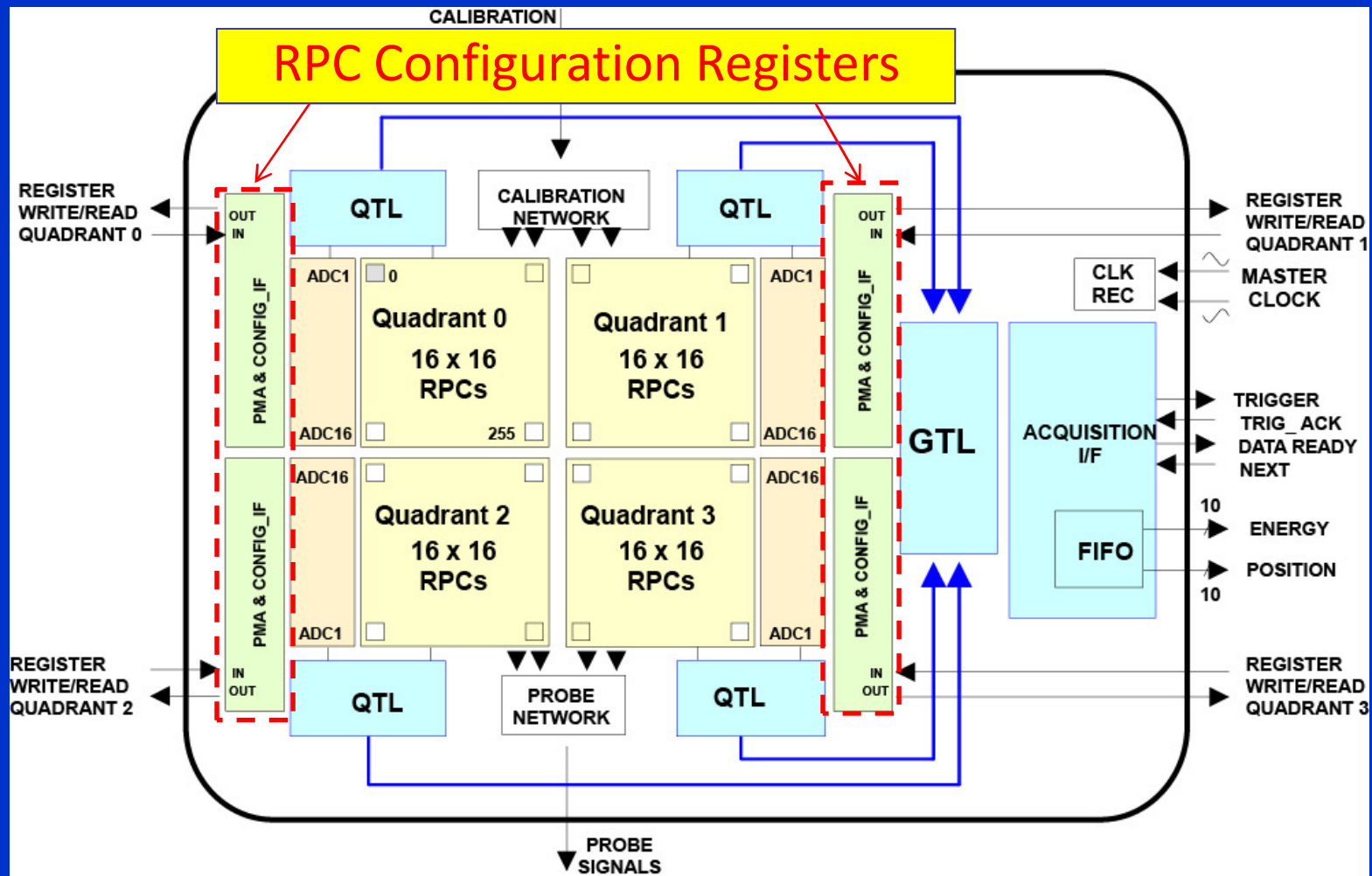
STAR-X Architecture



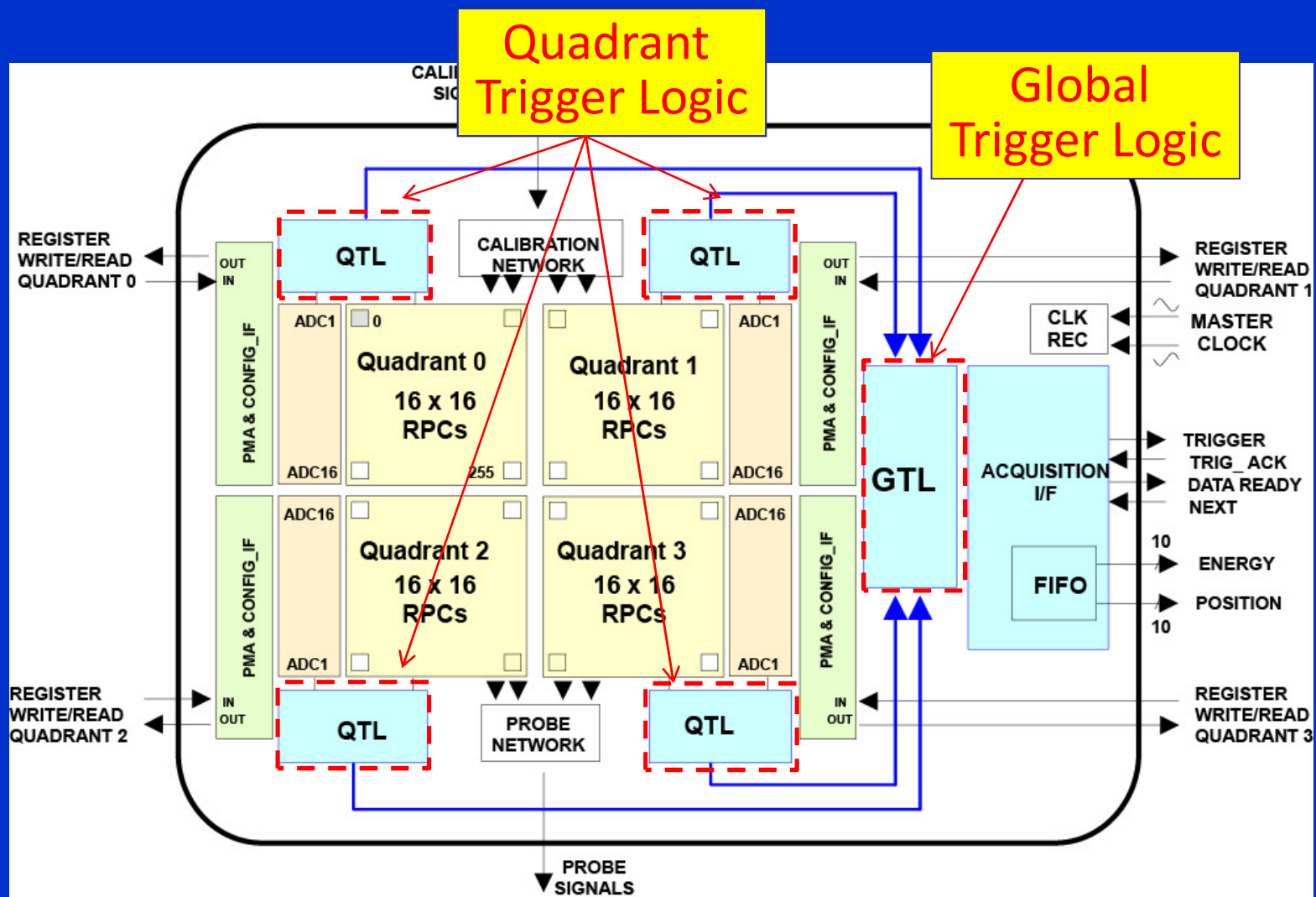
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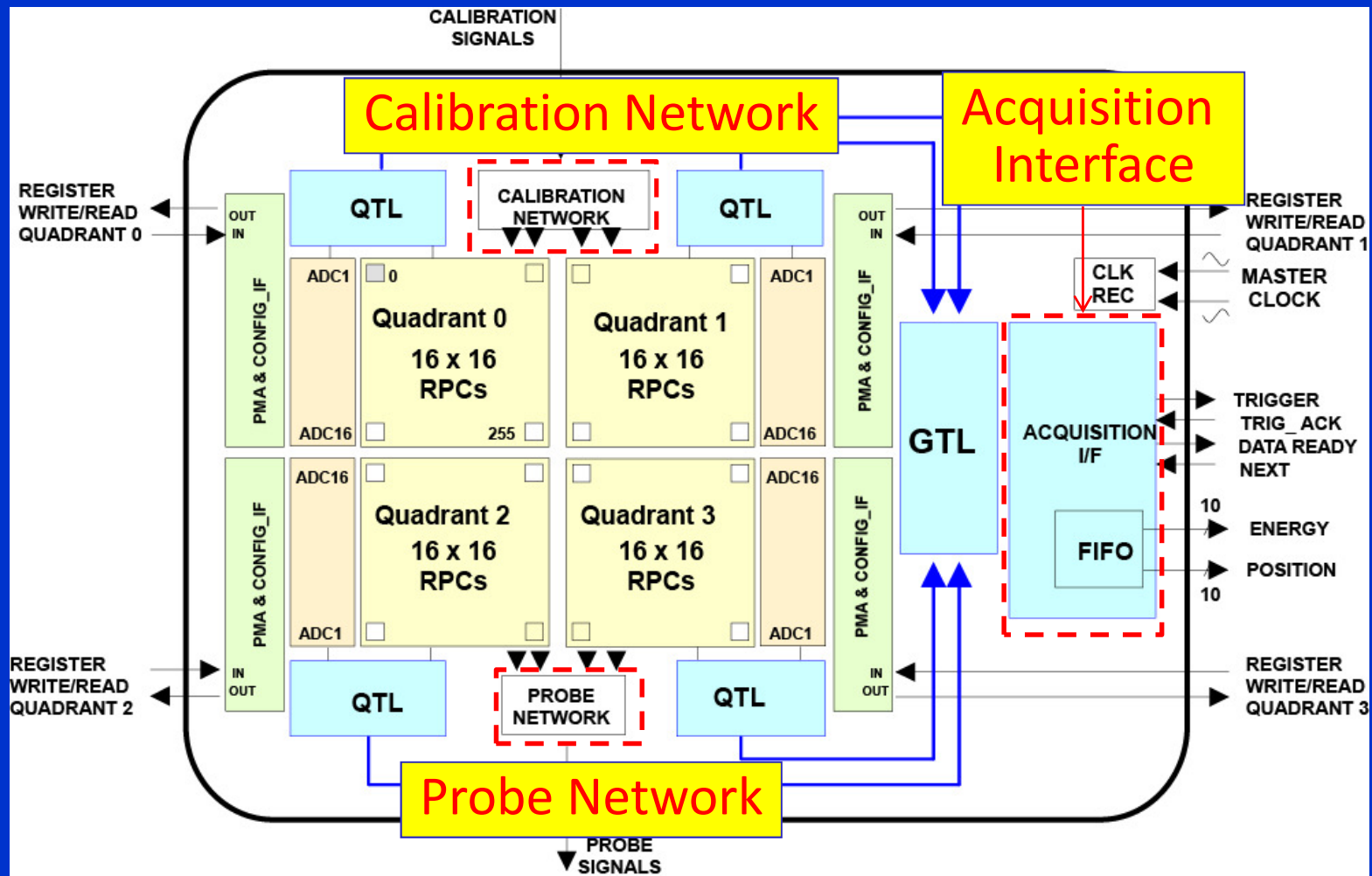
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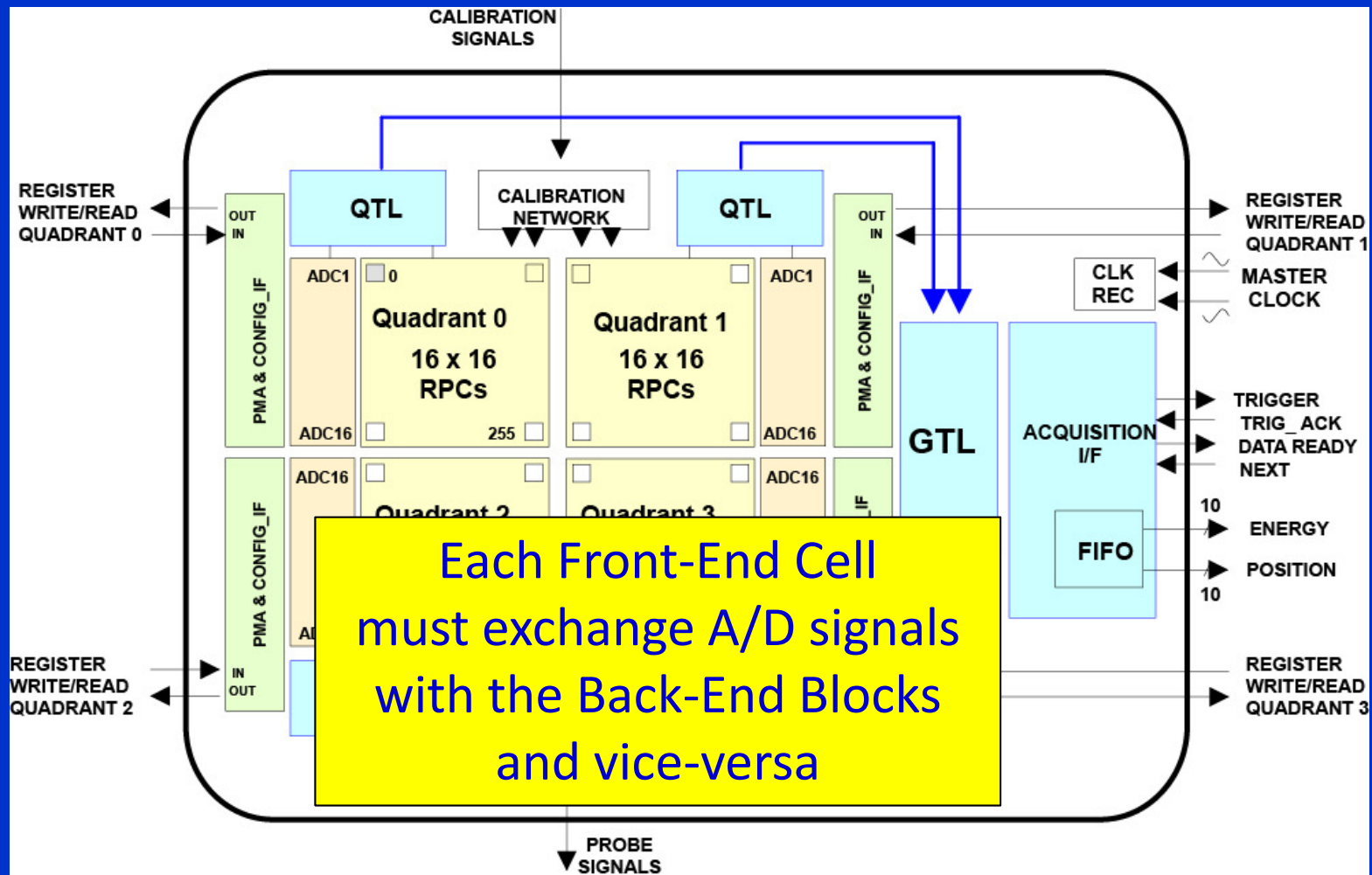
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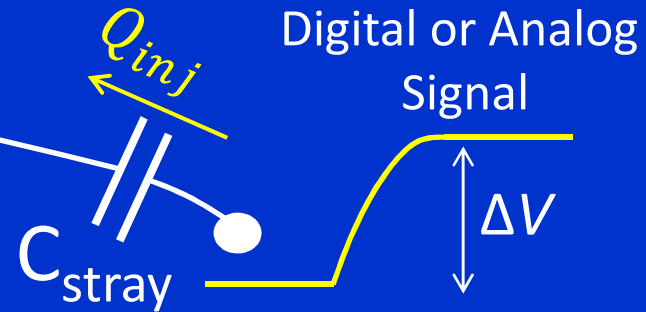
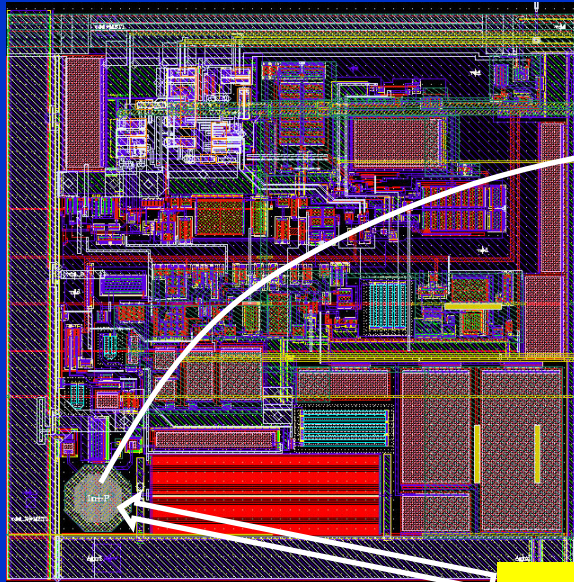
STAR-X Architecture



STAR-X Architecture



Low Noise Front-End...not sufficient!



$$Q_{inj} = C_{stray} \Delta V$$

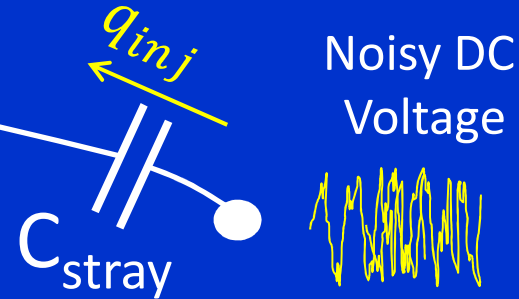
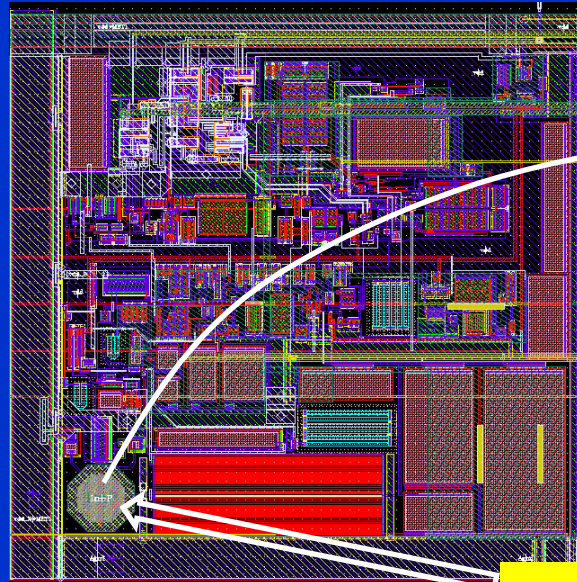
Input: Charge Sensitive Virtual Ground Node

$$Q_{inj} < \min(Q_{signal}) = 120 \text{ electrons} \quad (500 \text{ eV X-ray in GaAs})$$

$$\Downarrow \quad \Delta V \approx 1 \text{ Volt}$$

$$C_{stray} < 0.02 \text{ fF !}$$

Low Noise Front-End...not sufficient!



$$q_{inj(noise)} = C_{stray} v_n$$

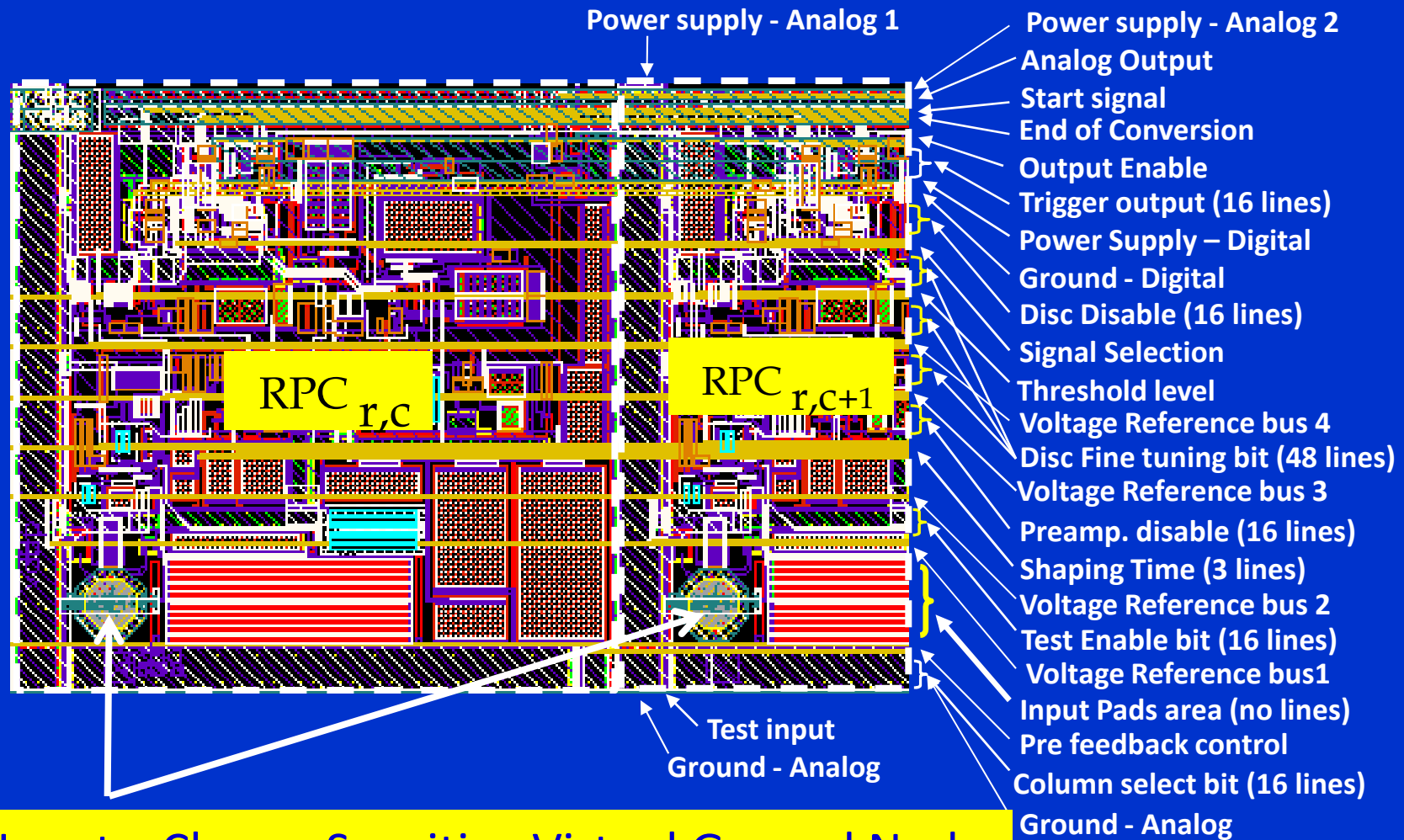
Input: Charge Sensitive Virtual Ground Node

$$q_{inj(noise)} \ll ENC = 16 \text{ electrons r.m.s.}$$



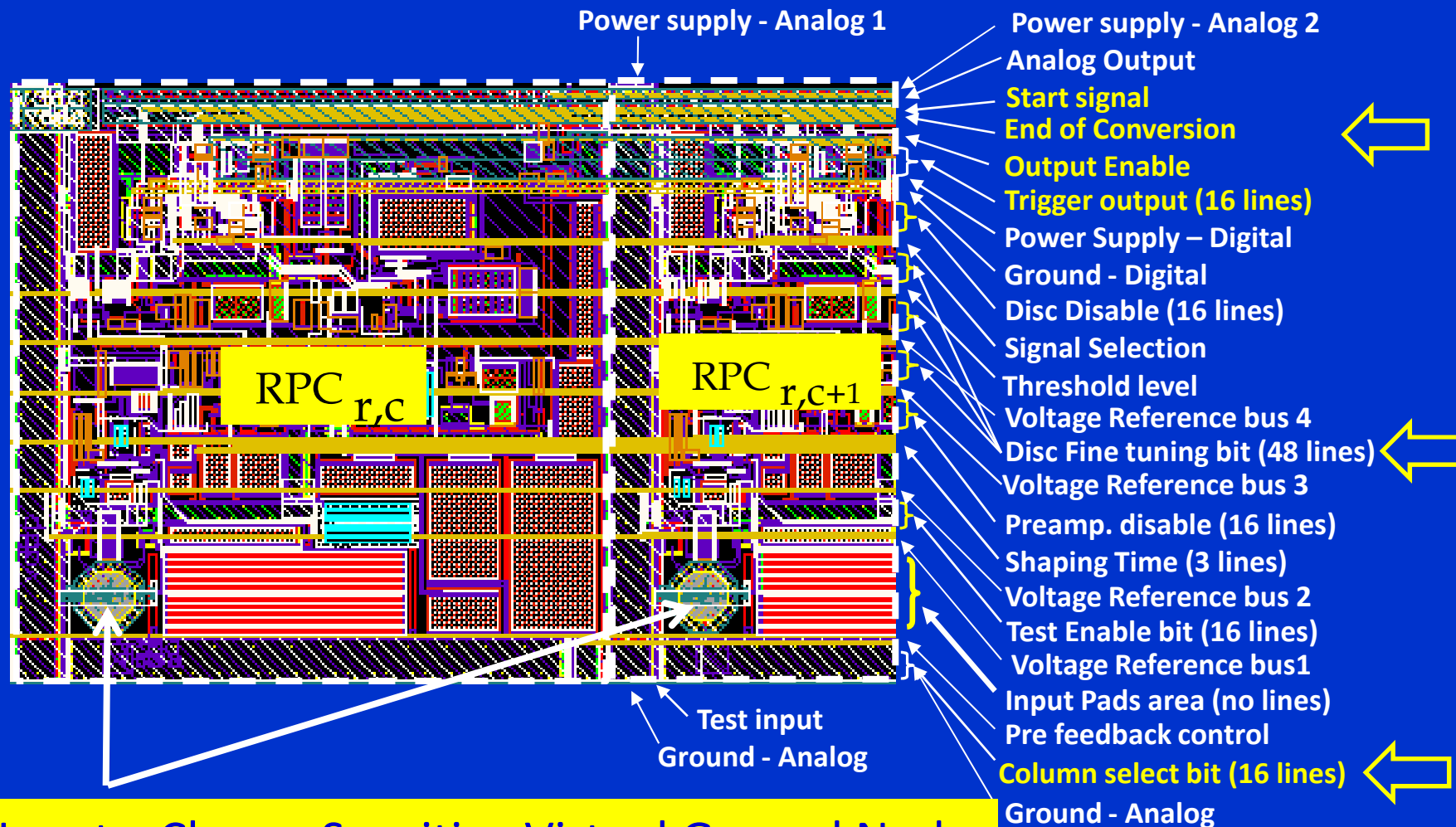
Stringent constraints on C_{stray} and v_n

Power and I/O Lines



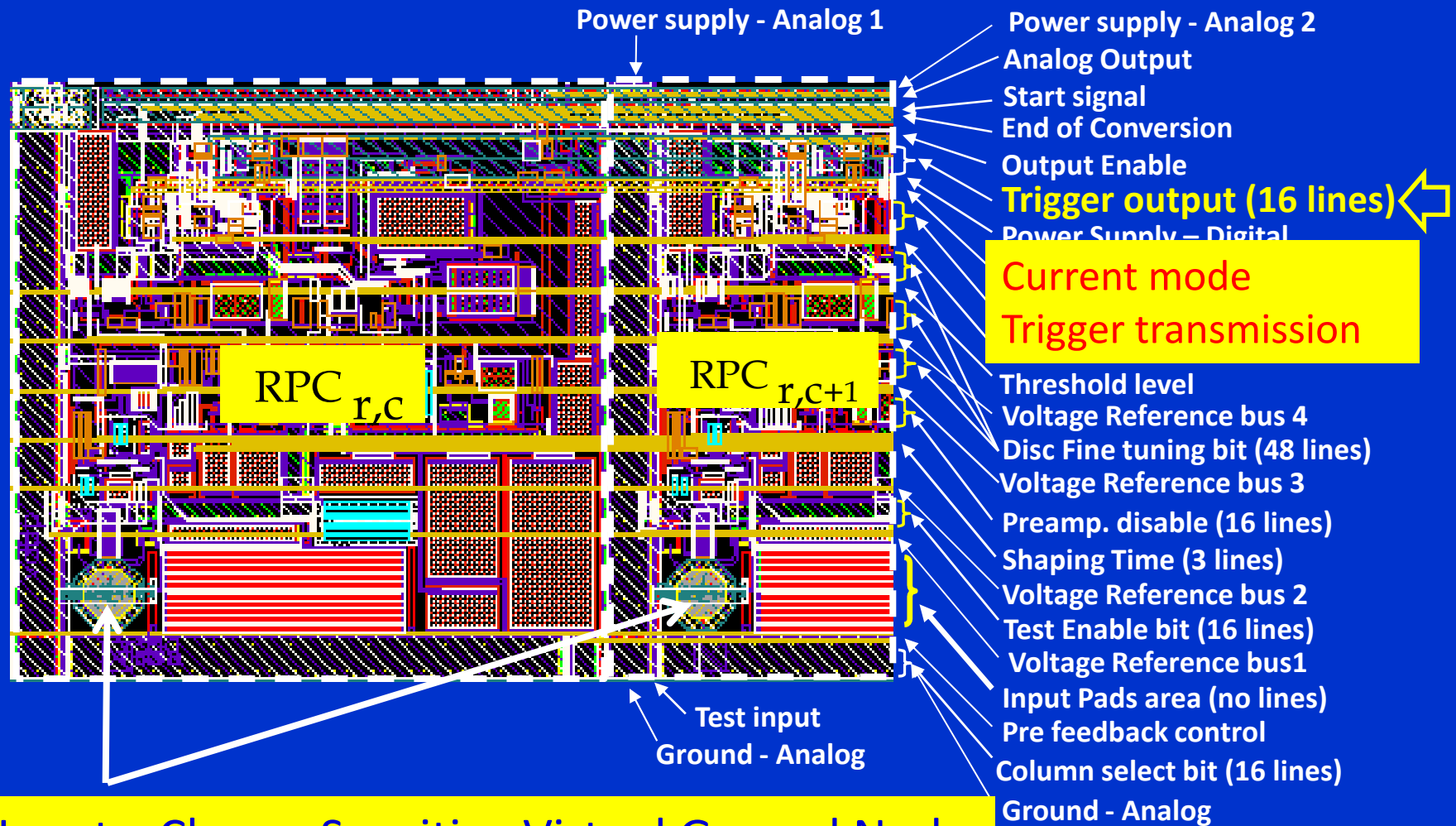
Power and I/O Lines

Lines with switching activity during acquisition
(up to 10^6 input signals per second on the full pixel matrix)

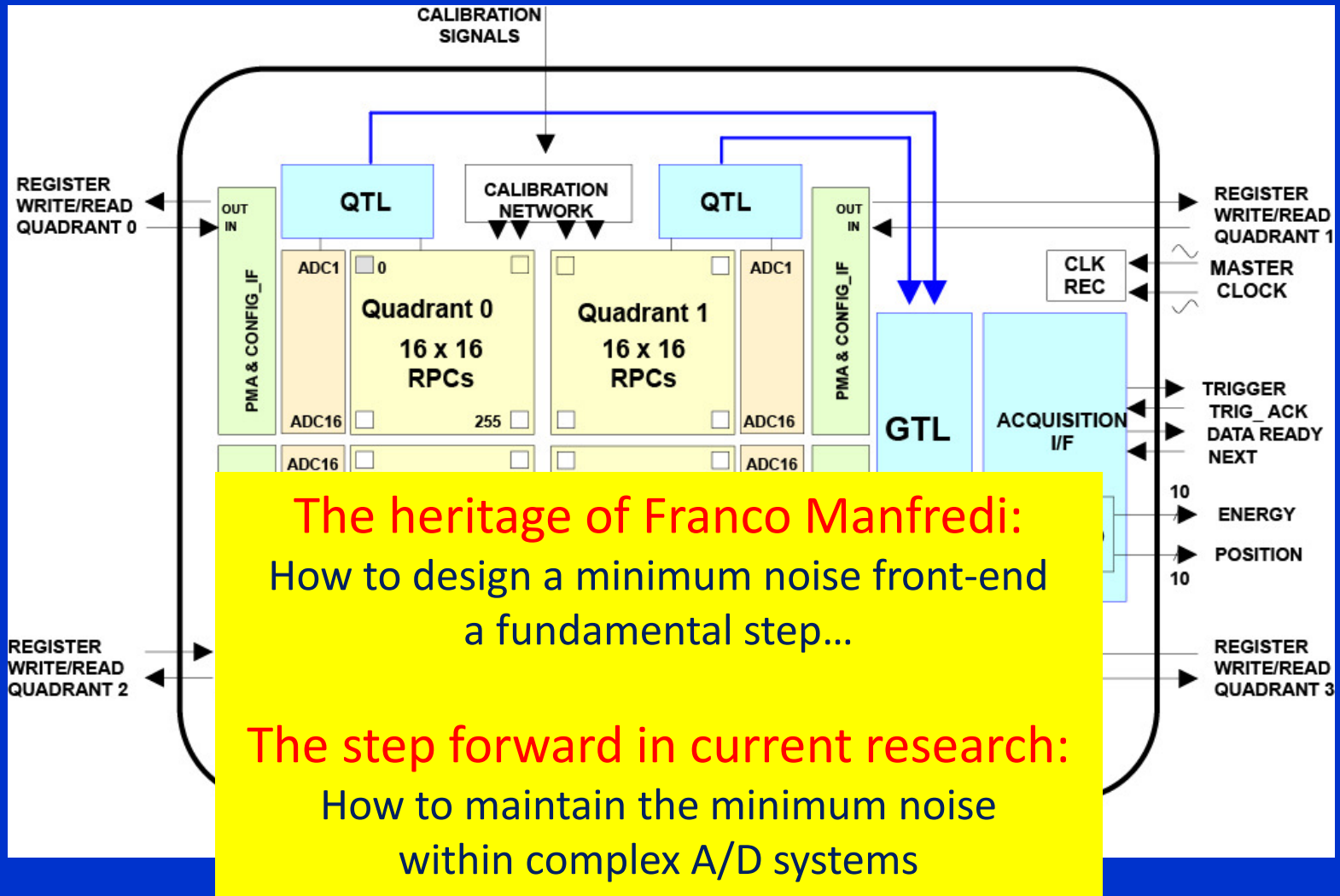


Inputs: Charge Sensitive Virtual Ground Node

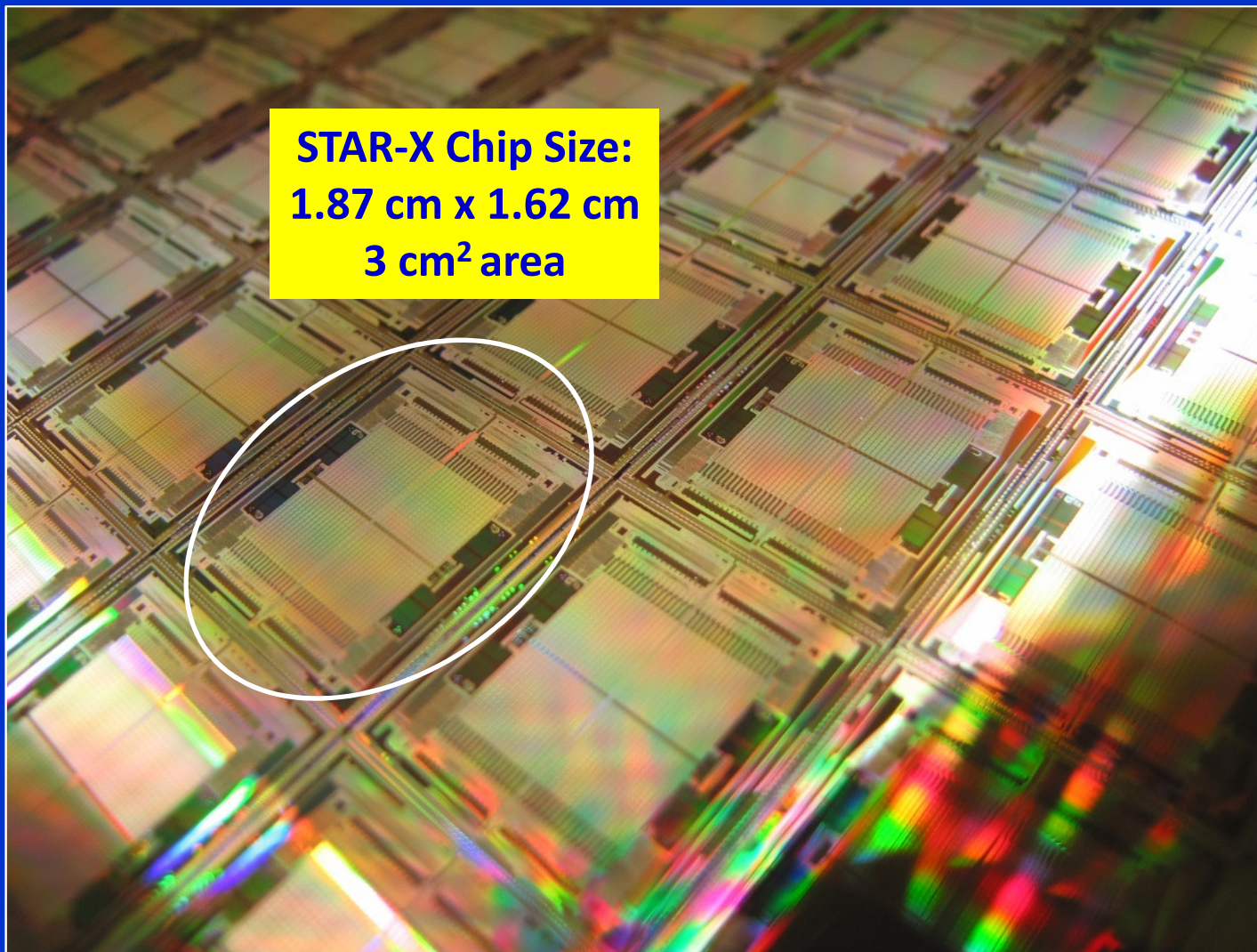
Power and I/O Lines



STAR-X Architecture

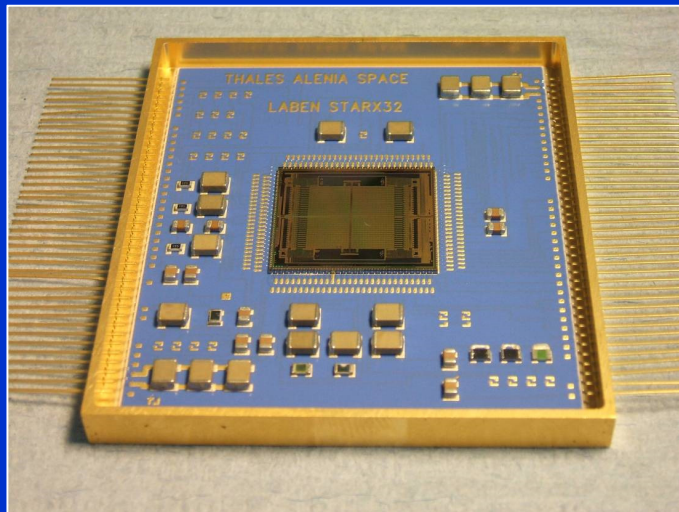


Wafer Photo

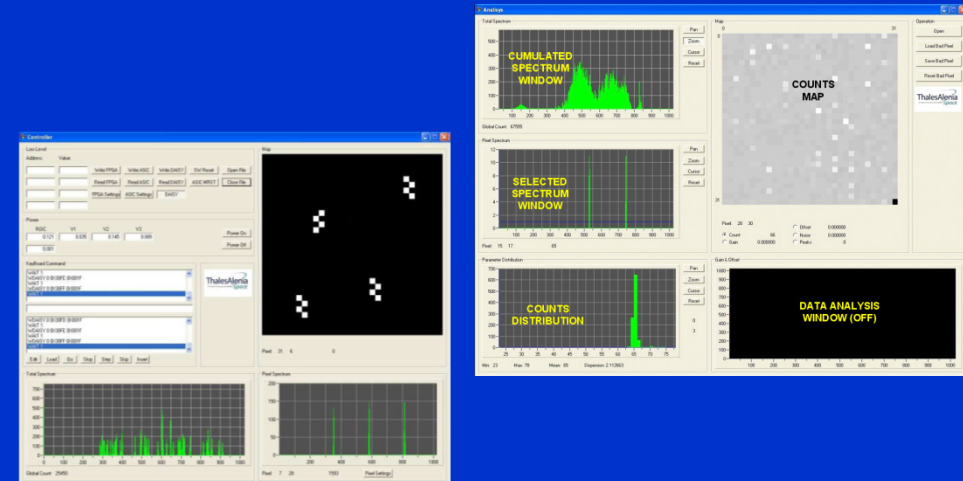


STAR-X DASIC

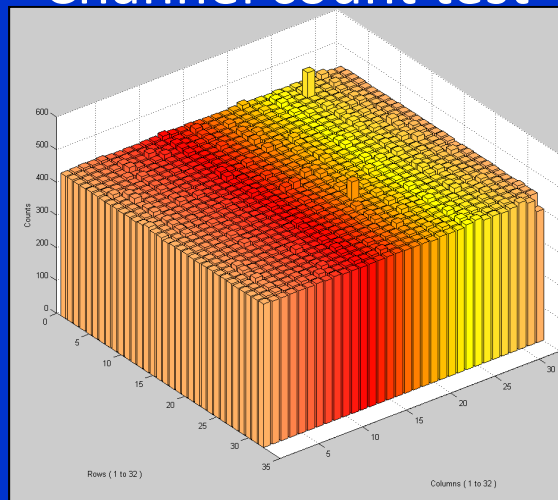
Packaged



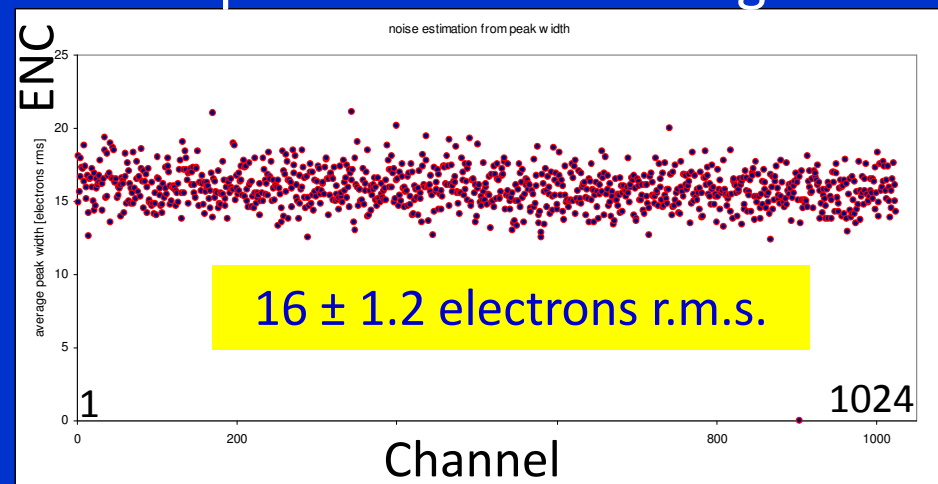
User Interface



Channel count test



Equivalent Noise Charge

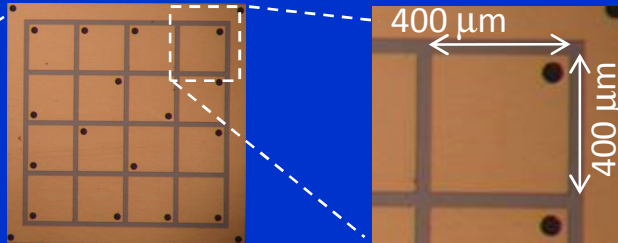


Toward The Ultimate Low-Noise Front-End Electronics

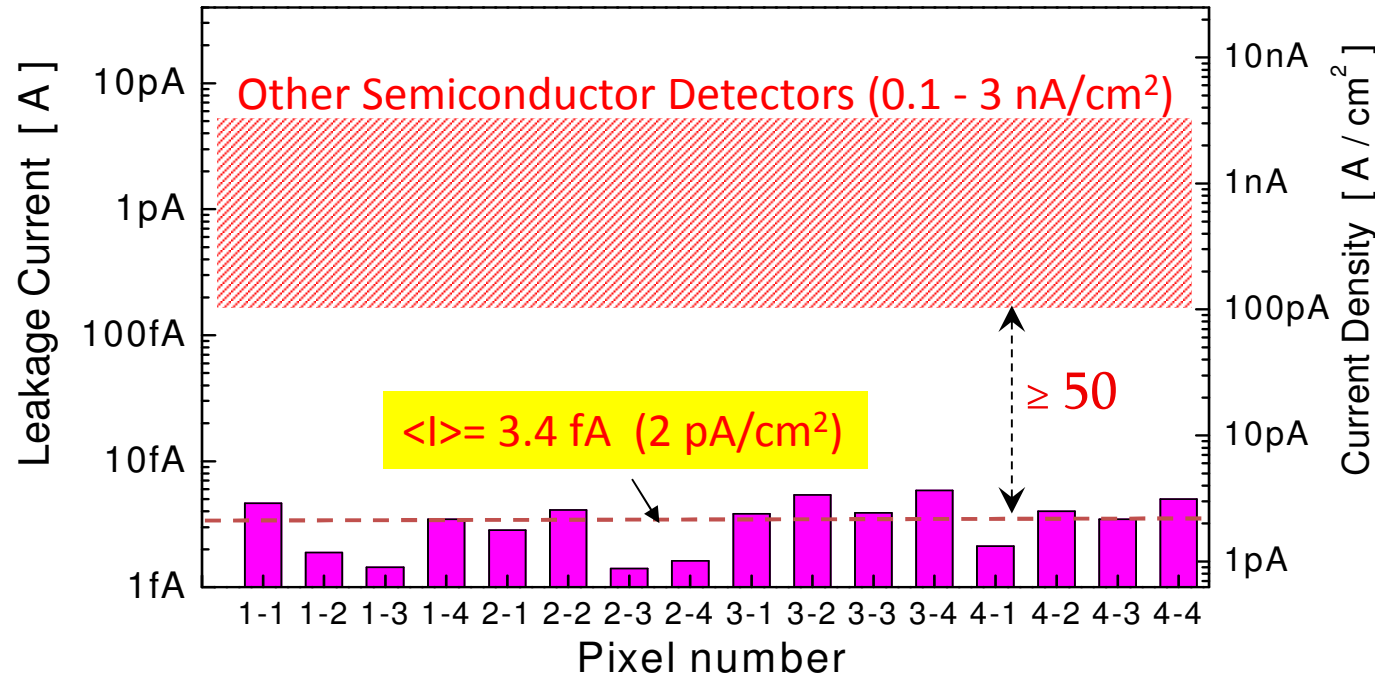
two examples:

- A Complex, High Functionality, Ultra Low-Noise ASIC
- An Ultimate-Low Noise Fully Integrated CMOS Charge Sensitive Preamplifier

Silicon Carbide Pixel Detector



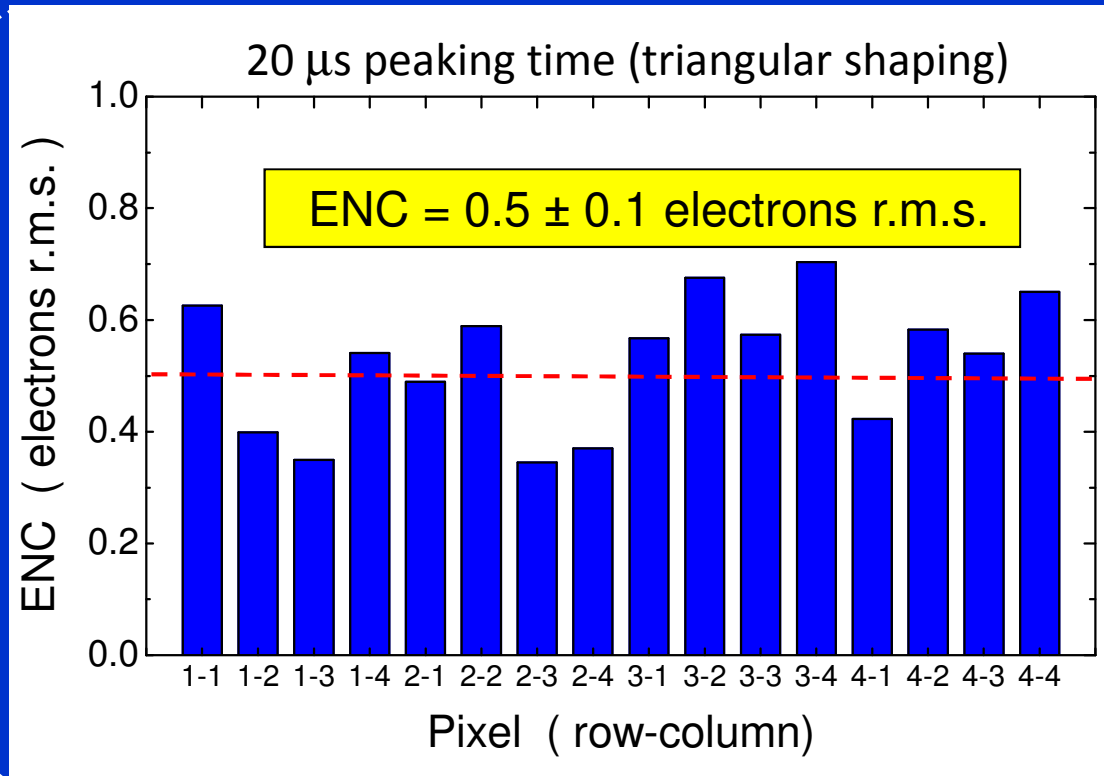
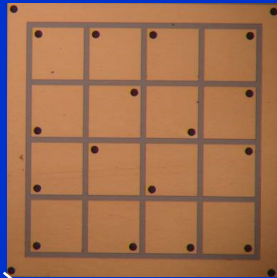
$T = + 27^{\circ}\text{C}$



G. Bertuccio et al. IEEE Trans. Nucl. Sci. 53, 2006

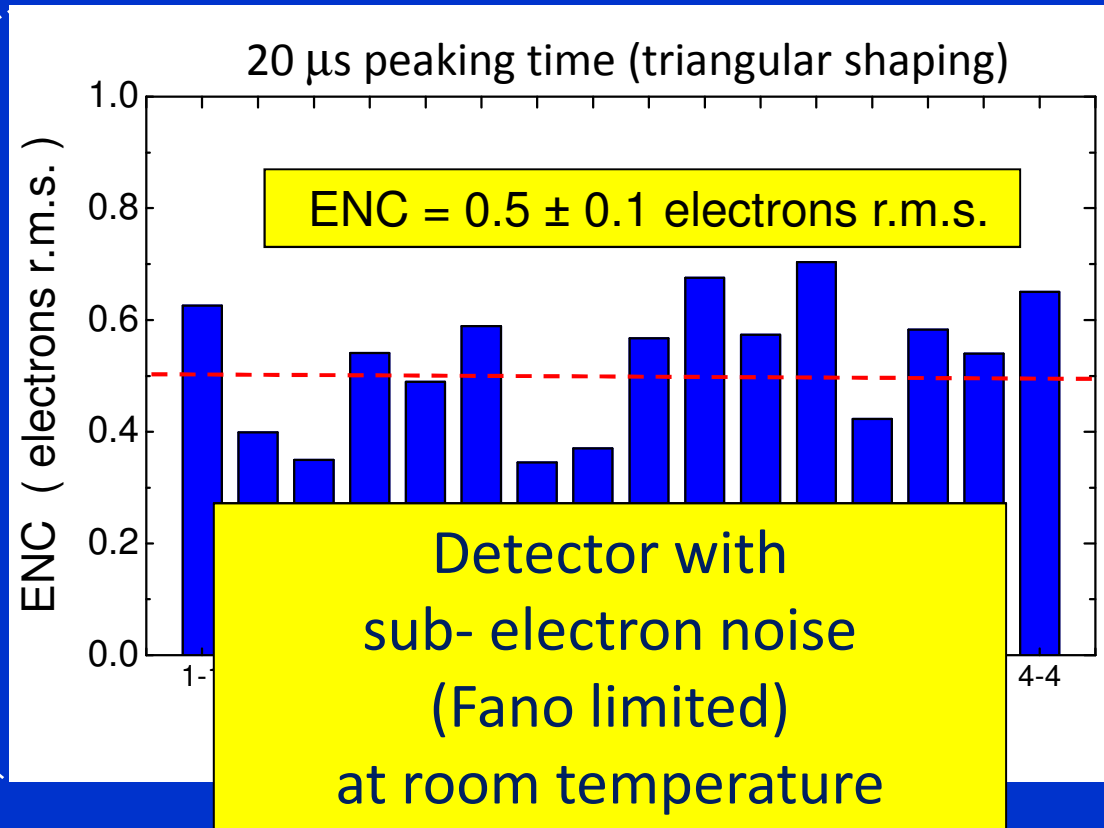
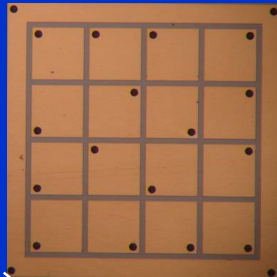
The SiC detector's noise

$$ENC(I_{Leak}) \cong 2\sqrt{I_{Leak} [pA]} \sqrt{\tau_{peak} [\mu s]} [e^- r.m.s.]$$



The SiC detector's noise

$$ENC(I_{Leak}) \cong 2\sqrt{I_{Leak} [pA]} \sqrt{\tau_{peak} [\mu s]} [e^- r.m.s.]$$



Ultimate Low Noise Charge Sensitive Amplifier needed!

2007: a breakthrough in CMOS CSA



Available online at www.sciencedirect.com



Nuclear Instruments and Methods in Physics Research A 579 (2007) 243–246

**NUCLEAR
INSTRUMENTS
& METHODS
IN PHYSICS
RESEARCH**
Section A

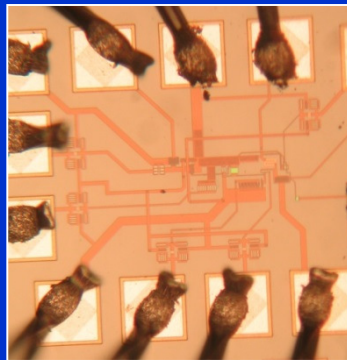
www.elsevier.com/locate/nima

Progress in ultra-low-noise ASICs for radiation detectors

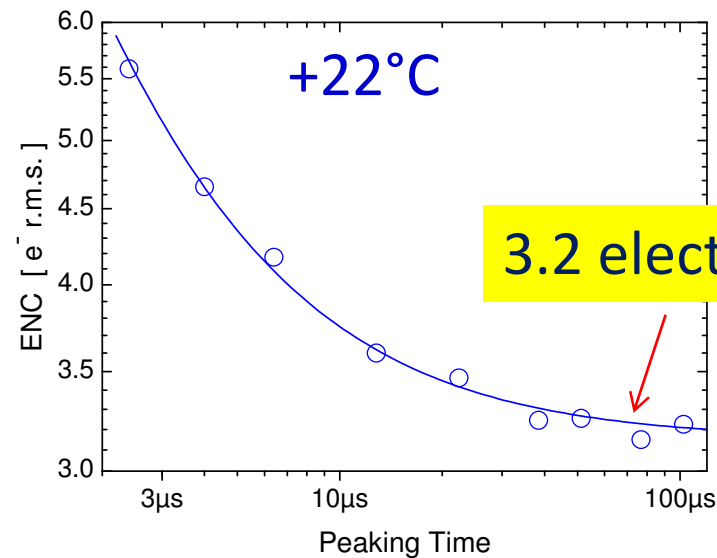
Giuseppe Bertuccio^{a,b,*}, Stefano Caccia^{a,b}

^aPolitecnico di Milano, Department of Electronics Engineering and Information Science, P.za L. da Vinci 32, 20133 Milano, Italy

^bNational Institute of Nuclear Physics, INFN sez., Milano, Italy



Technology
CMOS 0.35 μ m (AMS)



The ultimate CMOS low noise challenge

Is one electron noise CMOS Charge Amplifier possible ?

from 3 to 1 electron r.m.s.
small numbers, a Big step

The ultimate CMOS low noise challenge

Is one electron noise CMOS Charge Amplifier possible ?

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$$ENC_{TOTAL} = \sqrt{ENC_{N1}^2 + ENC_{N2}^2 + ENC_{N3}^2 + ENC_{N4}^2 + ENC_{N5}^2 + ENC_{N6}^2 + ENC_{N7}^2 + ENC_{N8}^2 + ENC_{N9}^2}$$

$$ENC_{TOTAL} = \sqrt{1 + 1 + 1 + 1 + 1 + 1 + 1 + 1 + 1} = \sqrt{9} = 3 \text{ electrons r.m.s.}$$

↓ - 55 %

$$ENC_{TOTAL} = \sqrt{1 + 1 + 1 + 1 + \cancel{1} + \cancel{1} + \cancel{1} + \cancel{1} + \cancel{1}} = \sqrt{4} = 2 \text{ electrons r.m.s.}$$

↓ - 50 %

$$ENC_{TOTAL} = \sqrt{1 + 1 + \cancel{1} + \cancel{1} + \cancel{1} + \cancel{1} + \cancel{1} + \cancel{1} + \cancel{1}} = \sqrt{1} = 1.4 \text{ electrons r.m.s.}$$

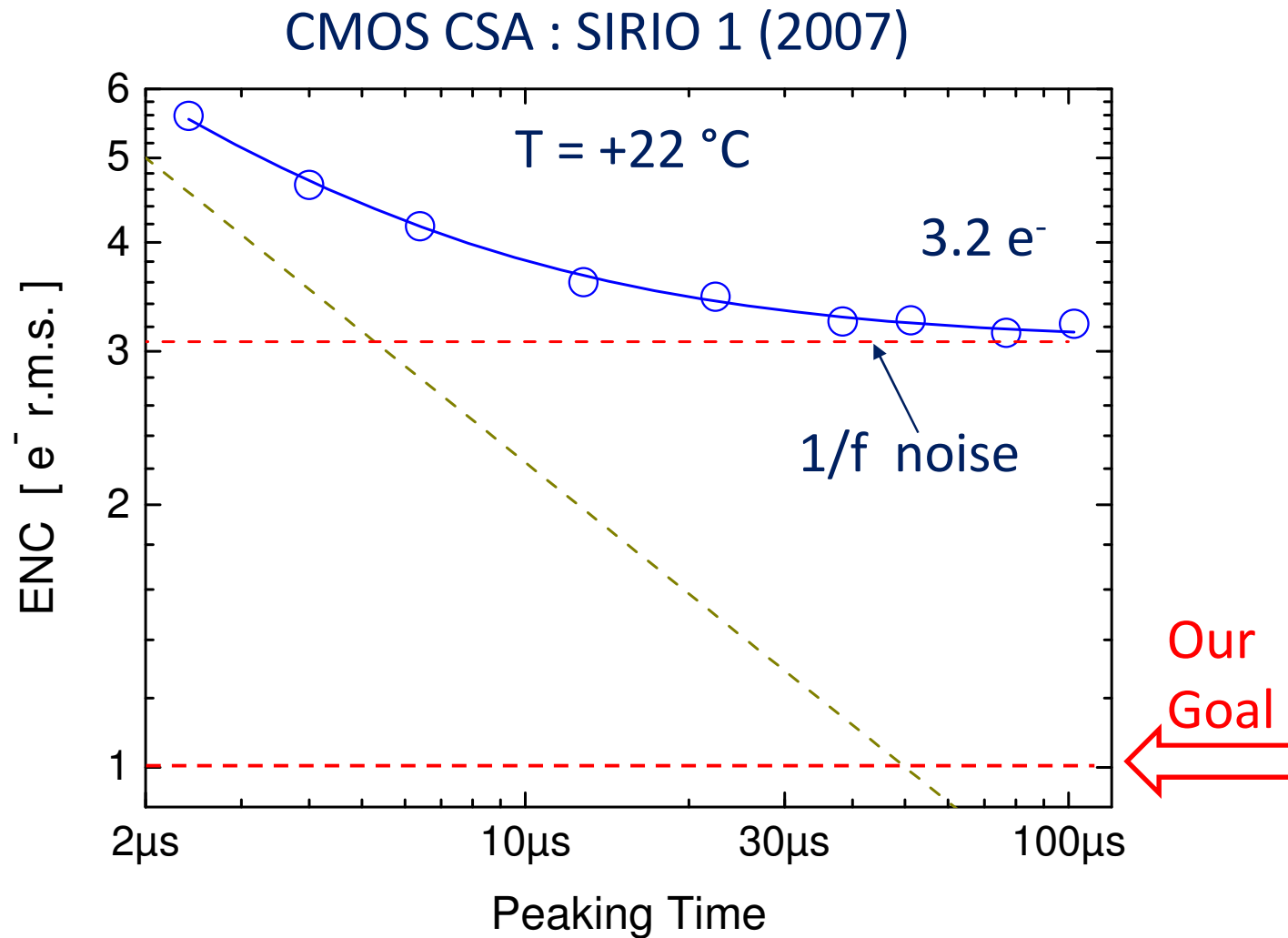
↓ - 50 %

$$ENC_{TOTAL} = \sqrt{1 + \cancel{1} + \cancel{1} + \cancel{1} + \cancel{1} + \cancel{1} + \cancel{1} + \cancel{1} + \cancel{1}} = \sqrt{1} = 1 \text{ electrons r.m.s.}$$

Noise
removed

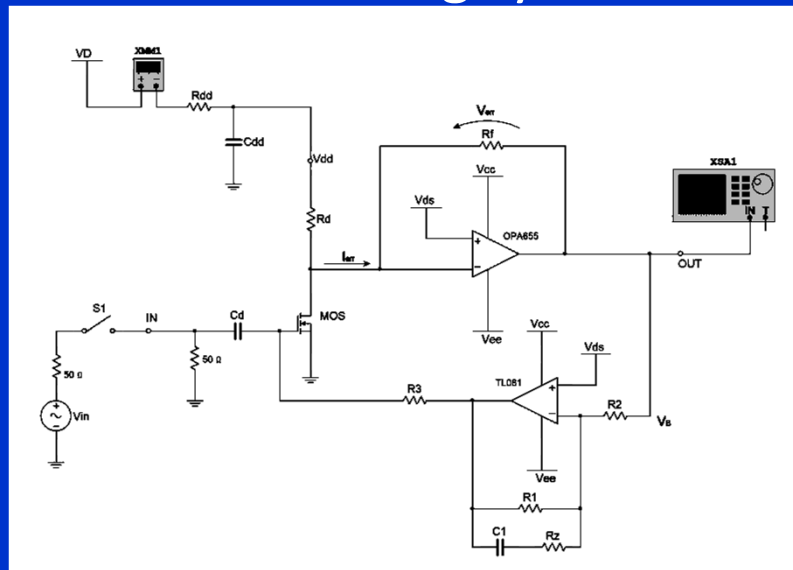
- 90 %

The $1/f$ noise limit...

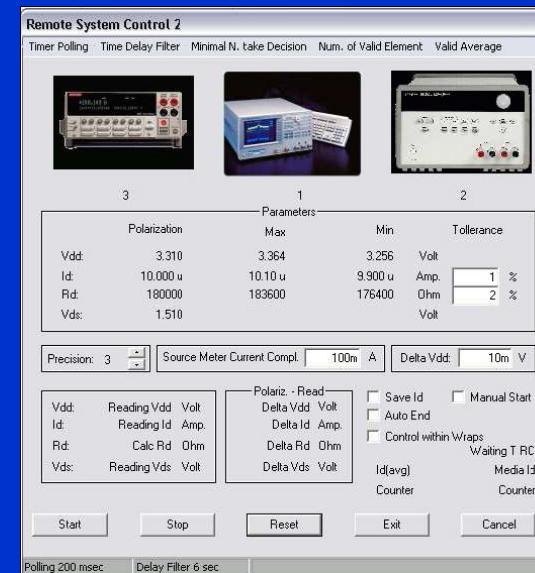


Study of CMOS Low Frequency Noise

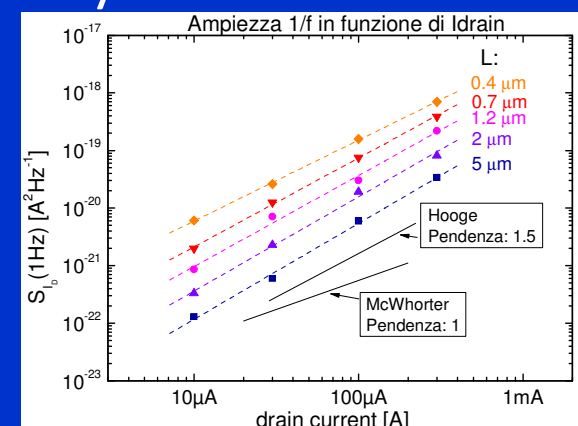
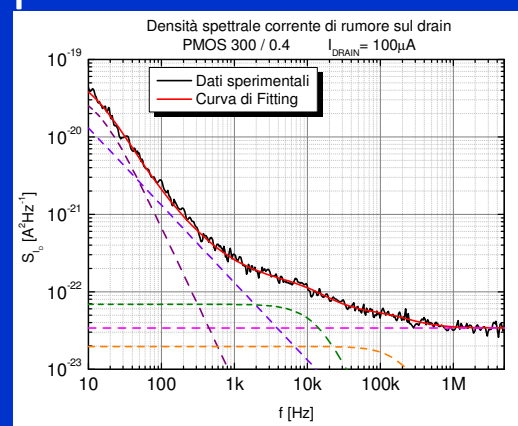
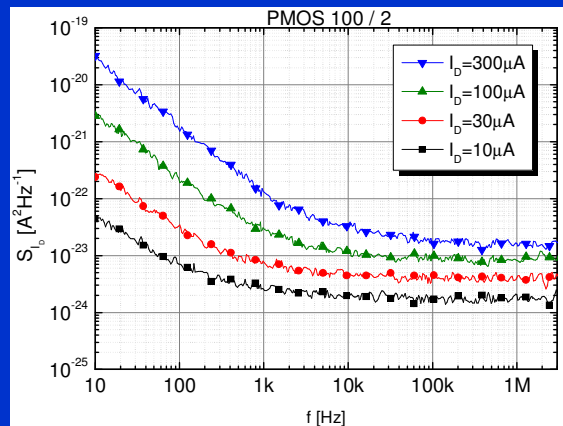
Measuring system



User Interface



Experimental data and analysis



Low Frequency Noise of Transistors

The heritage of Franco Manfredi and of his Research Group:
the development of Instrumentation for accurate measurement of the noise spectral density



Extremely low-noise amplifier for interfacing active devices to instruments for spectral analysis

P. F. Manfredi¹, V. Speziali¹ and F. Svelto¹

Rev. Sci. Instrum. 65, 3848 (1994); <http://dx.doi.org/10.1063/1.1145176>



Nuclear Physics B (Proc. Suppl.) 44 (1995) 599–606

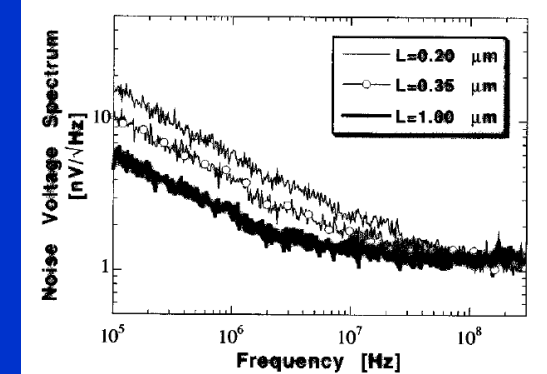
NUCLEAR PHYSICS B
PROCEEDINGS
SUPPLEMENTS

Instrumentation for High Accuracy Noise Characterisation of Front-End Devices
in Detector Applications

V. Re and F. Svelto

Dipartimento di Elettronica, Università di Pavia, Via Abbiategrasso 209, 27100 Pavia, Italy, and
INFN, Sezione di Milano, Via Celoria 16, 20133 Milano, Italy

IEEE TRANSACTIONS ON NUCLEAR SCIENCE, VOL. 49, NO. 3, JUNE 2002



1281

Instrumentation for Noise Measurements on CMOS Transistors for Fast Detector Preamplifiers

M. Manghisoni, L. Ratti, V. Re, and V. Speziali



G. Bertuccio, "Toward the Ultimate Low-Noise Front-End Electronics for X-γ Ray Detectors: the heritage of Franco Manfredi to current research"

Aula Volta, University of Pavia, Italy, 5 December 2016

slide 52



CMOS CSA: what possible ENC?

IEEE TRANSACTIONS ON NUCLEAR SCIENCE, VOL. 56, NO. 3, JUNE 2009

1511

Noise Minimization of MOSFET Input Charge Amplifiers Based on $\Delta\mu$ and ΔN $1/f$ Models

Giuseppe Bertuccio and Stefano Caccia

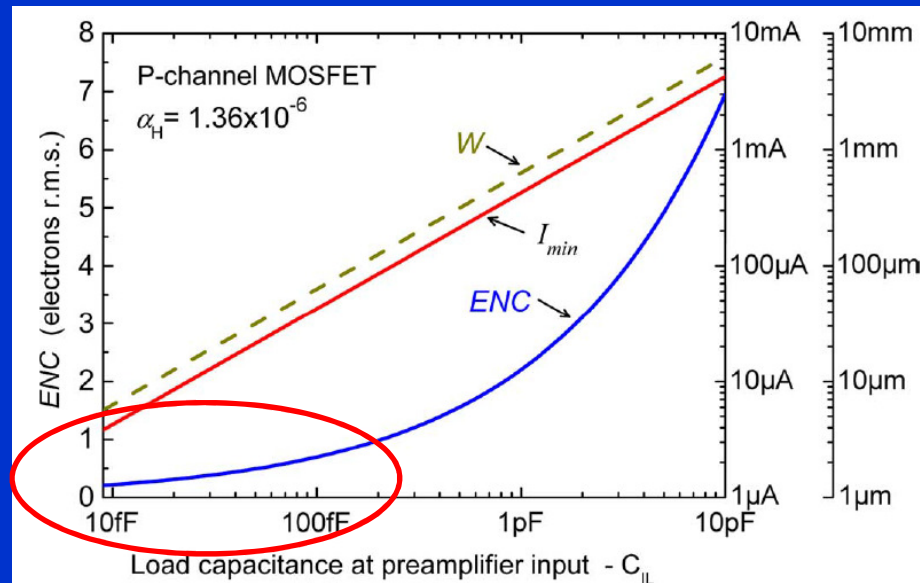


Fig. 8. $\Delta\mu$ -type MOSFETs: minimum $ENC_{1/f}$ calculated as a function of the load capacitance C_{IL} at the preamplifier input (13a). Also, the MOSFETs bias current I_{min} (13b) and its optimum gate width obtained by setting $C_G = C_{IL}$ are shown (33). The parameters for the evaluation are shown in Table I.

ENC < 1 e- r.m.s.

Predicted for $C_{input} < 100$ fF
(Pixel & Drift Detectors)

Prediction based on
experimental data

IEEE Trans. Nucl. Science 56, 2009

1/f & white noise optimization

$$ENC^2 = ENC_{ws}^2 + ENC_{1/f}^2 + ENC_{wp}^2$$

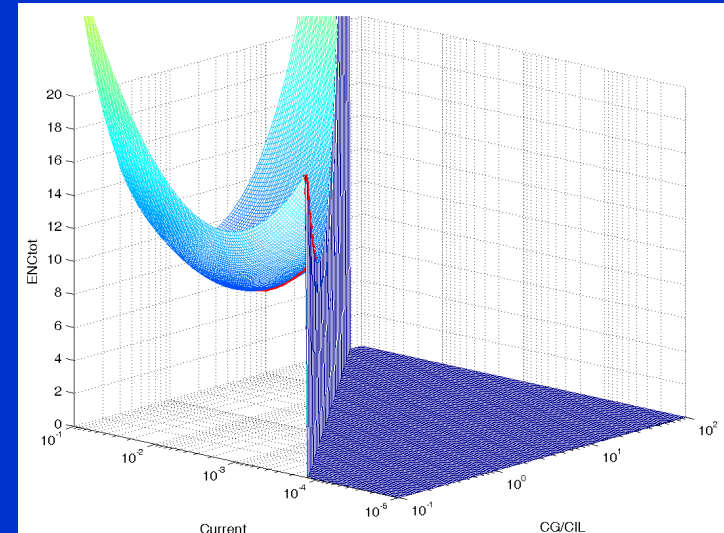
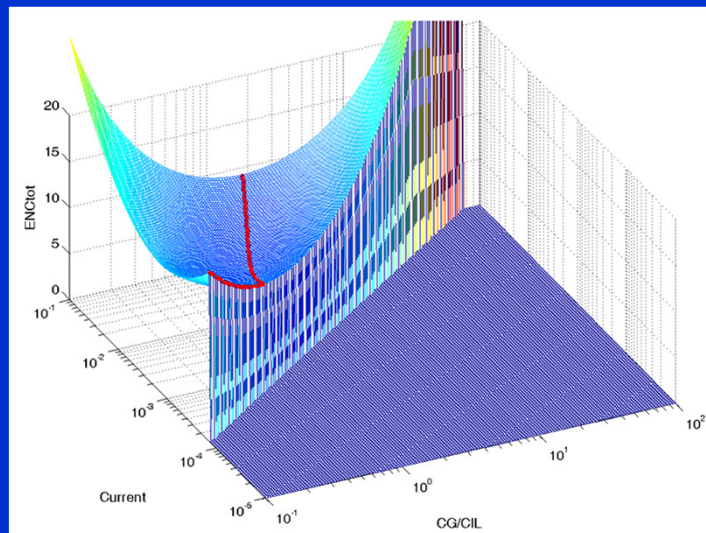
for p-MOSFET input ($\Delta\mu$ 1/f noise model)

$$ENC_{tot(\Delta\mu)}^2 = k_{ws} \frac{(C_{IL} + C_G)^2}{\sqrt{C_G}} \frac{1}{\sqrt{I}} \frac{1}{\tau} + k_{\Delta\mu} \frac{(C_{IL} + C_G)^2}{\sqrt{C_G^3}} \sqrt{I} + k_{wp} \tau$$

$$k_{ws} = A_1 \left(\frac{\beta 4kT L}{q^2 \sqrt{2\mu/n}} \right)$$

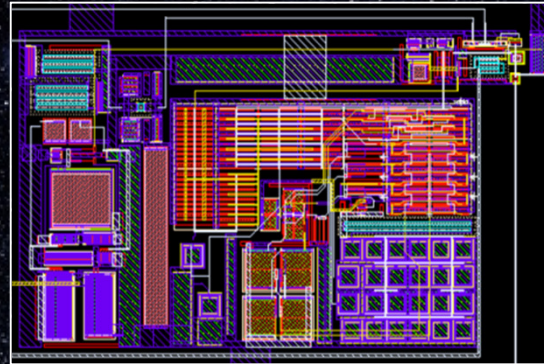
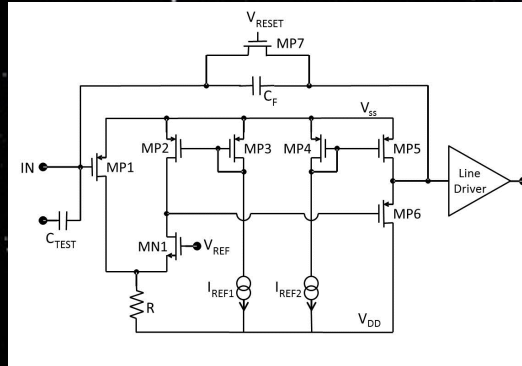
$$k_{\Delta\mu} = 2\pi A_2 \left(\frac{n\alpha_H L}{q\sqrt{2\mu}} \right)$$

$$k_{wp} = \frac{A_3}{q^2} S_I$$



IEEE Trans. on Nuclear Science, Vol. 56, 2009, pp. 1511

SIRIO Charge Sensitive Amplifier



Technology: AMS CMOS 0.35 μm

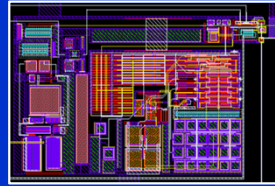
4 metals / 2 Poly / High Resistive Poly

$V_{DD} = 3.3\text{V}$

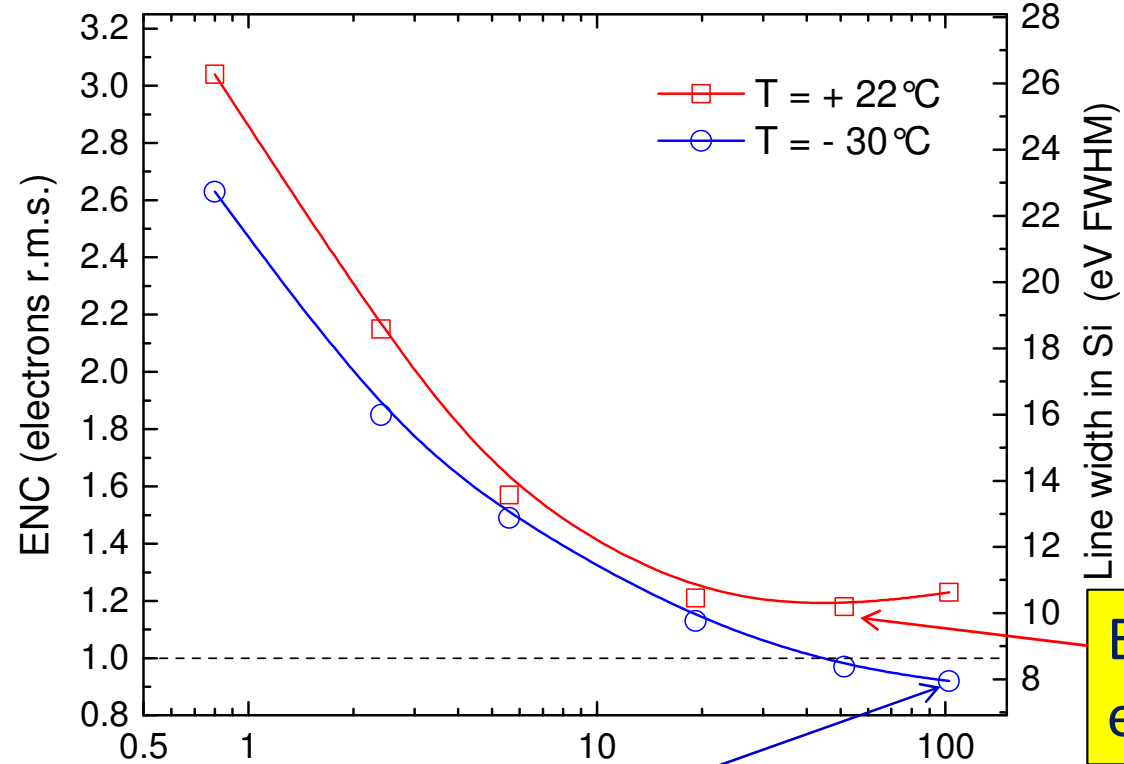
Size: 530 μm x 450 μm

- Optimized for Silicon Drift Detectors ($C_{DET} \leq 0.1\text{pF}$)
- Designed for X-ray Spectroscopy: from 50 eV to 50 keV

SIRIO: measured ENC



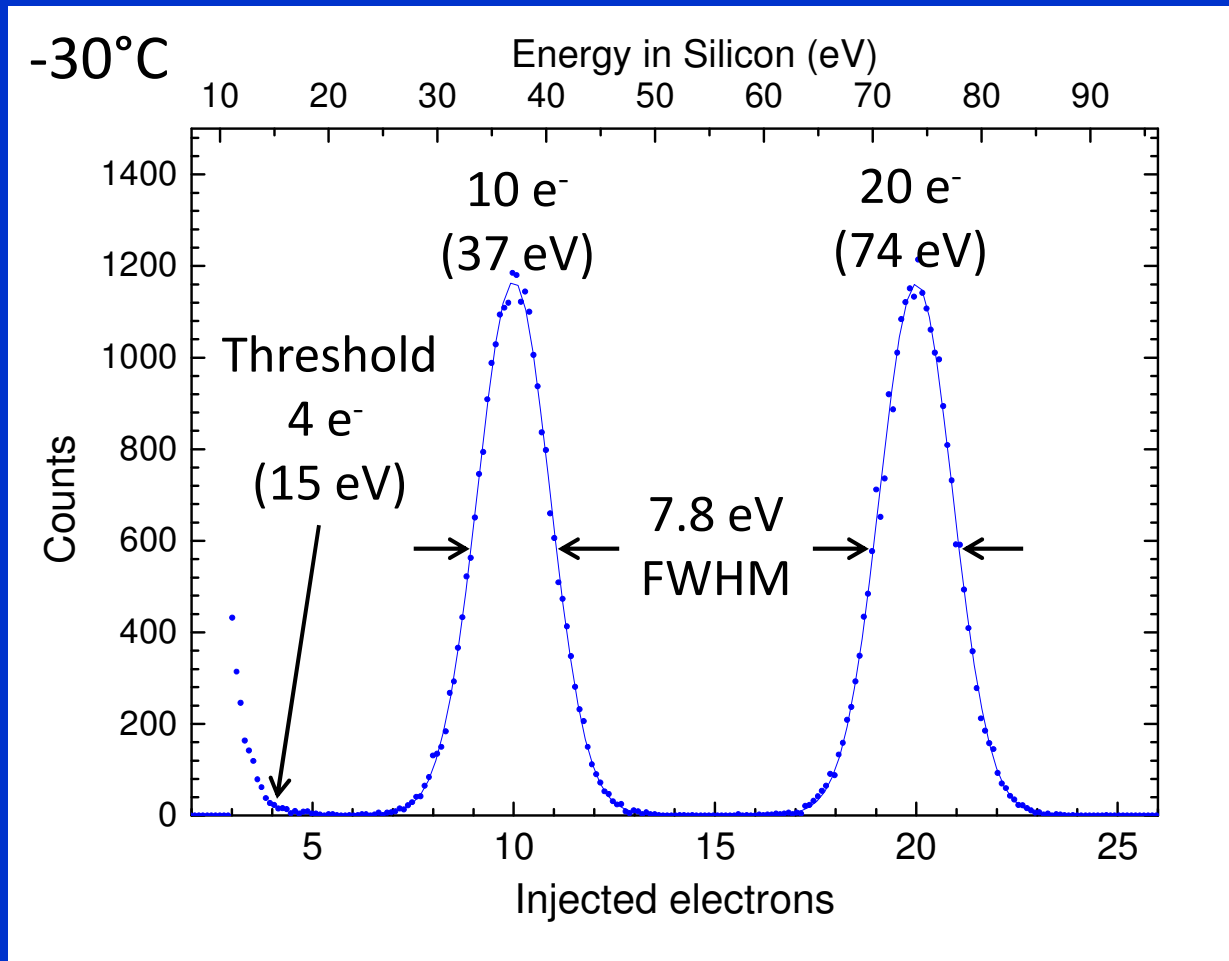
G. Bertuccio et al., 2014 IEEE Nuclear Science Symposium and Medical Imaging Conference



$\text{ENC}_{\min} = 1.18$
electron r.m.s.

$\text{ENC}_{\min} = 0.92$ electron r.m.s.

Intrinsic noise (no detector) @ -30°C



$$\text{ENC} = 0.89 \text{ e}^- \text{ r.m.s.}$$

Franco Manfredi and Emilio Gatti

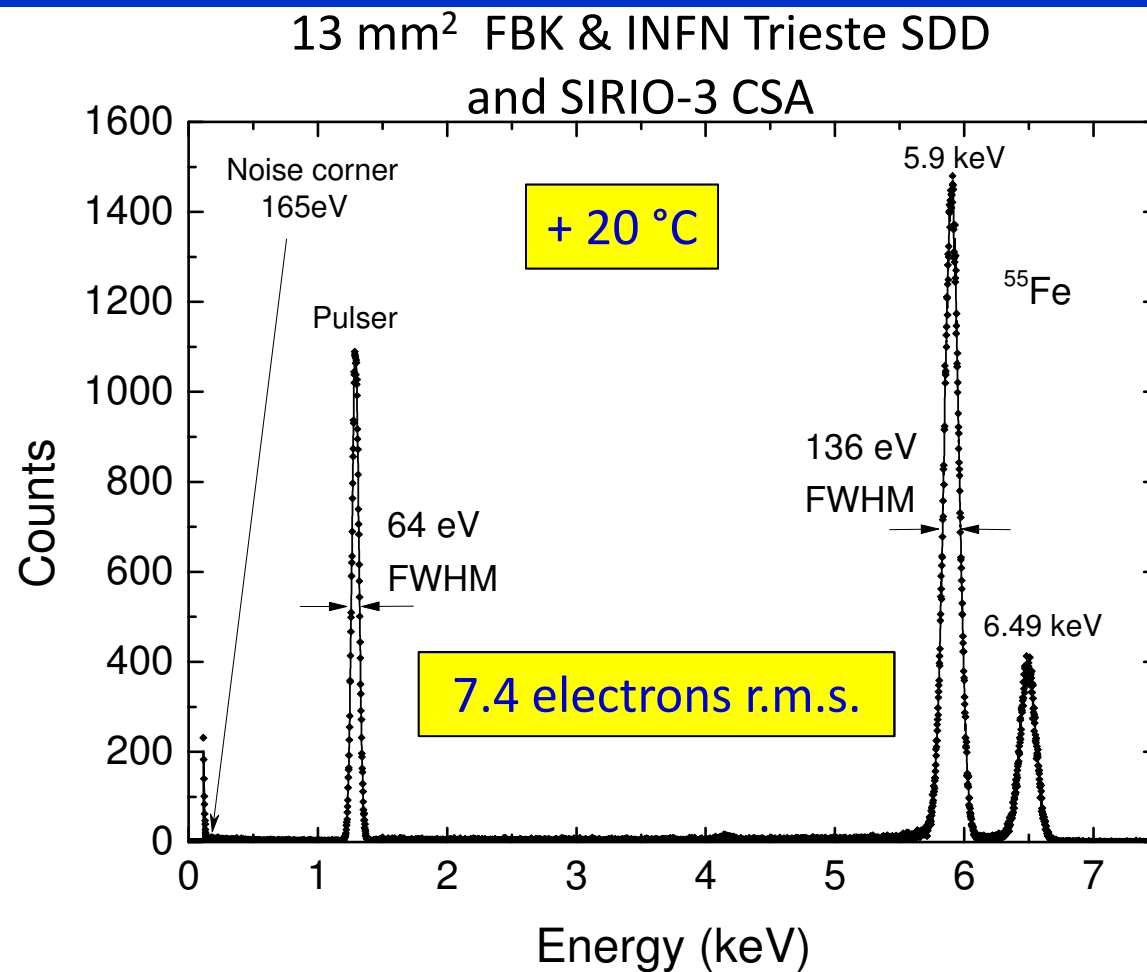
On 8 December 2009, Emilio Gatti and I celebrated the 50 years of scientific cooperation and a very nice friendship.



Scientific life dedicated to
the **design of Low Noise
Front-End Electronics**
for Radiation Detectors

Inventor of the
Charge Sensitive Amplifier
and of the
Silicon Drift Detector

Today 2016: SIRIO and SDD



G. Bertuccio et al. IEEE TNS, 63, 2016

Highest Energy Resolution reached at Room Temperature
with a semiconductor detector of large area

Acknowledgements

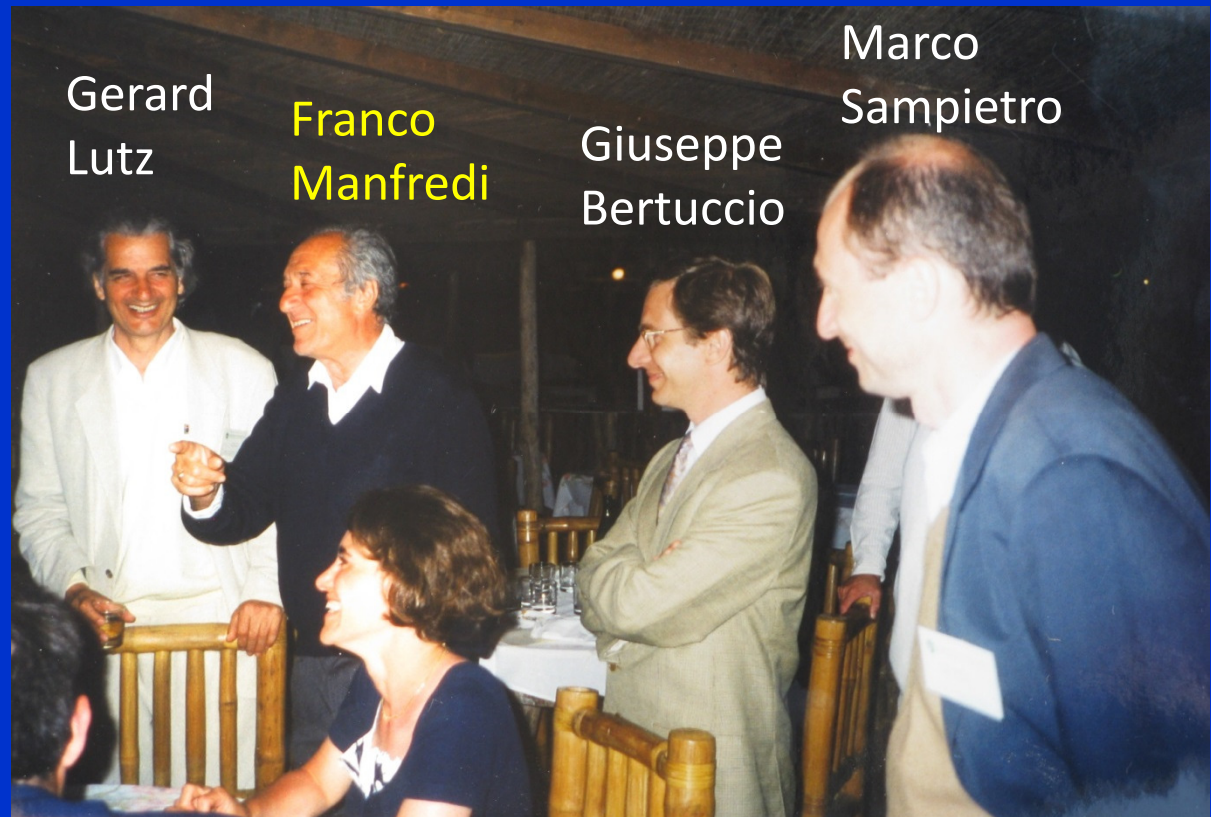
Ahangarianabhari M., Bastia P., Bellutti P., Caccia S., Cappelluti I.,
Grassi M., Graziani C., Macera D., Malcovati P., Martin D.,
Masci S., Picciotto A., Piemonte C., Rachevsky A., Ratti N.,
Rossi T., Pozzi A., Sampietro M., Shi Y.,
Vacchi A., Zampa G., Zampa N., Zorzi N.

Conclusion

Two examples of results of the research in last years has been shown here...

the cultural heritage of Franco Manfredi is within these results.

At the 7th Pisa Meeting on Advanced Detectors
25 - 31 May 1997, Isola d'Elba, Italy



But the main Heritage which Franco Manfredi transmitted to all his collaborators is the passion for the design of low-noise front-end electronics for radiation detectors