

# SyncFEL

Advanced pixel detectors for hard X-rays at Synchrotron light and FEL sources

Torino people (~1.5 FTE)

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- CMOS 65nm electronics

M.Truccato, C.Lamberti, E.Vittone

- material science

Contributions:

- WP1 : definition of Instrument spec based on the experience made at ESRF for studies made at Torino (Truccato, Lamberti, Vittone).
- WP3 : interest to solutions for Analog Very Front End and digitisations - based on CHIPIX experience
- WP4: study of digital architecture, ASIC integration with DoT (IP-block, digital, analog) possible IP-block : PLL (to be seen)
- lab test of naked chip;
- pilot run at x-ray source (ESFR) with possible Torino case study; contribution to integration of DAQ into ESFR facility

DRAFT evaluation :

- Missioni : 3ke (2017-18); 5-10ke (2019)
- Consumi :  $\geq 5\text{ke}$  / anno
- AdR :  $\sim 1.5\text{ year} \Rightarrow \sim 45\text{ ke}$