Study of surface radiation damage of segmented Si sensors at Hamburg University: Results, status and next steps

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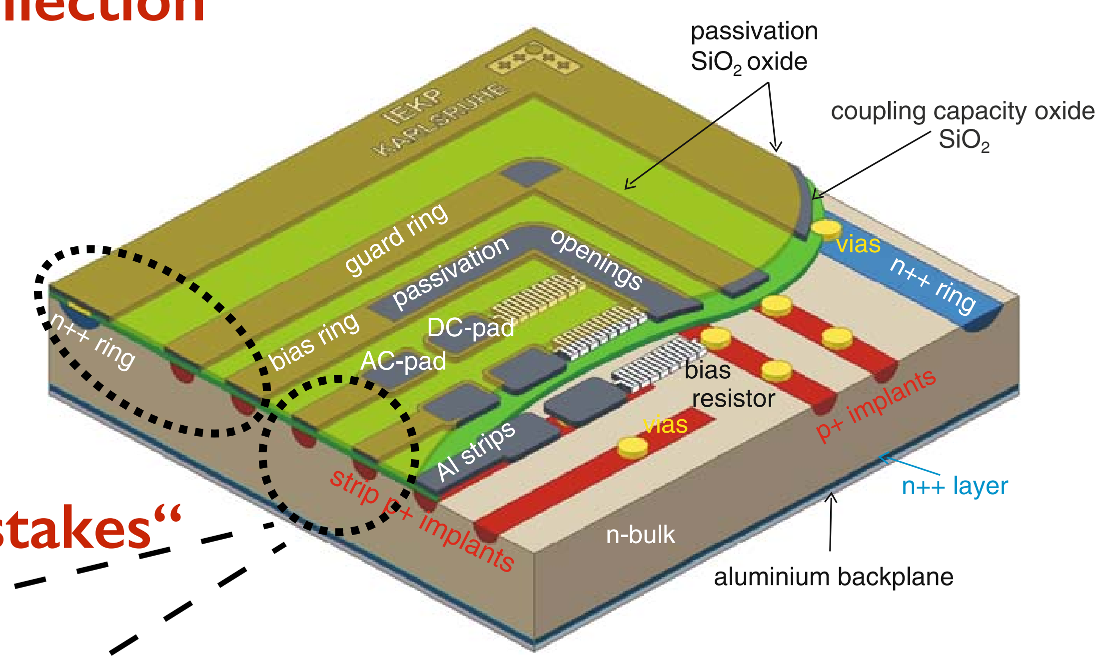
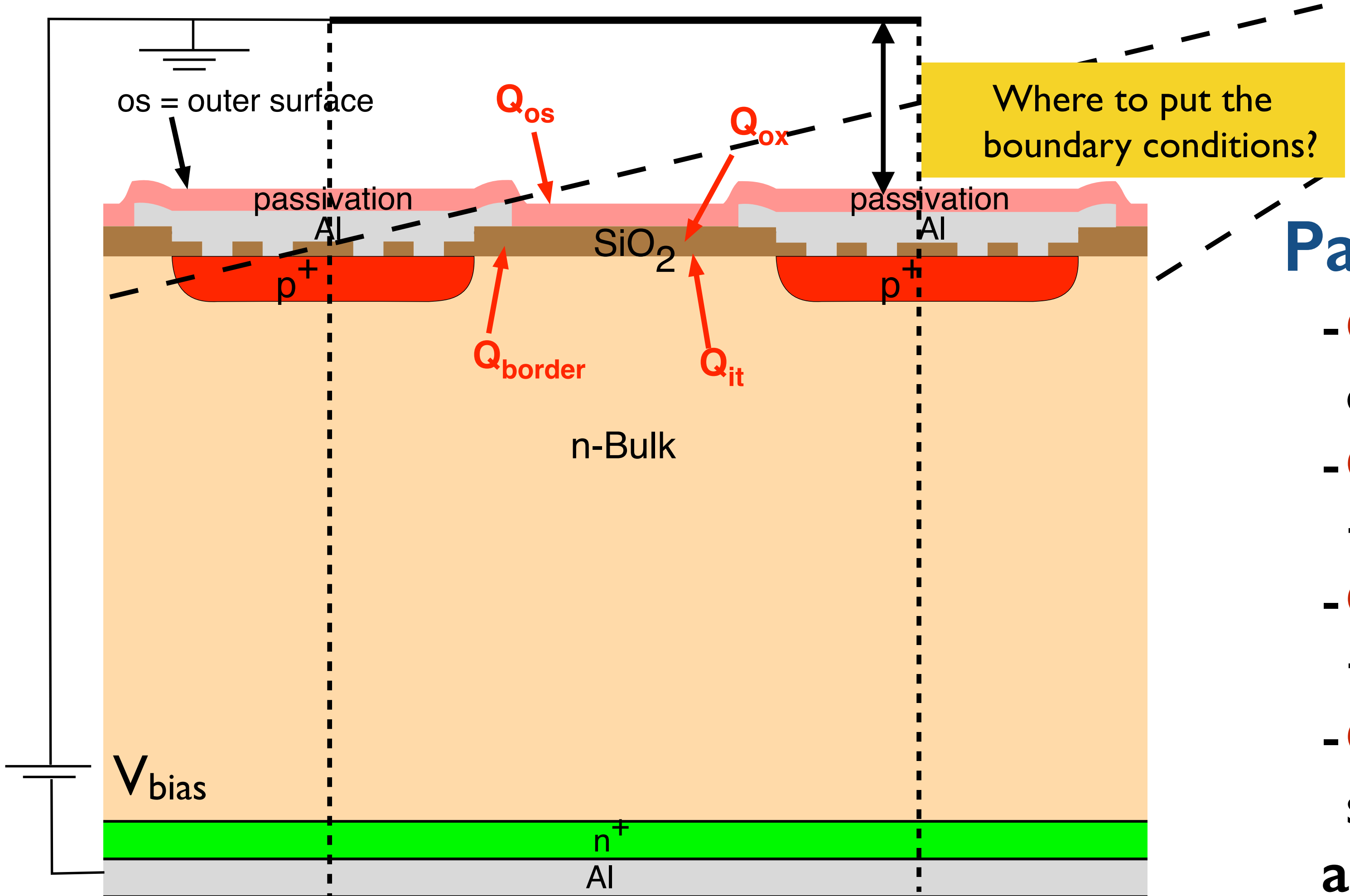
Outline

1. Introduction
2. Surface damage effects
3. Boundary conditions + humidity effects
4. Surface damage measurements during irradiation and with E-field
5. Summary

Introduction

- Surface effects influence the **stability** and the **charge collection** properties of segmented Si sensors
- Understanding and simulation of surface effects requires knowledge of **many parameters**
  → Methods have been developed to measure them
  → Parameter depend on technology (vendor)
    → needs characterization
- Simulation can help to **optimize designs** and avoid **„mistakes“**





Parameters determining the effects?

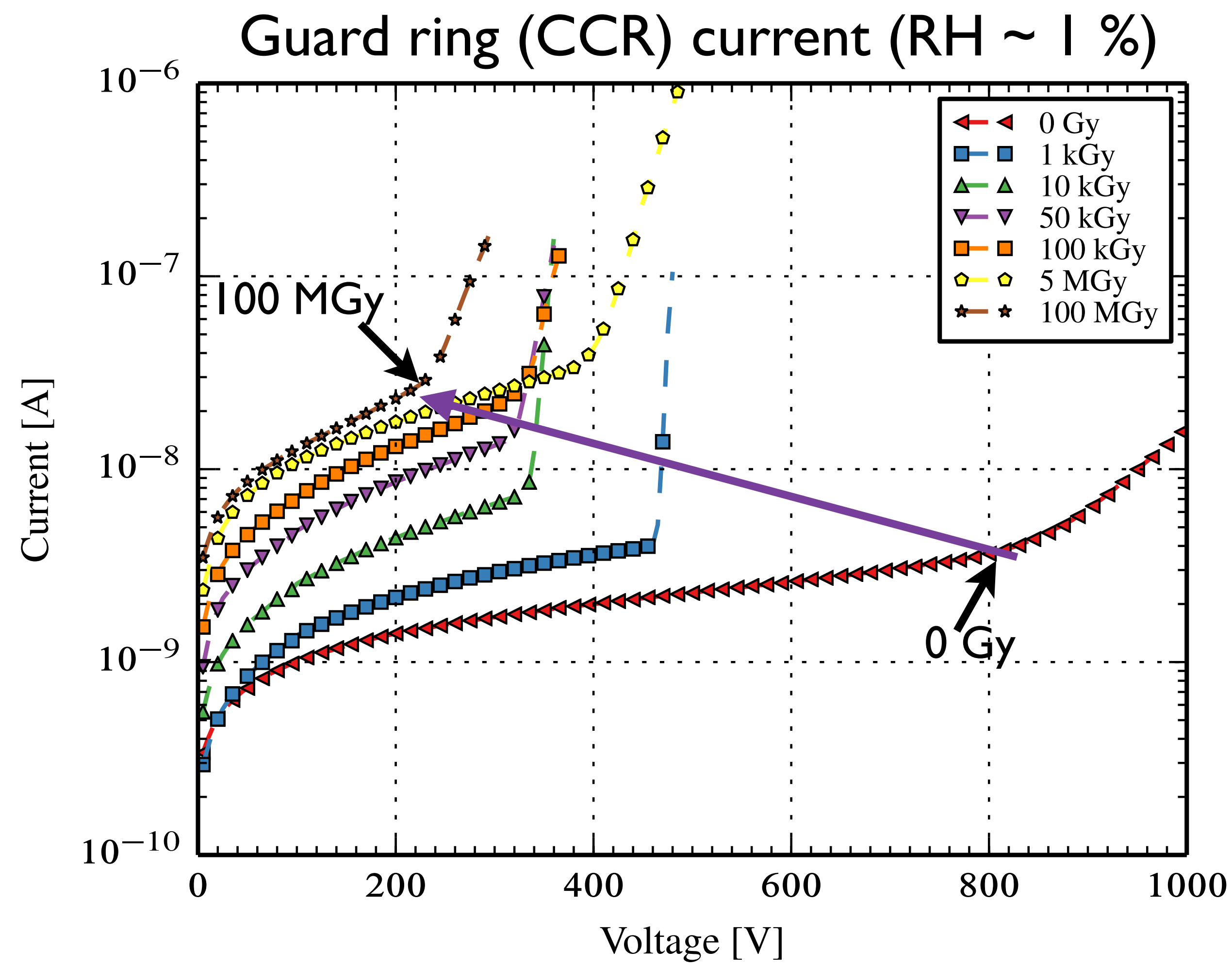
- Q_{os}: outer surface charge distribution → o.s. resistivity R_{sq} → time dependence
- Q_{ox}: “oxide” charge density → technology + surf. damage + time dependence
- Q_{border}: border trap density → technology + surf. damage + E-field + time dependence
- Q_{it}: interface trap density → technology + surf. damage + E_{Fermi}@interface

and **boundary conditions** for simulations

Surface damage effects

Example:

- **Breakdown voltage** of a Sintef p⁺-n guard ring decreases with X-ray dose from 800 V at 0 Gy to 200 V at 100 MGy



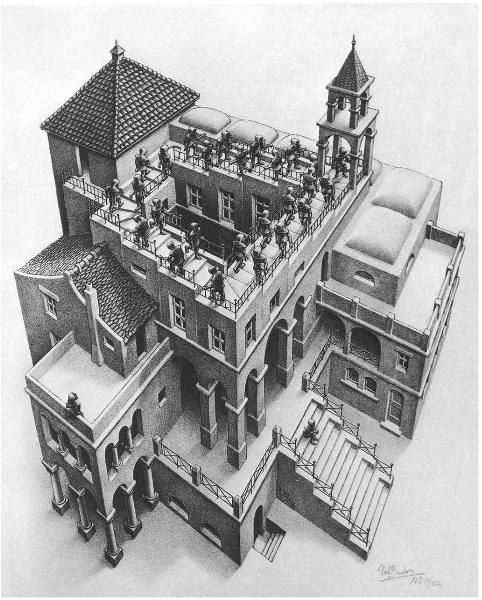
Problem:

- AGIPD p⁺-n pixel sensor for the European XFEL requires operation in vacuum and a **breakdown voltage** of **above 900 V** for doses between 0 G and **1 GGy**

Strategy of the optimization for radiation hardness:

- Measure parameter using test structures
- Simulate impact on sensors
- Verify simulations with measurements on sensors
- Use simulations to optimize the sensor design

(an infinite loop ?)



Can such an approach converge?

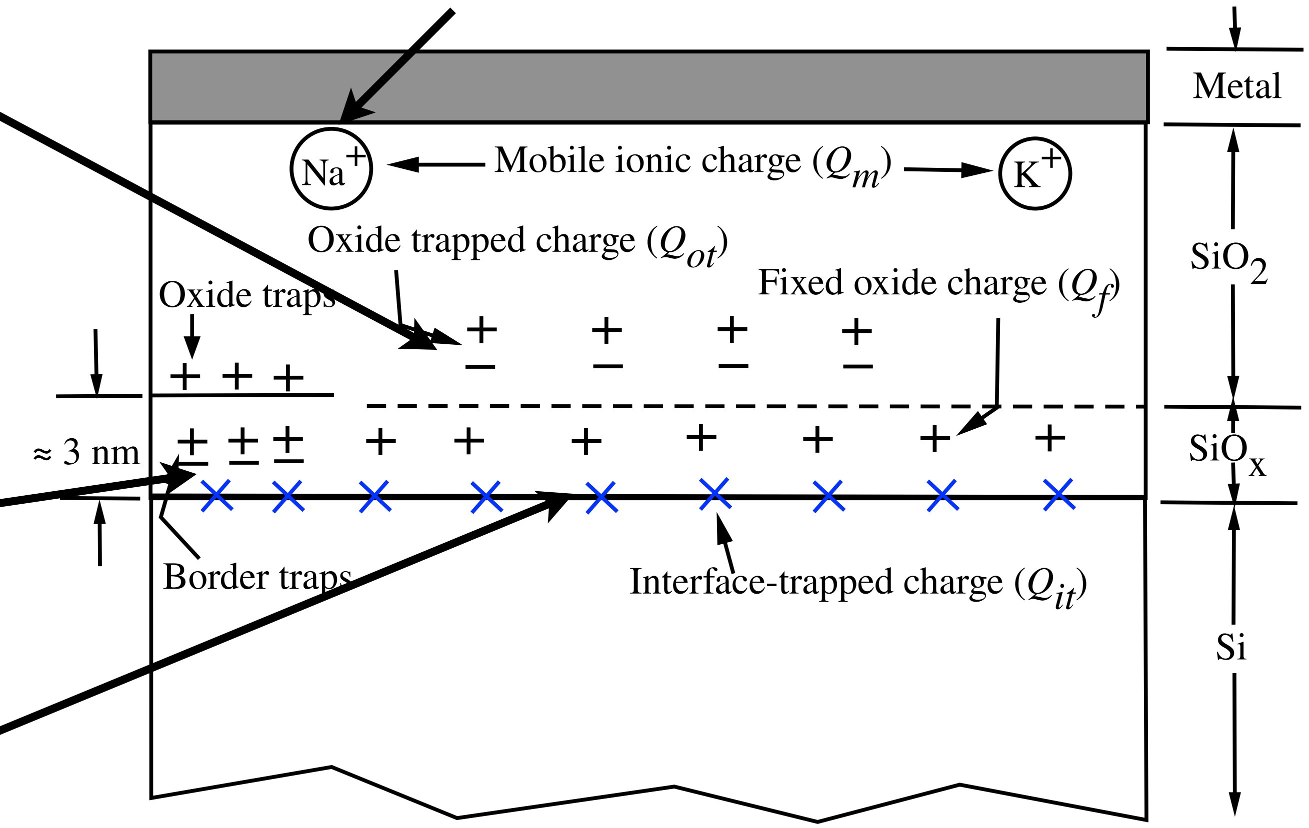
Oxide charges, interface traps and border traps

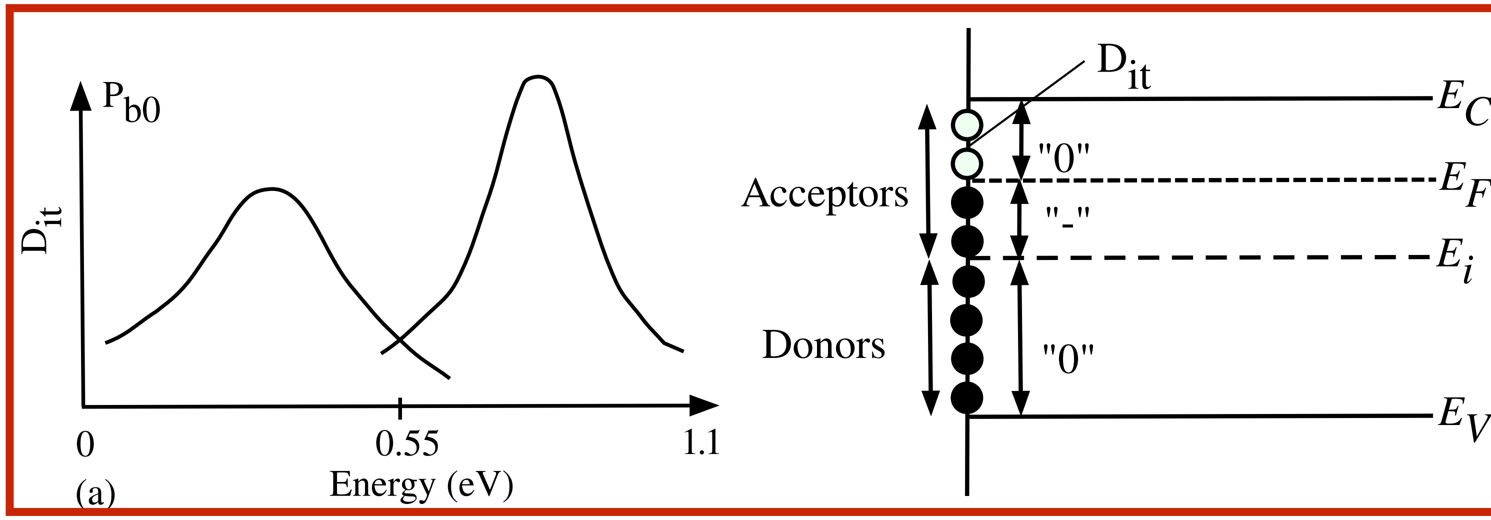
- **Oxide trapped charges (N_{ox}):**
  - Mainly positive oxygen-vacancy defects (one shallow trap → hole transport, + one deep trap E'$_\gamma$ @~3 eV) saturation: h-trapping = eh-recombination

- **Border traps ("+/-" to N_{ox}):**
  - E'$_\gamma$ defect can exchange charge with Si depending on Fermi-level on time scales > 0.01 s to seconds

- **Interface traps (D_{it}*)):**
  - Traps at interface (no barrier !) dangling bond defects (P_b)
  - No. limited by no. of dangling bonds
  - Charge state dep. on surface potential

Mobile Ionic Charge: usually not an issue anymore





*) Distribution of traps in the Si bandgap: D_{it} [(eV · cm^2)$^{-1}$]

A complex many parameter problem

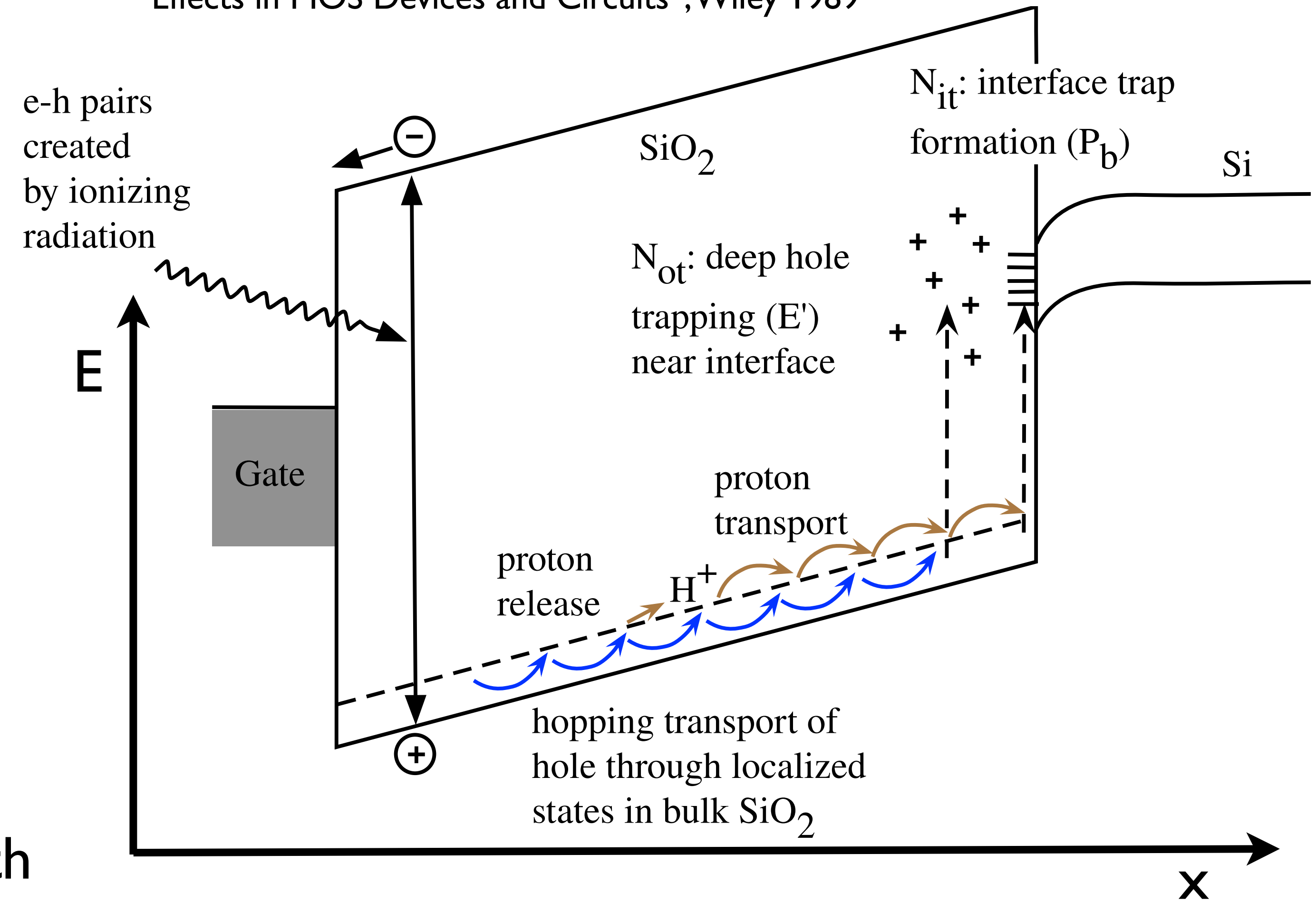
Defects by ionizing radiation

Simplified model of formation:

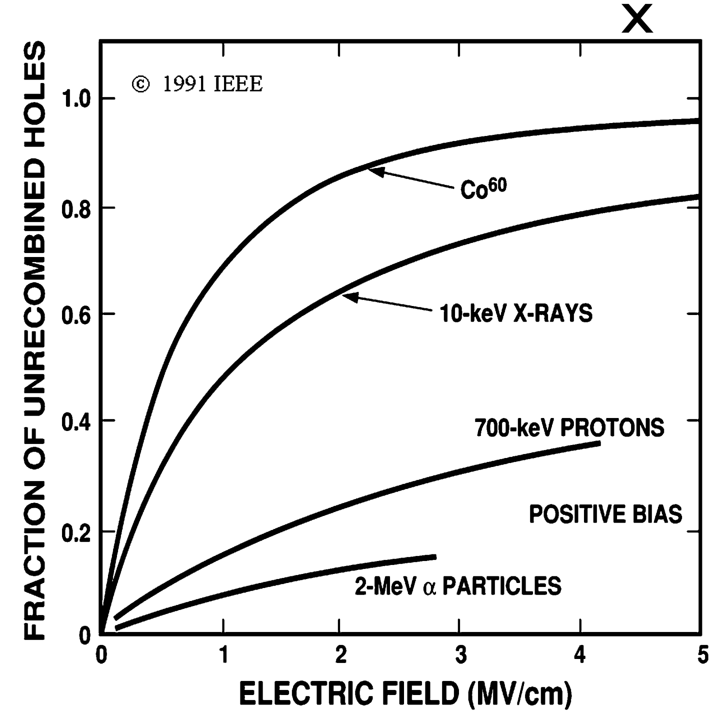
- Ionizing radiation produces electron-hole pairs in SiO_2
- Fraction of electron-hole pairs recombine
- Remaining electrons escape from SiO_2 [$\mu_e \sim 20$ cm²/(V•s)]
- Remaining holes will move toward the Si-SiO_2 interface [$\mu_h \leq 5 \cdot 10^{-5}$ cm²/(V•s)]

1. Oxide trapped charges: N_{ox}
2. Interface traps: $D_{it}(E)$
   ➡ Surface current

from T.P. Ma and Paul V. Dressendorfer, „Ionizing Radiation Effects in MOS Devices and Circuits", Wiley 1989



- Details depend on: Oxide thickness, growth and annealing, dose, dose rate, electrical field temperature, crystal orientation
- Also electron can be trapped (cross-section ≈ 10^{-17} cm²)



For simulations frequently used:

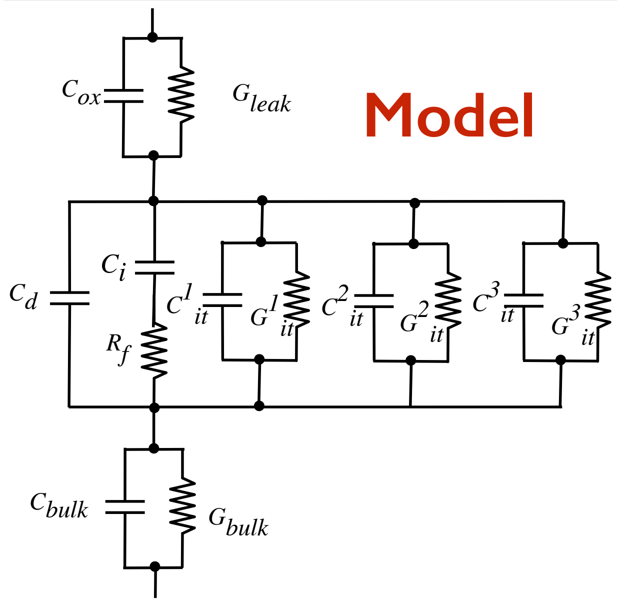
- Position-independent effective oxide charge density N_{ox}^{eff} [cm^{-2}]
- Position-independent surface recombination velocity s_0[cm/s] (or J_{surf} [A/cm²] where Si-SiO_2 interface is depleted)

Measurement: Oxide-charge density (N_{ox})

C/G-V+TDRC for MOS-C (from 4 vendors, <100> and <111>, surface damage by X-rays (0 - 1 GGy), no E-field)

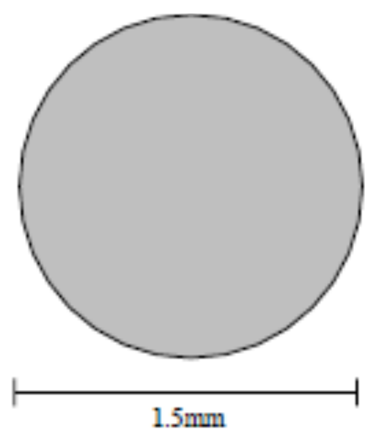
How to obtain reproducible results ?
(1) Annealing at 80°C for 10 min
(2) Stop voltage scan before strong inversion

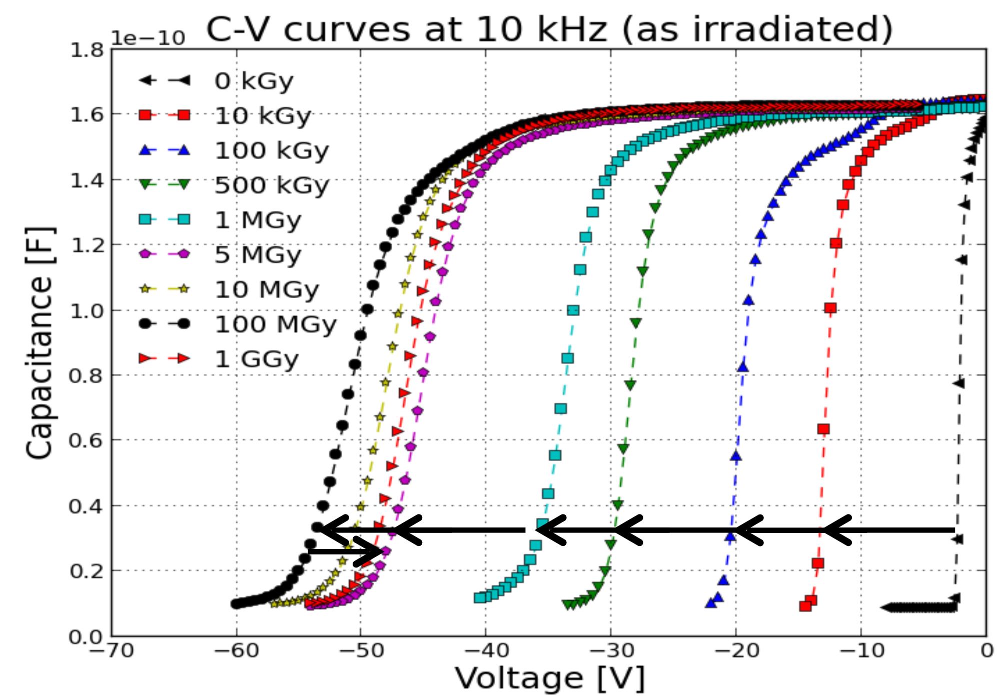
Analysis method:
- TDRC spectra → N_{it}^{i}
- Electric model → shape C/G (V,f)
- Shift along voltage axis → N_{ox}

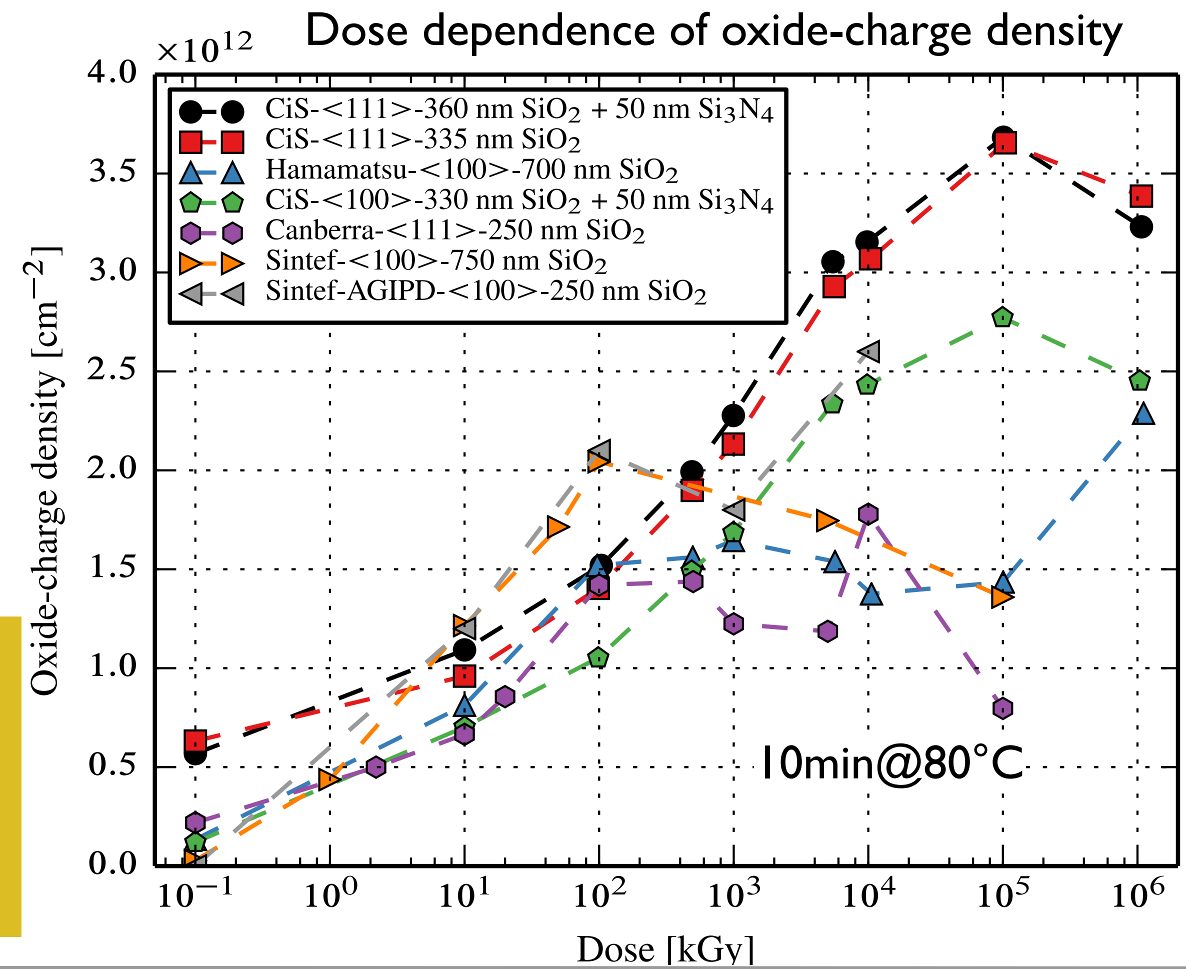


Model

MOS Capacitor



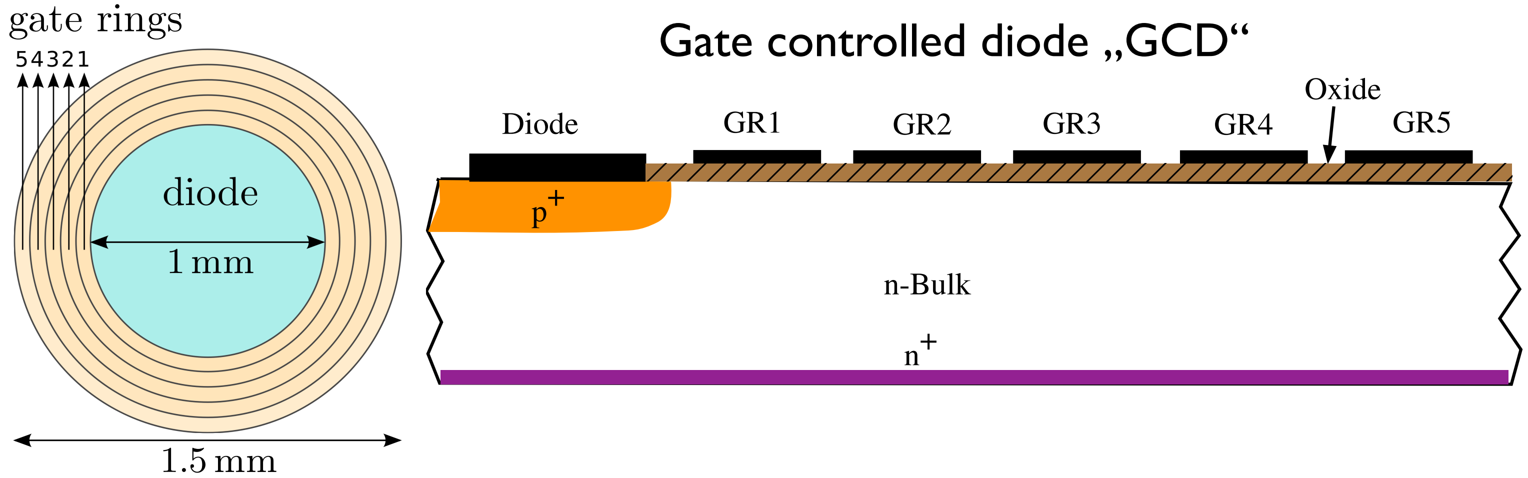


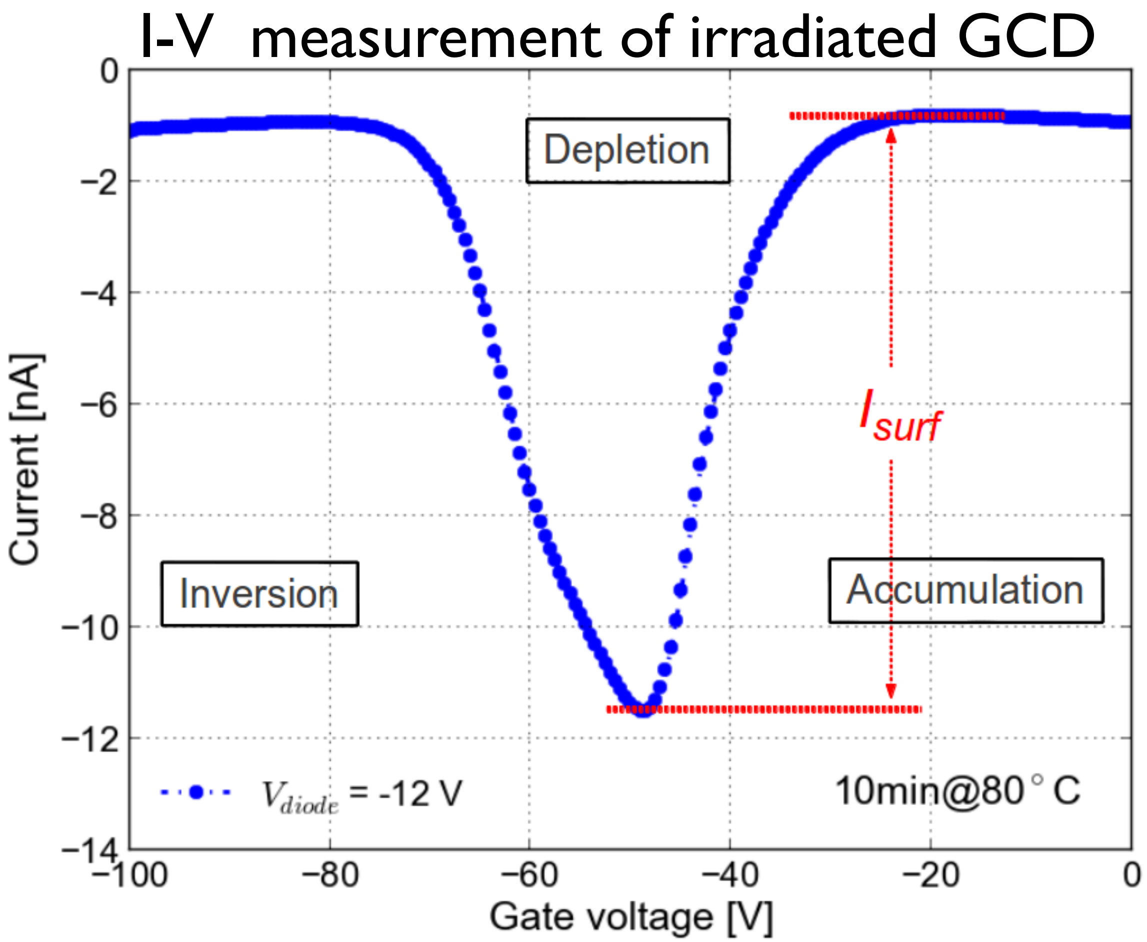


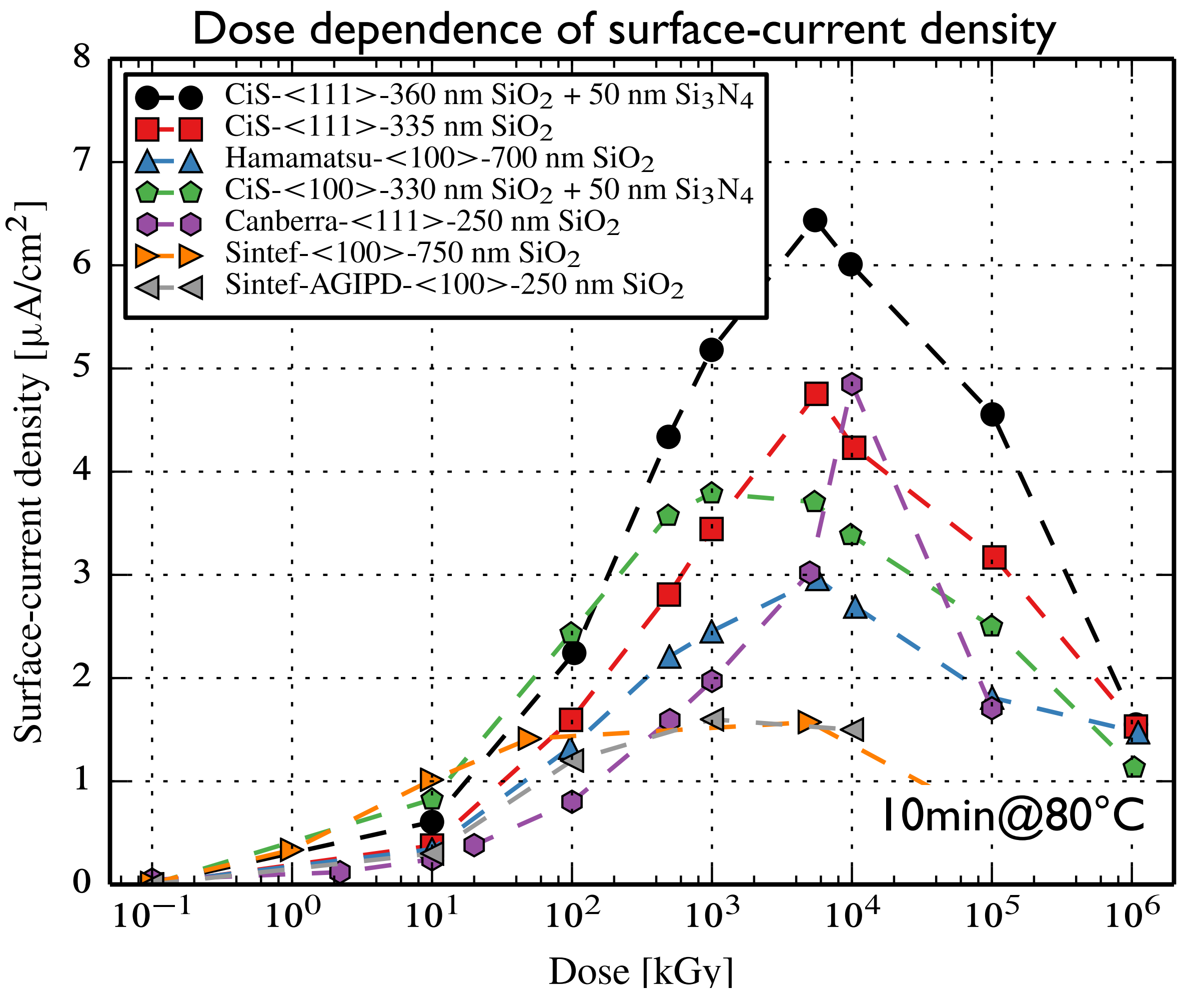
- X-ray radiation damage of N_{ox} saturates at high dose
- 0 Gy: N_{ox} for <111> » than for <100>
  higher dose values: difference is getting smaller
- For different vendors the values are within a factor ±2

Measurement: Surface-current (J_{surf})

I-V for Gate Controlled diodes (GCD) (from 4 vendors, <100> and <111>, surface damage by X-rays (0 - 1 GGy), no E-field)





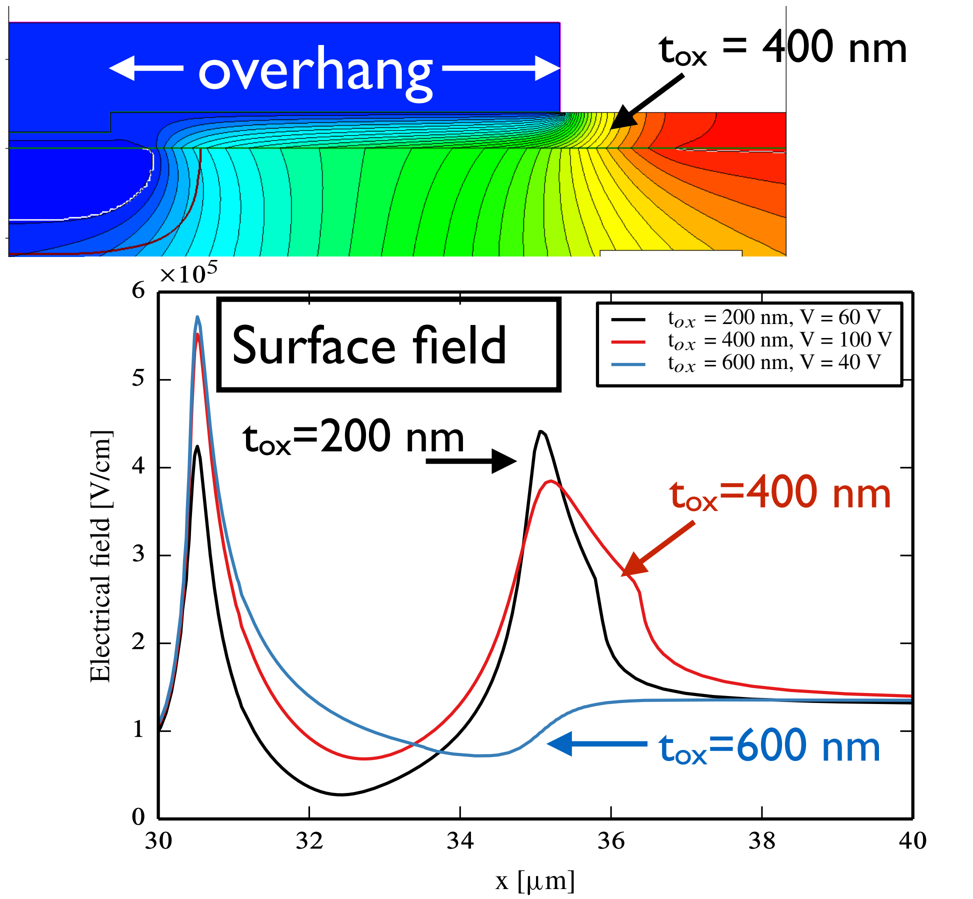


Surface-current densities peaks at 1- 10 MGy and then decrease
(max. value: J_{surf} = 1.5 - 6 $\mu A/cm^2$)

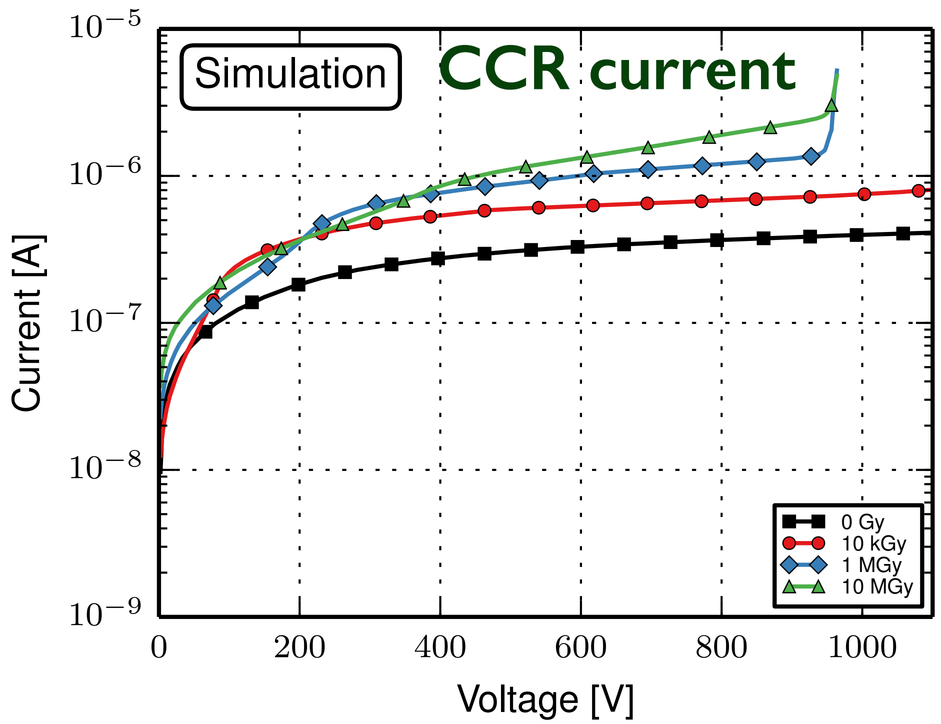
AGIPD guard ring optimization

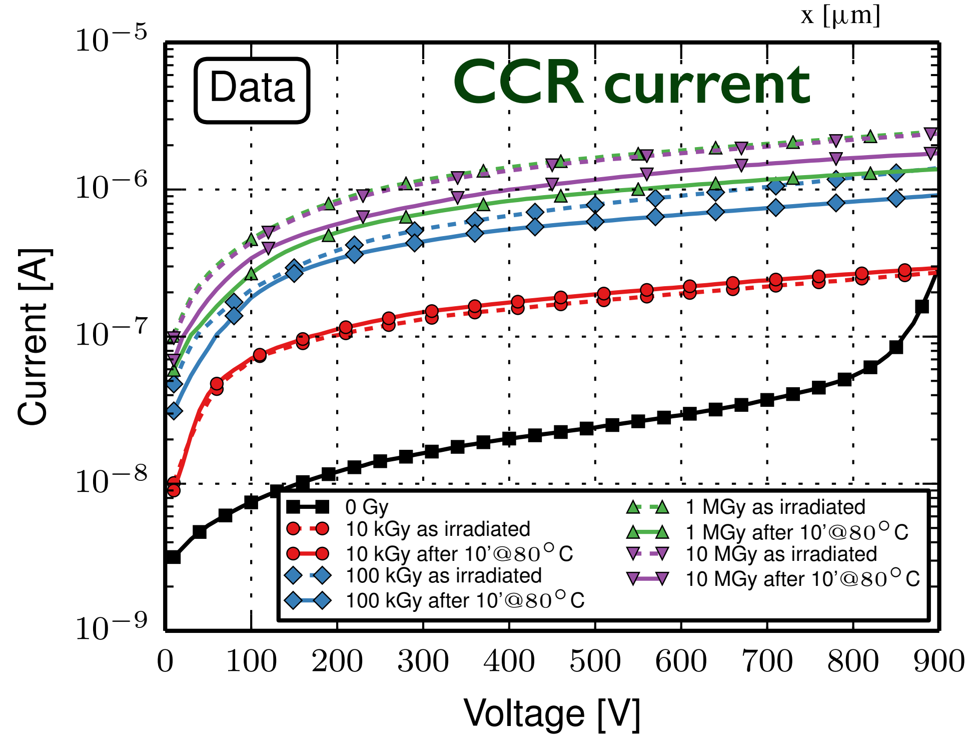
Strategy of guard-ring (GR) optimization:

- Use measured N_{ox} and s_0
- Study breakdown behavior of 0 GR (CCR only) as function of junction depth, oxide thickness and Al overhang
- Estimate number of floating GRs for 1000 V
- Vary spacing between rings, implant width and overhang to achieve maximum V_{bd}
  → max E-field between individual GRs the same
- Minimize space



Optimized GR design:



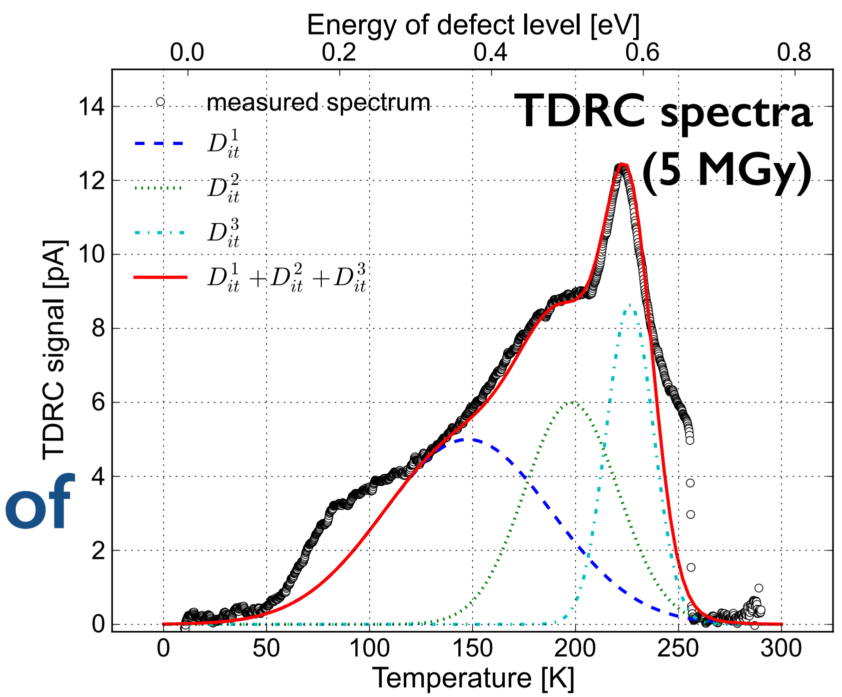


Radiation hard design needs dedicated optimization based on proper simulation

What is missing - under study

Accurate determination of D_{it} from MOS-C:

- Interface traps are continuously distr. in energy
- High-ohmic substrate + irradiation
  → Standard methods usually are not applicable
- TDRC spectra difficult to interpret
- Further investigations are required



So far assumed that N_{ox} and D_{it} are independent of E-field during irradiation and of biasing condition

➡ Crude approximation because

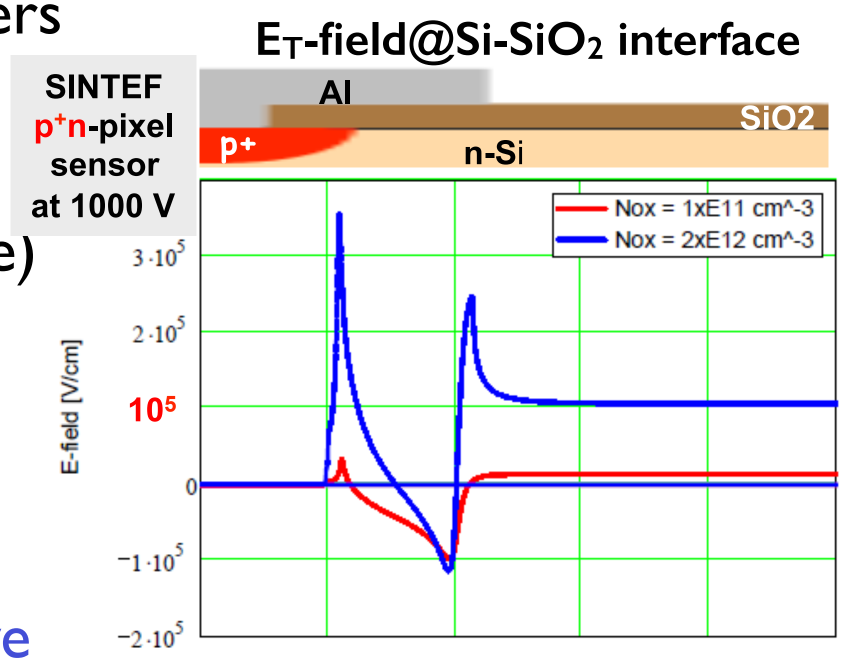
1) Build up of N_{ox} and interface traps depends on E-field
2) Charge of interface traps depends on surface potential
3) Surface currents depend on presence/absences of charge layers
4) Charging up of oxide during irradiation (ignored)

➡ Needed for understanding of sensor performance:

- Irradiation under bias (i.e. different E-fields at Si-SiO_2 interface)
- Determination of parameters during and shortly after irradiations under bias
- Annealing

Relevant E-field from simulation of sensors 3 nm from SiO_2

➡ local transverse fields up to ~300 kV/cm positive and/or negative



Problems in extracting parameters I

Parameters required for accurate simulations:

- Oxide charges density N_{ox} [cm^{-2}]
- Interface traps density distribution D_{it} [$(eV \cdot cm^2)^{-1}$], type (acceptor, donor, amphoteric), electron and hole cross sections
- Border traps: concentration, energy levels, type and cross sections,

Assumption which are made in the current analysis:

- All interface traps are acceptor → max. N_{ox}
- Interface traps communicate only with CB → no hole cross sections
- 1D electrical model of MOS-C
- TDRC (Thermal Dielectric Relaxation Current)
  - Bias MOS-C in e-accumulation at 0 V
    - ➡fill interface traps with electrons
  - Cool to ~10 K
    - ➡freeze electrons in traps
  - Bias to week inversion and heat up to 290 K

  $I_{TDRC}(T)$ → $D_{it}(E)$ assuming only emission no surface generation

➡ $N_{ox} = N_{it}$ for the range of the band gap corresponding to 0V and voltage used for heating up

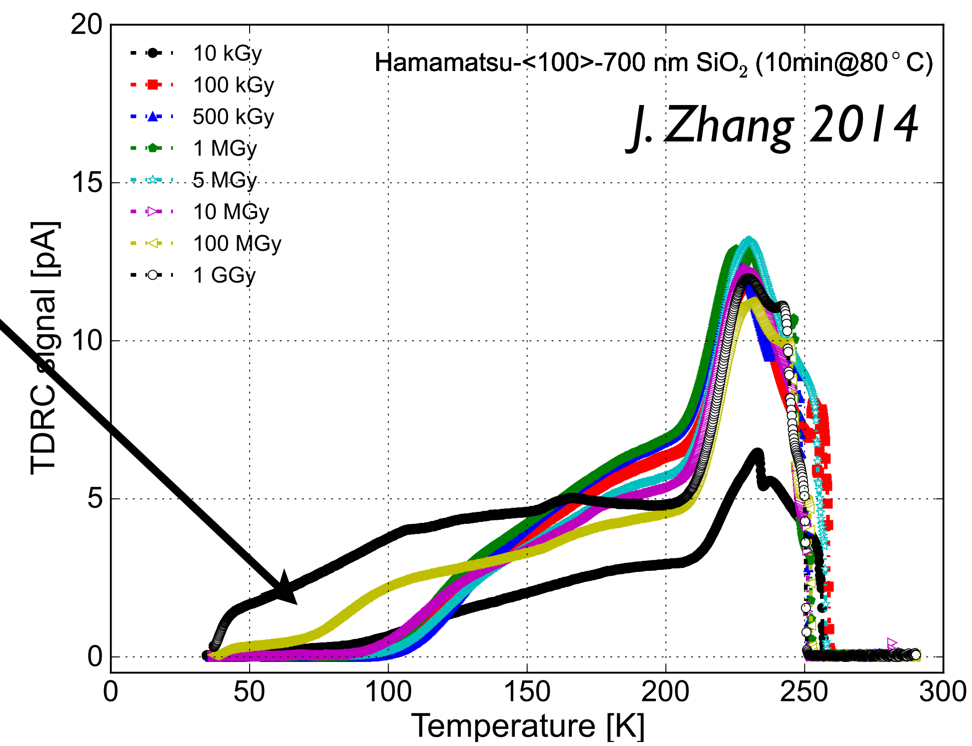
So far not achieved:

- Extract parameters from a MOS-C and reproduce the I-V curve of a GCD with TCAD

Problems in extracting parameters II

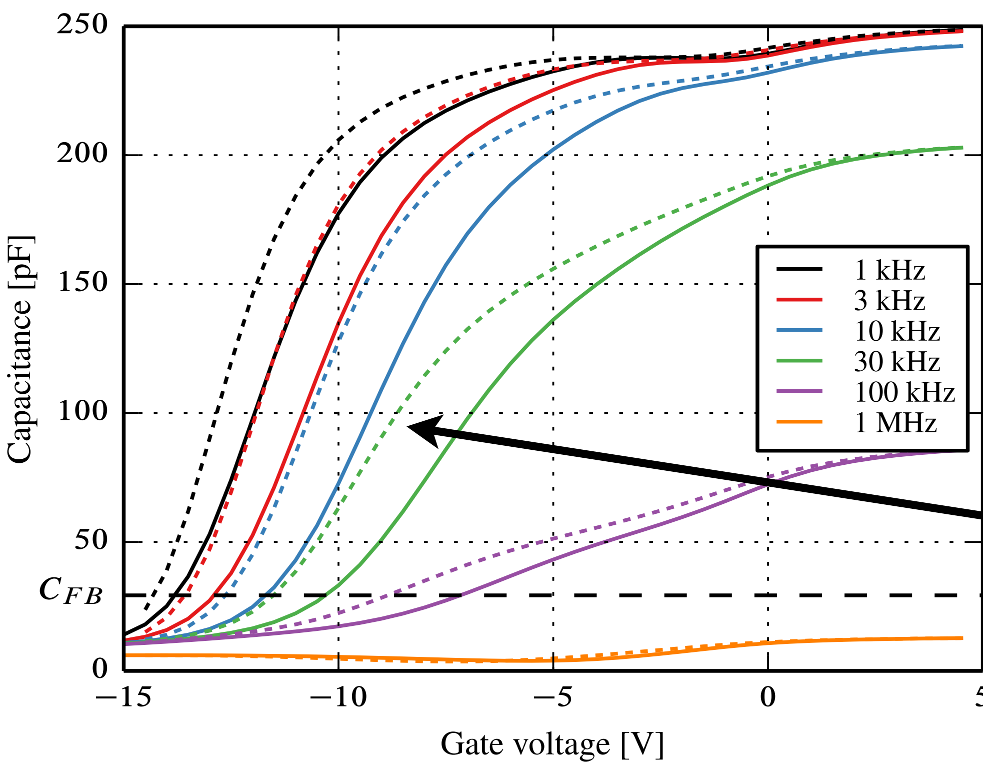
TDRC

- Not all interface traps filled by this method



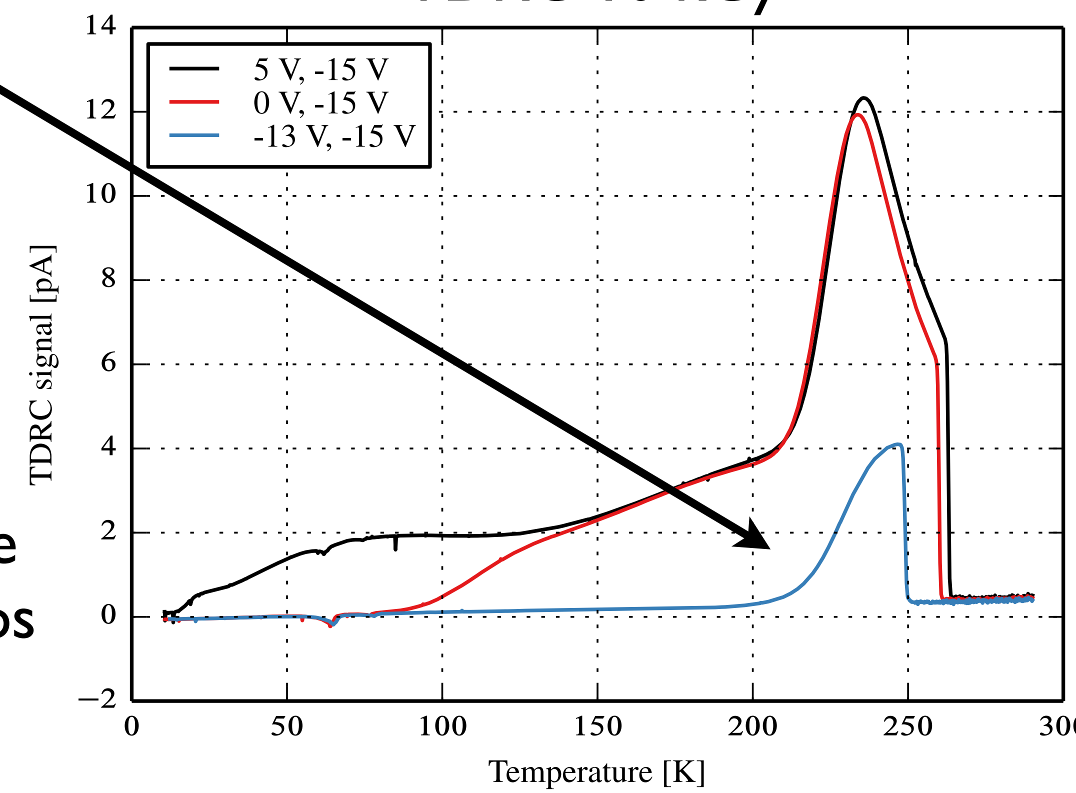
- Generation current contributes significantly to peaks above 200 K

CV 10 kGy



hysteresis due to border traps

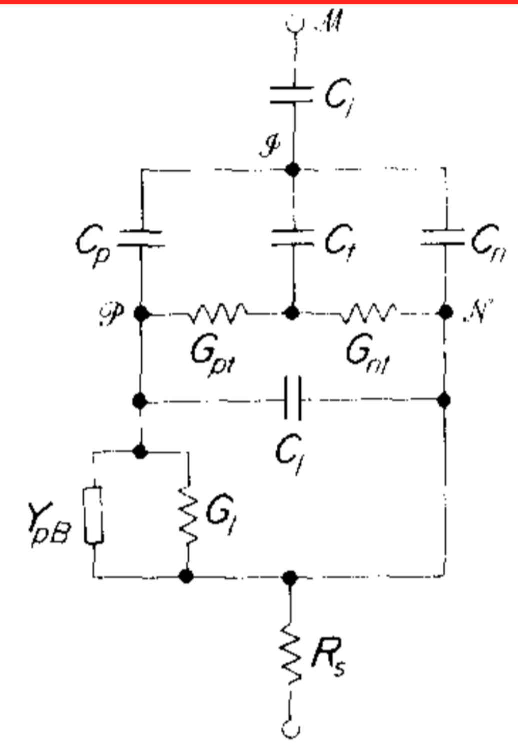
TDRC 10 kGy



Problems in extracting parameters III

Latest attempt:

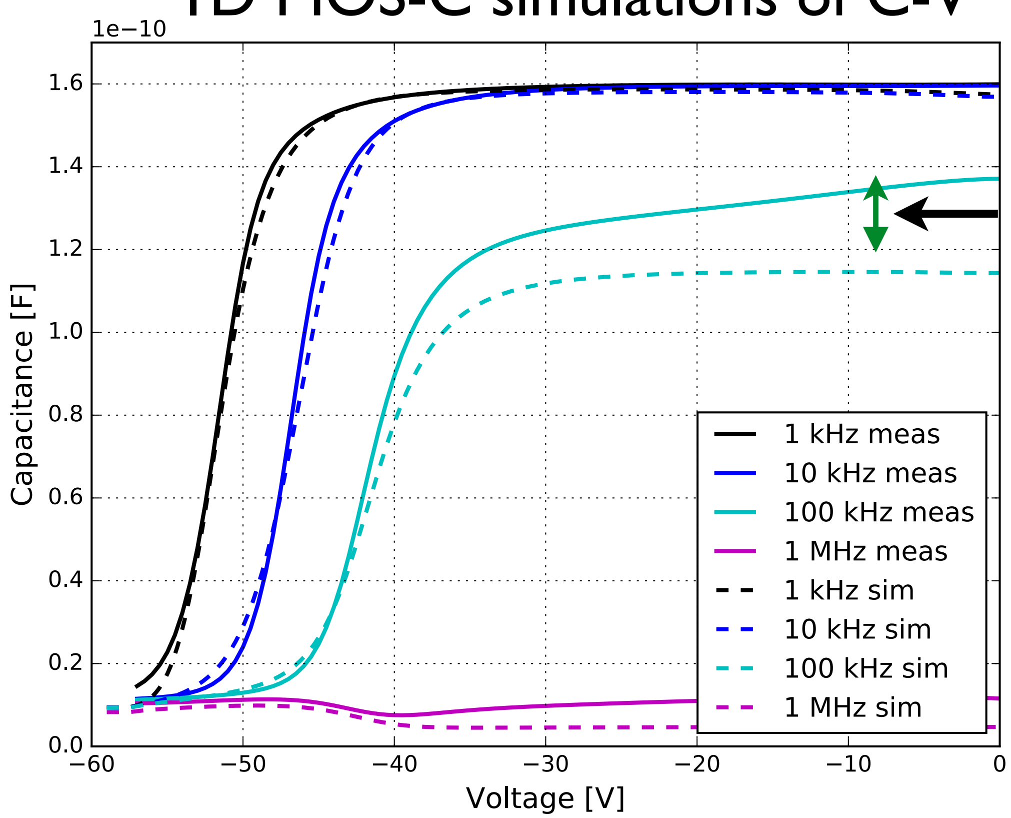
- CiS: <111> 5 MGy, 10min at 80°C
- circular MOS-C and GCD
- New 1D electrical model taking CB and VB into account
- 3 acceptor + 1 donor gaussian distributed traps
- Simultaneous fit of C/G-V and TDRC (up to 200 K)



R. S. Nakhmanson, Solid-State Electronics 1976

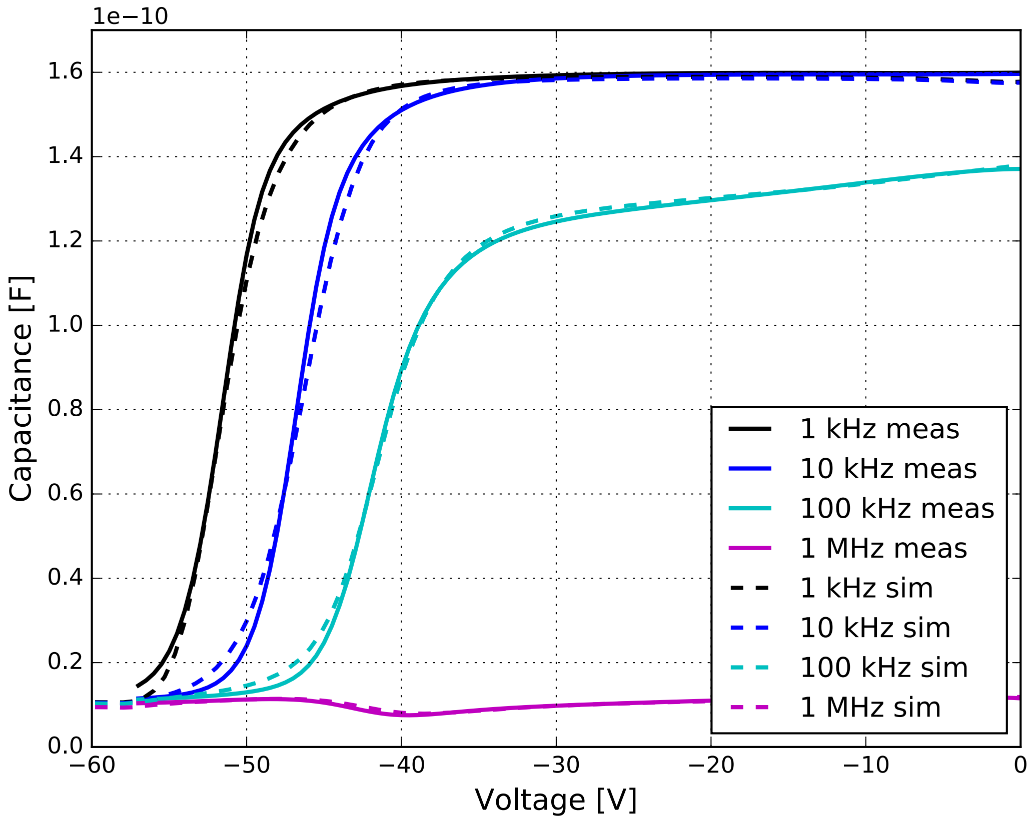
Check of extracted parameters with 1D and 3D TCAD simulations:

1D MOS-C simulations of C-V



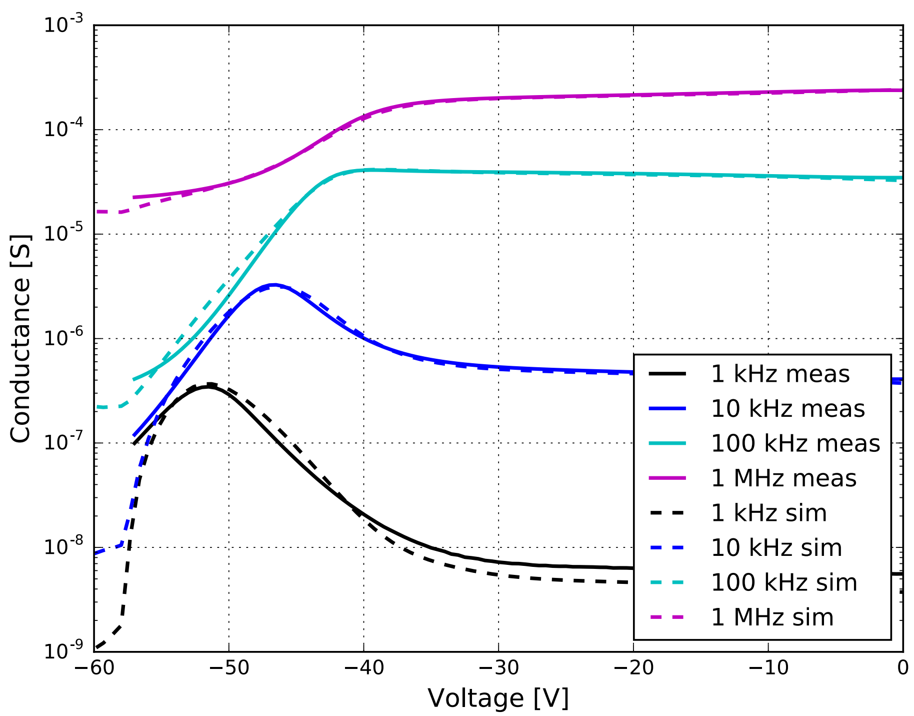
series resistance effects difficult to model in 1D

3D MOS-C simulation of C-V

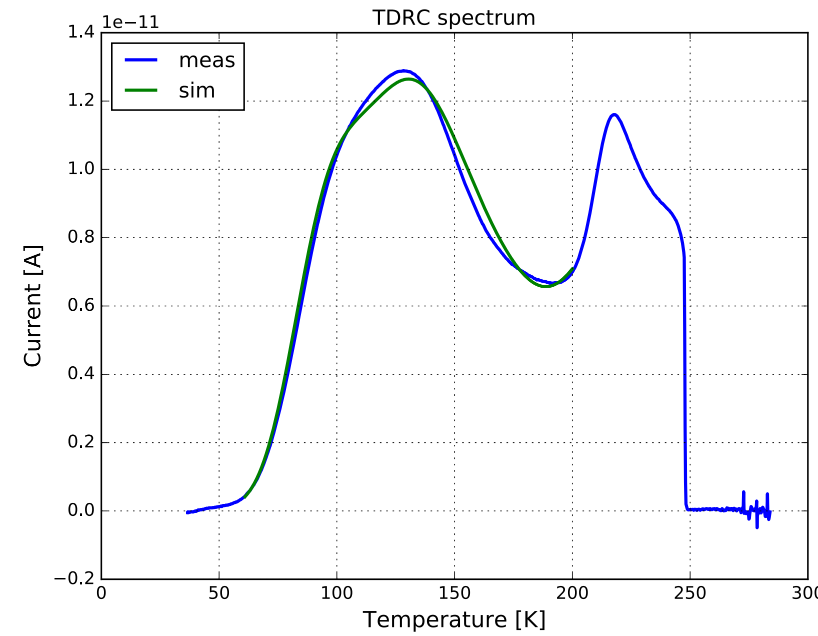


Problems in extracting parameters IV

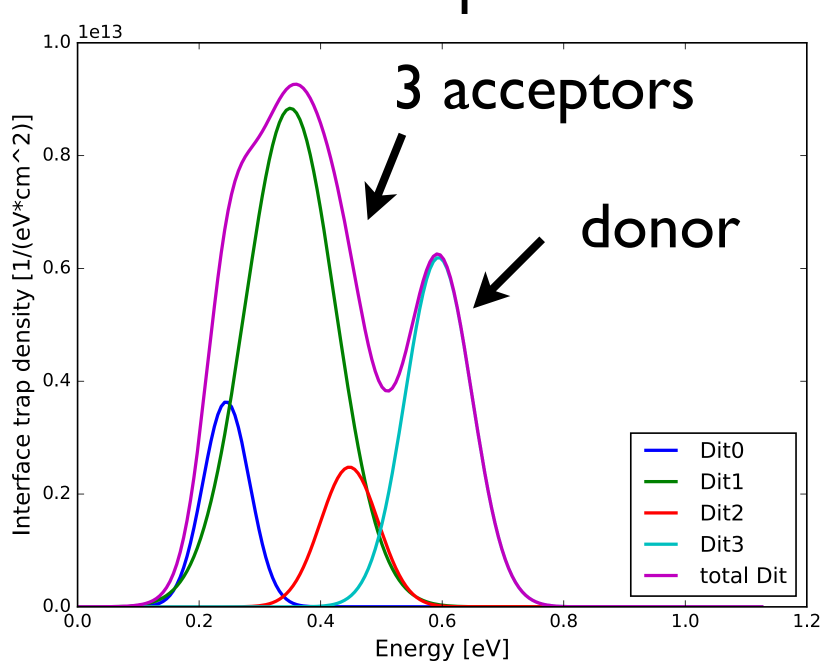
3D MOS-C simulation of G-V



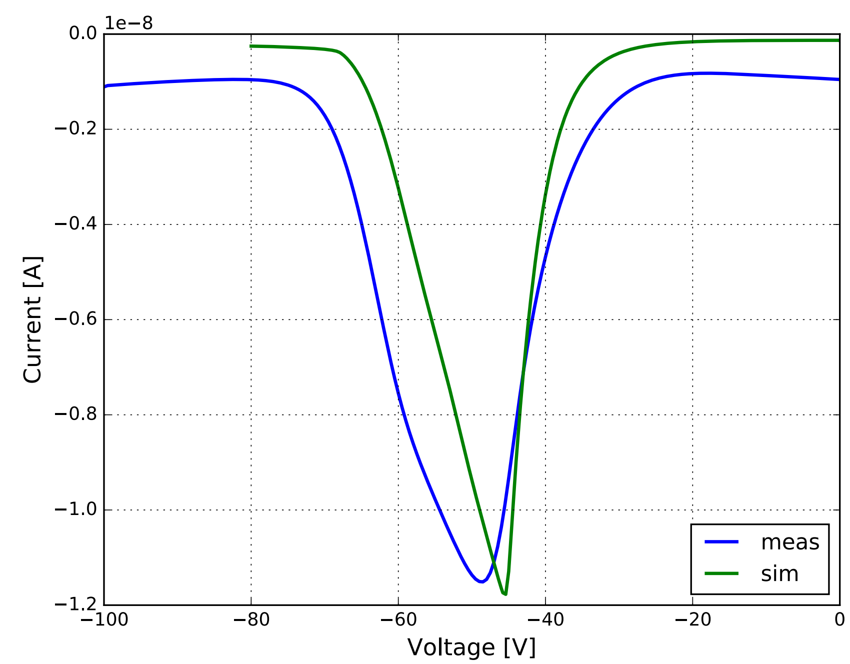
TDRC spectrum



Interface trap distribution



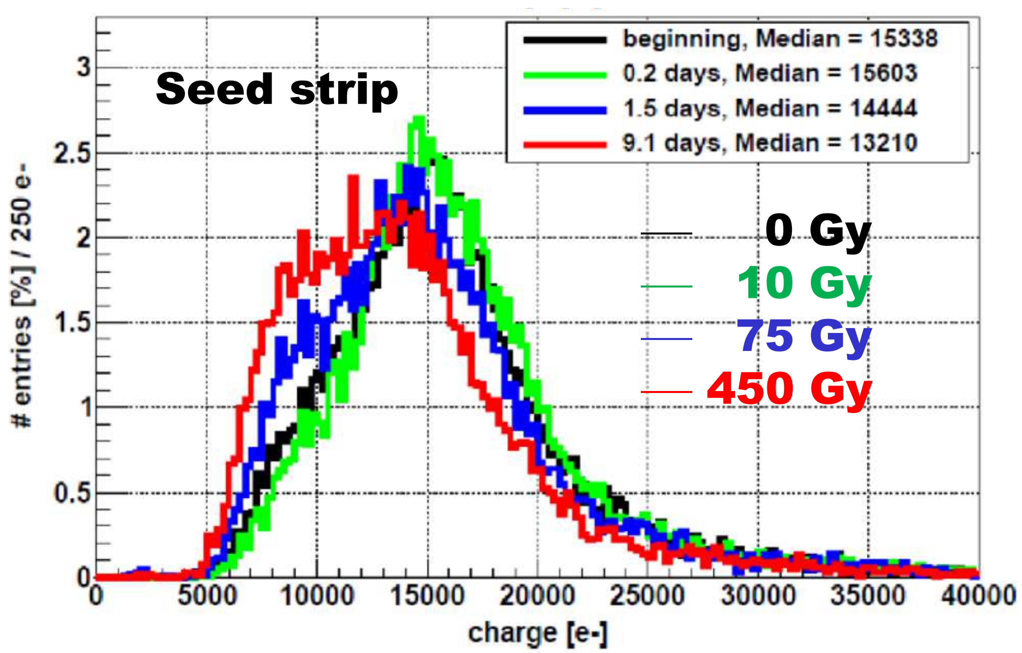
3D GCD simulation of I-V

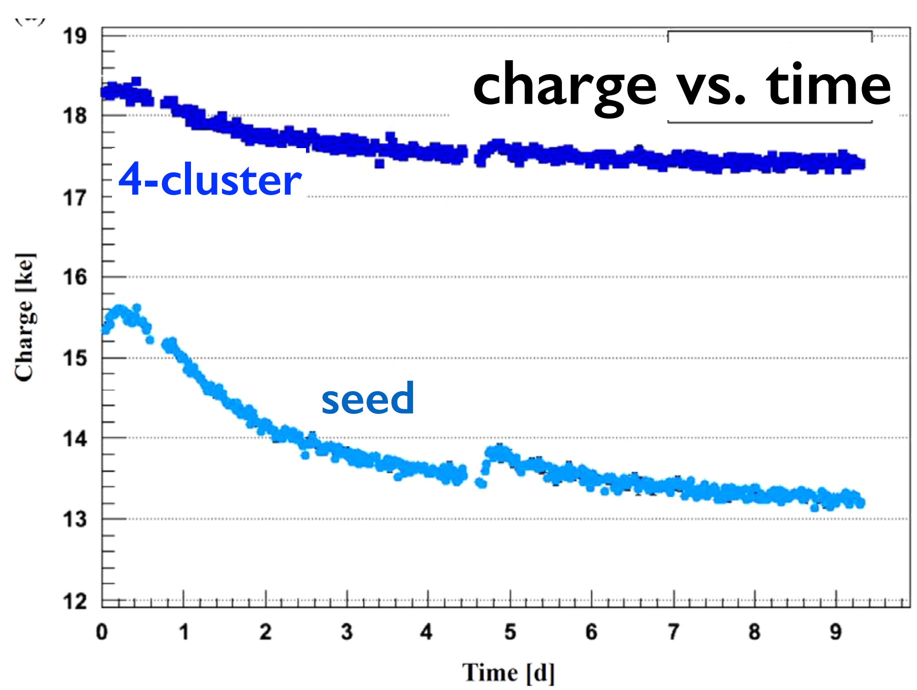


Up to now best fit of GCD current

Boundary conditions + humidity effects

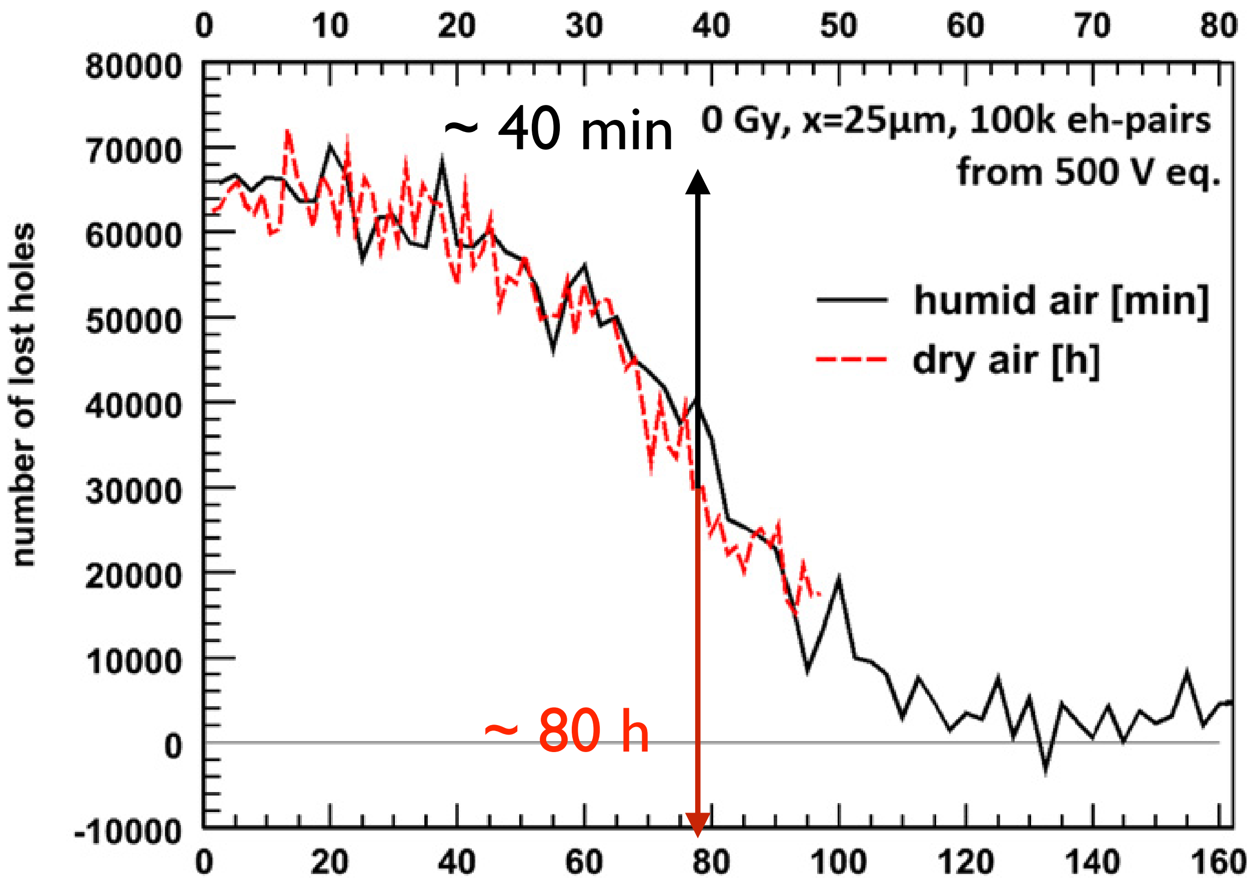
- **Charge collection** of a HPK n^+-p strip sensor is **sensitive** to doses of a few tens of Gy from a ^{90}Sr β-source





CMS Tracker, NIMA 2015

- **Hole losses** vs. time after changing bias voltage from 500 V to 200 V of a HPK p^+-n strip sensor (670nm laser)



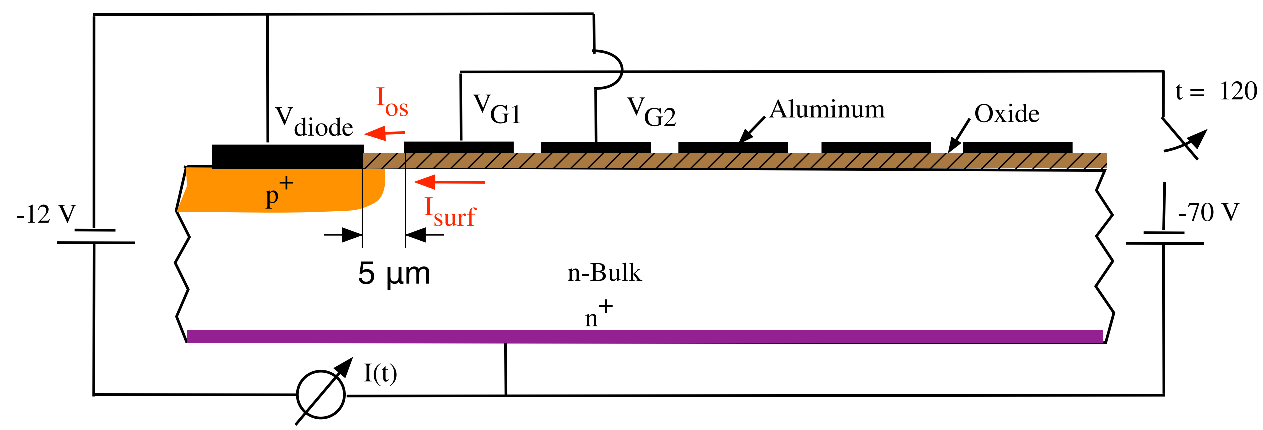
Steps for understanding

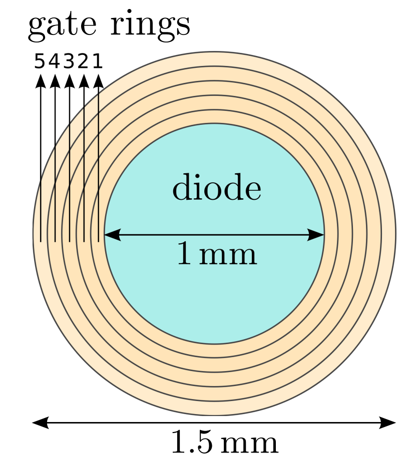
- Measurement of R_{sq} for different humidities using test structures
- TCAD simulation with proper implementation of boundary conditions

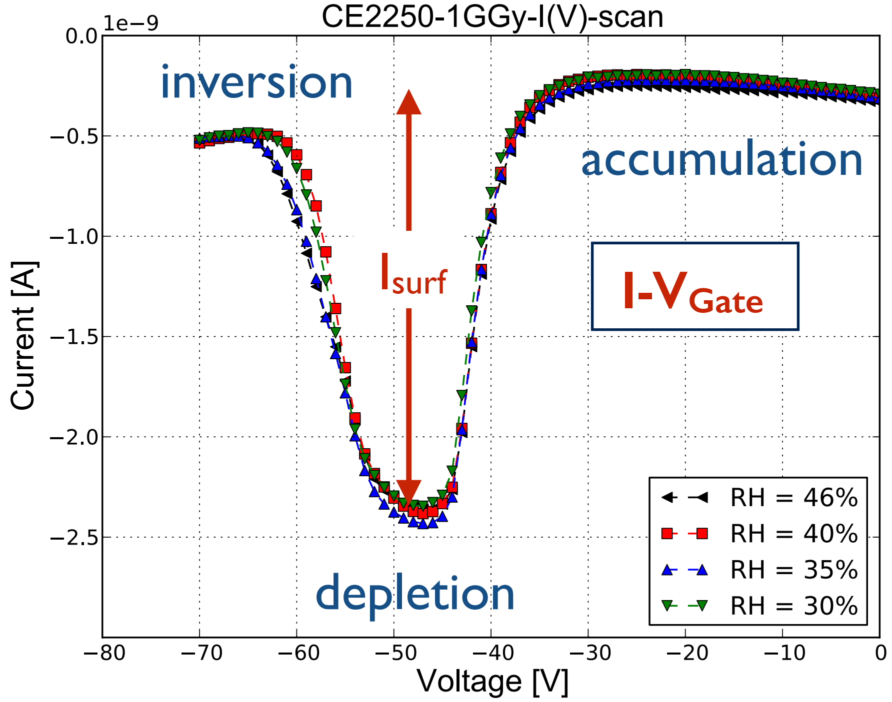
Outer surface charges and resistivity R_{sq}

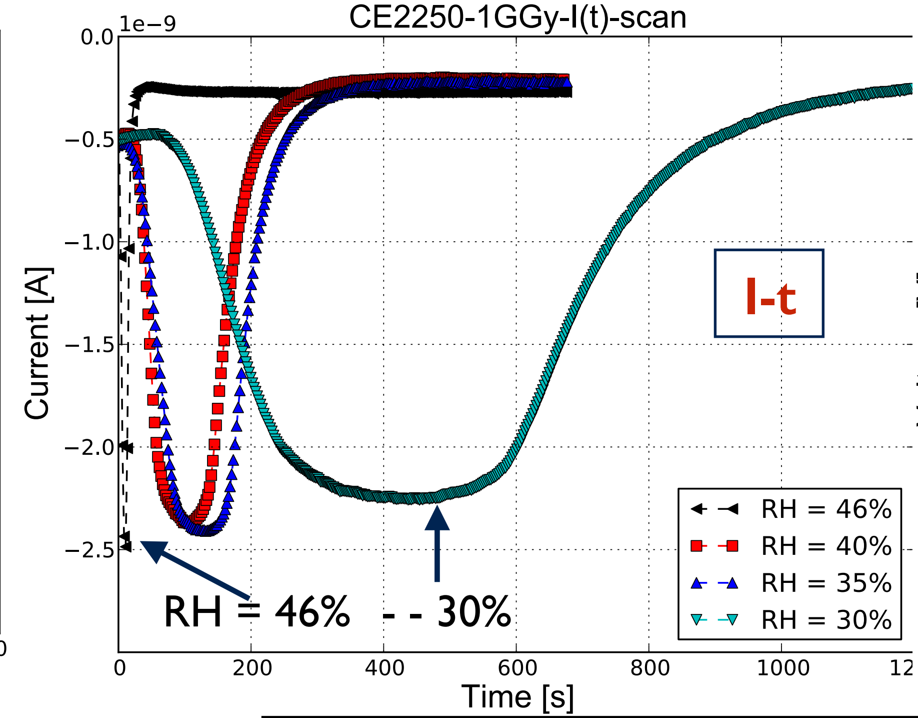
Measurement of $R_\square$ over 5 µm SiO_2 by Si-SiO_2 interface current in GCD

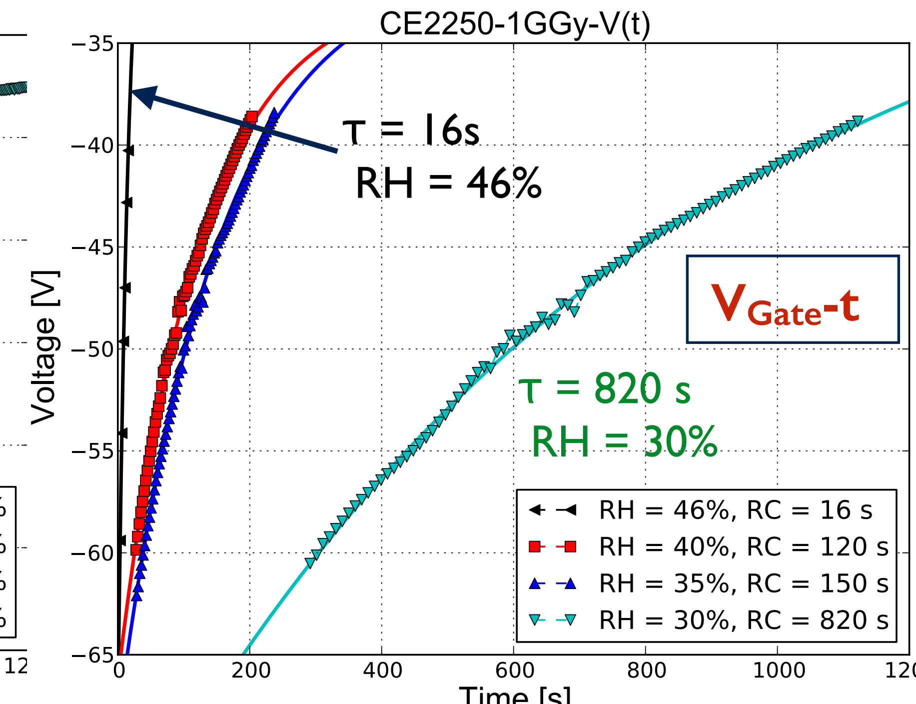
Biasing scheme for GCD (Gate Controlled Diode)











Relative humidity *RH* [%]	30	35	40	46
Discharge time [s]	820	150	120	16
R_{surf} [10^{12} Ω]	50	9.1	7.3	0.97
R_{sq} [10^{15} Ω]	66	12	9.7	1.3

Time constants change by factor 50 for RH 46% → 30%

R_{sq} ~10^{17}Ω @ RH = 30%

(difficult to measure for lower RH!)

N.B. Measurement of R_{sq} with MOSFET Floating Gate Technique are also possible

Outer surface charges and resistivity R_{sq}

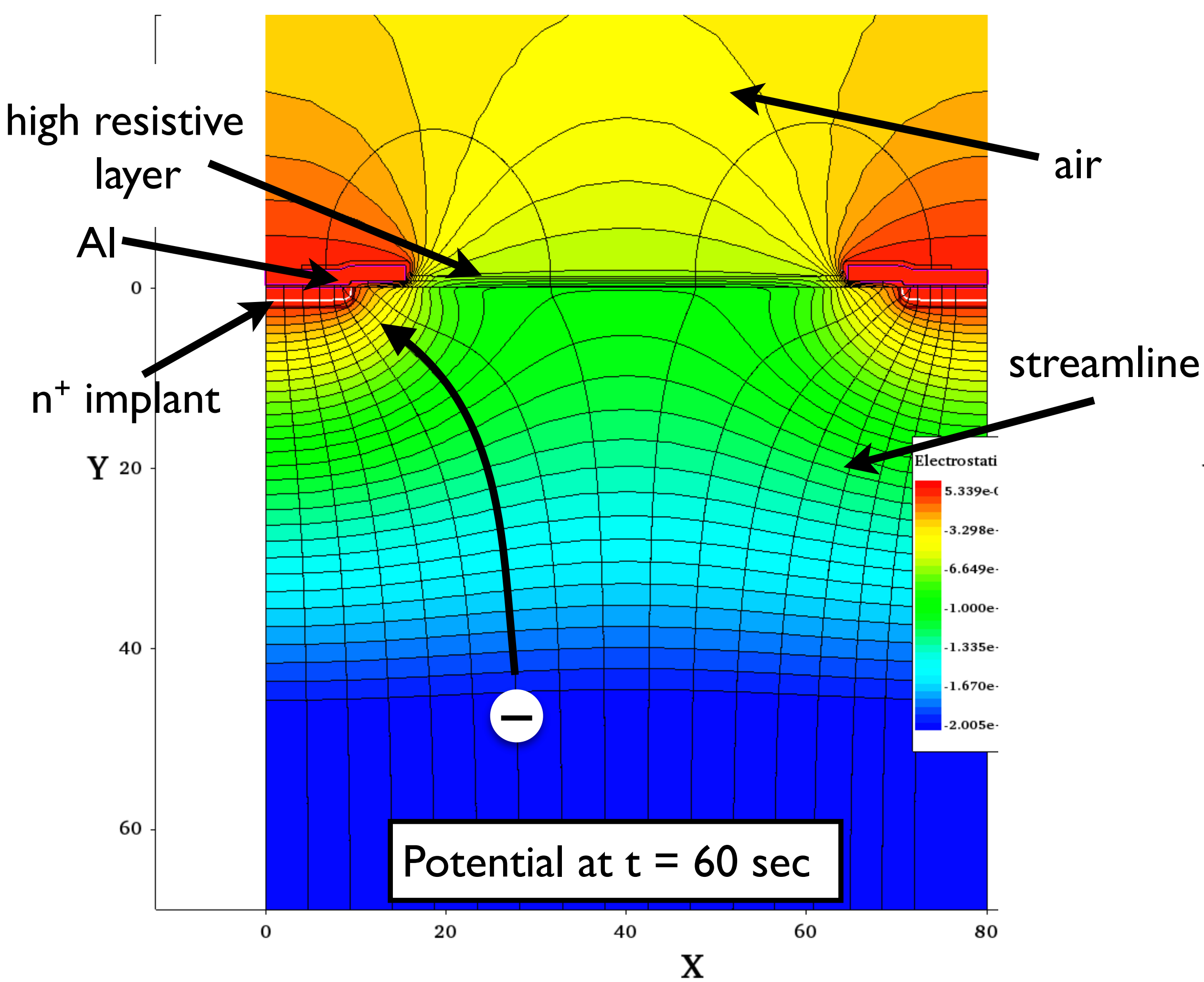
Explanation of long-term changes (w.o. radiation damage)*):

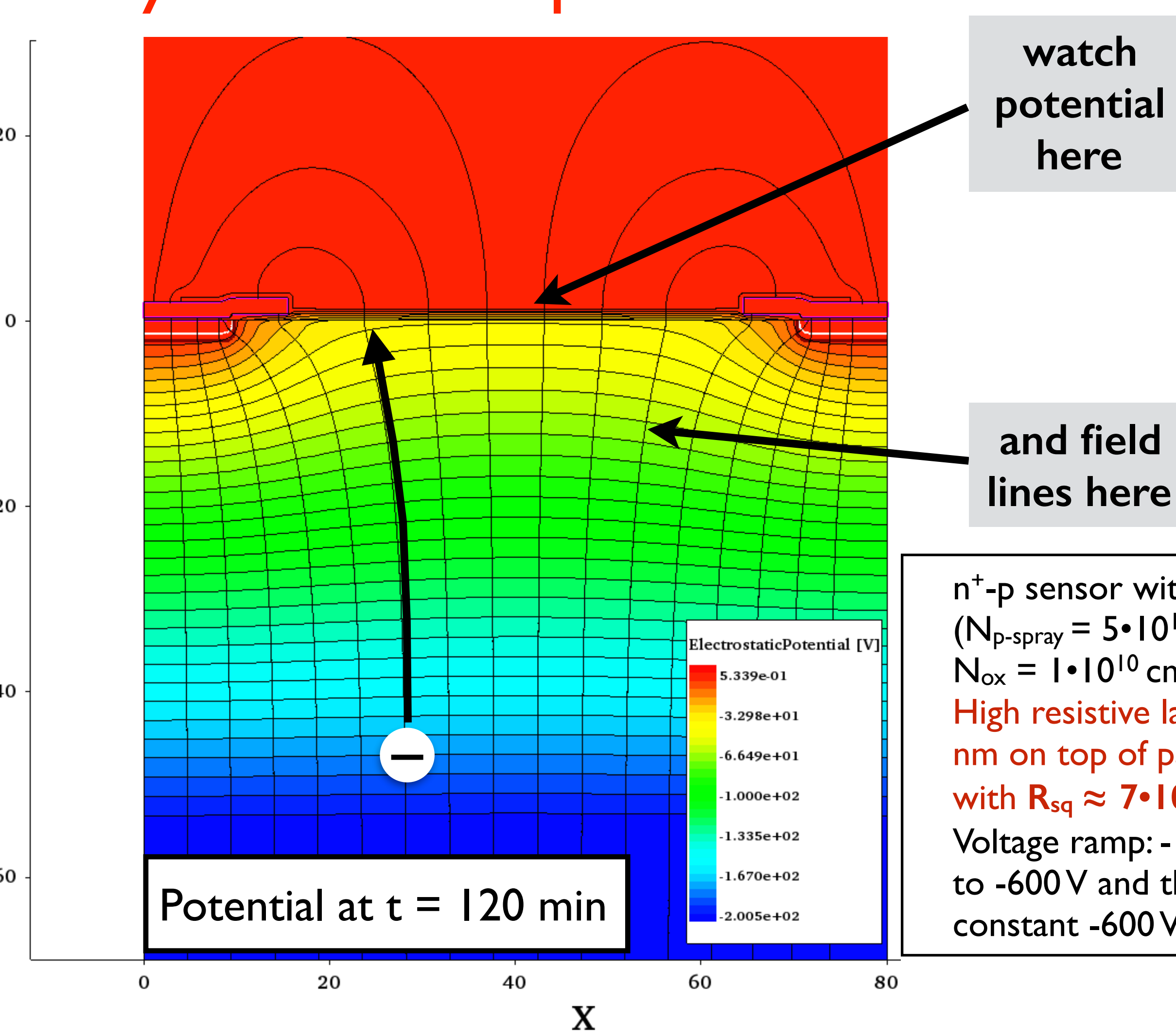
Biasing → longitudinal E-field component on o.s. → rearrangement of Q_{os} until E_{long} = 0 and V_{os} = const → time constant depends R_{sq}, which changes by many orders of magnitude with humidity (and T)

*) already discussed by A.Longoni et al., NIM-A288(1990)35

Simulation of boundary conditions:

- Outer surface layer with high resistivity for time dependence





n^+-p sensor with p-spray ($N_{p\text{-spray}} = 5 \cdot 10^{11}$ cm^{-2}), $N_{ox} = 1 \cdot 10^{10}$ cm^{-2}
High resistive layer of 10 nm on top of passivation with $R_{sq} \approx 7 \cdot 10^{14}\ \Omega$
Voltage ramp: -10 V/s up to -600 V and than constant -600 V

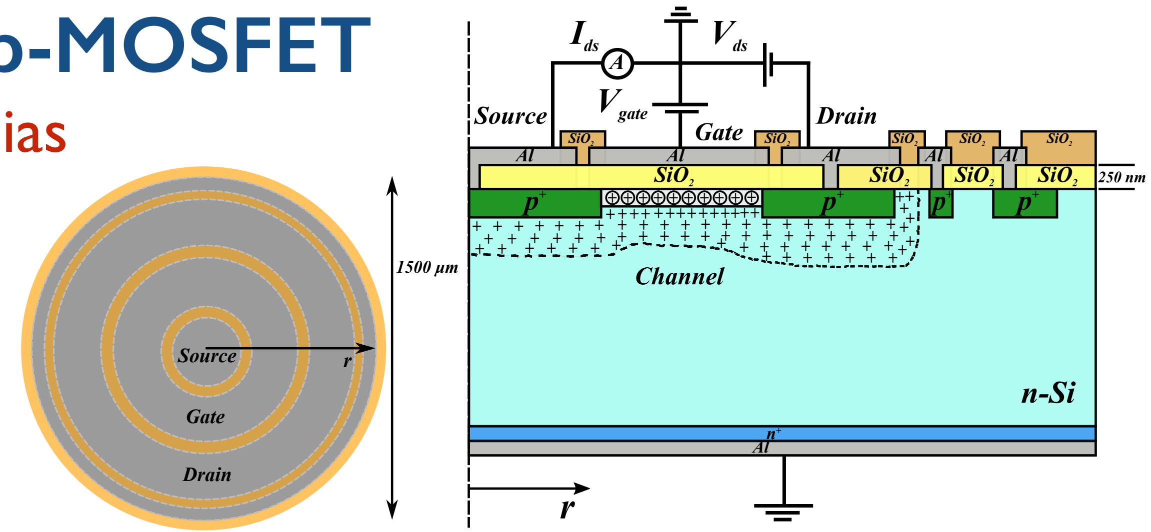
Changes of potential leads to different charge collection

Field-enhanced N_{ox}^{eff} before/during/after irradiation

Experimental investigation using n- and p-MOSFET

➡ Allows to study of oxide **during** irradiation with bias

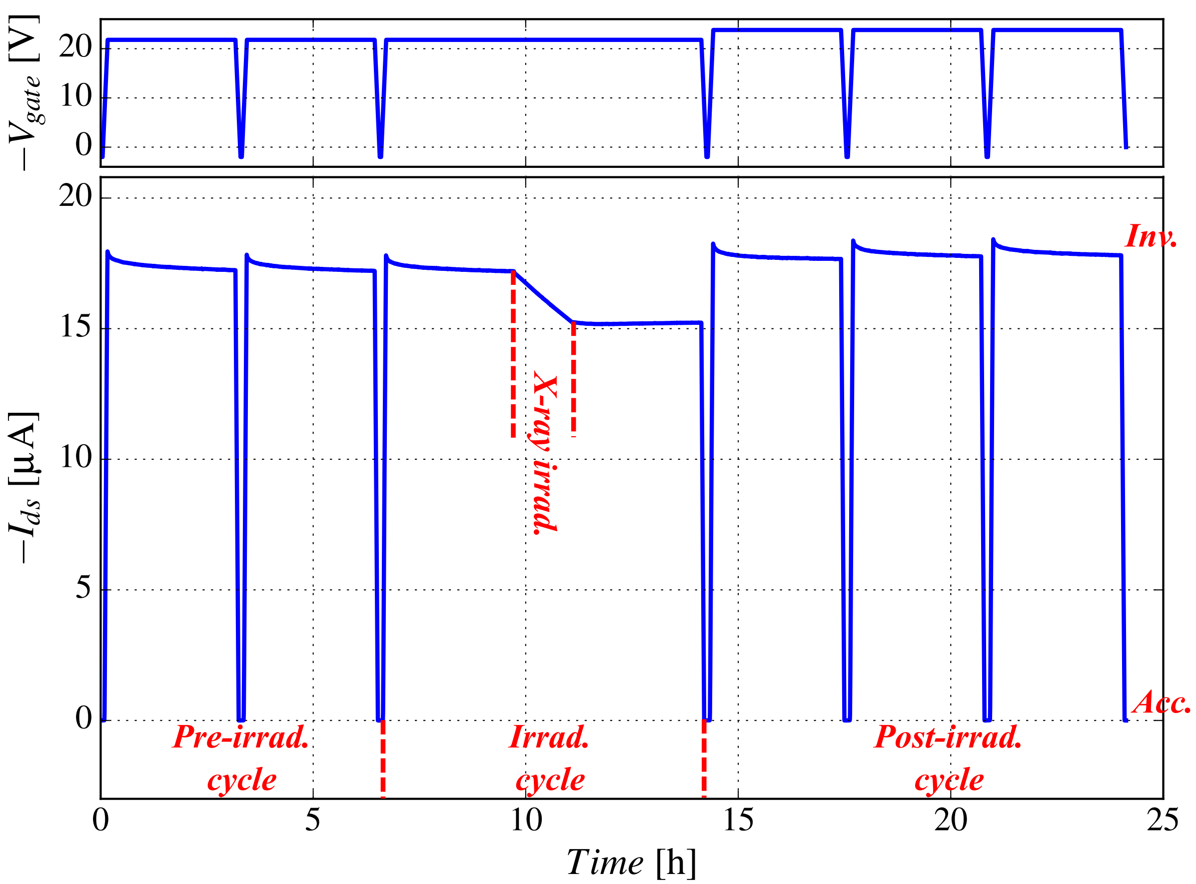
$$I_{ds} = \frac{\mu_0^p}{1 + (V_{gate} - V_{th})/V_{1/2}} C_{ox} \frac{W}{L} V_{ds}(V_{gate} - V_{th})$$

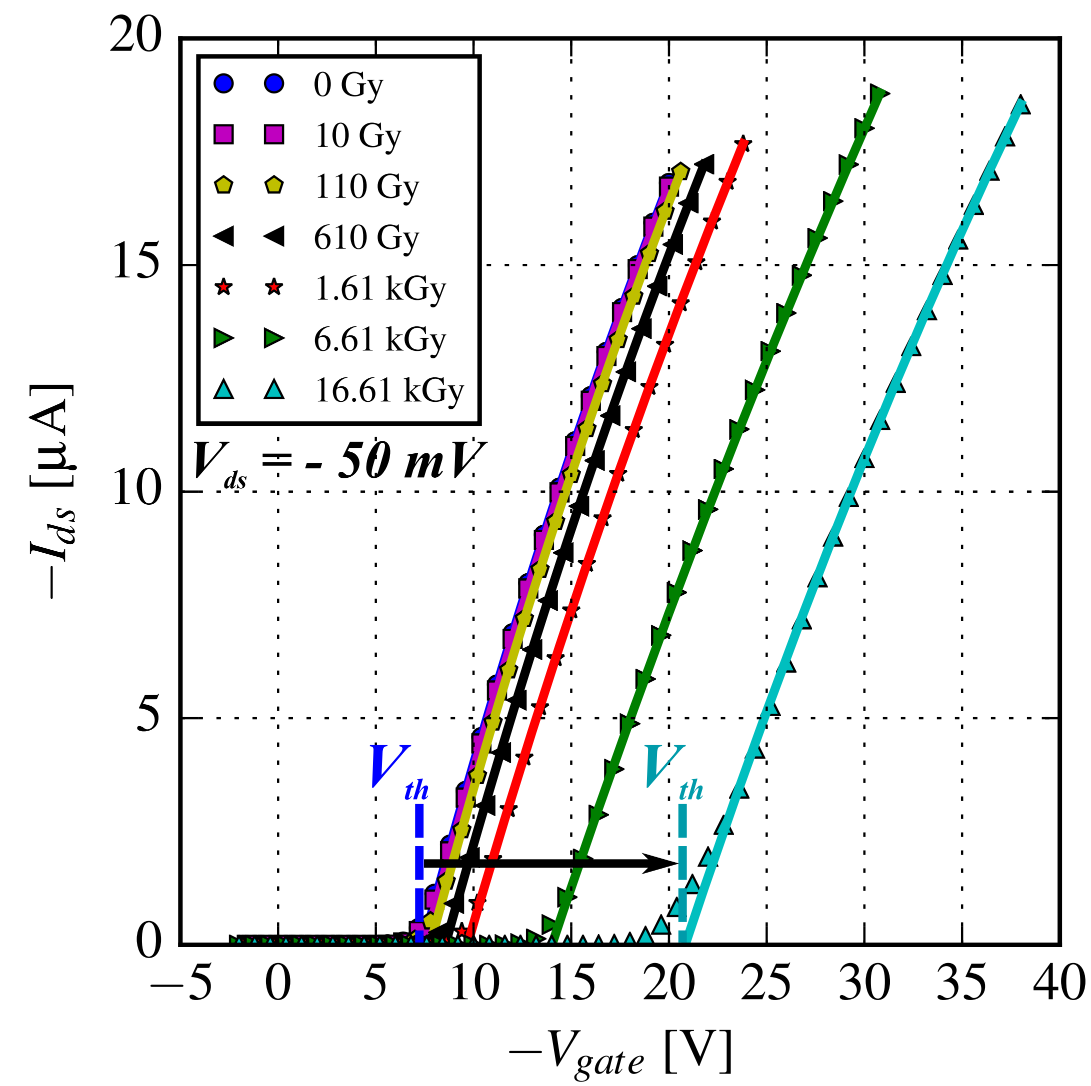


1. Calibrate $I_{ds}(V_{gate})$ for const. V_{ds}
2. Fix V_{gate} and measure $I_{DS}(t)$ and calculate $V_{th}(t)$
3. Calculate $N_{ox}(t)$ from $V_{th}(t)$

Difficulty: $\mu_0(N_{ox})$ and $V_{1/2}(N_{ox})$

(→ several calibrations before/after irr.)



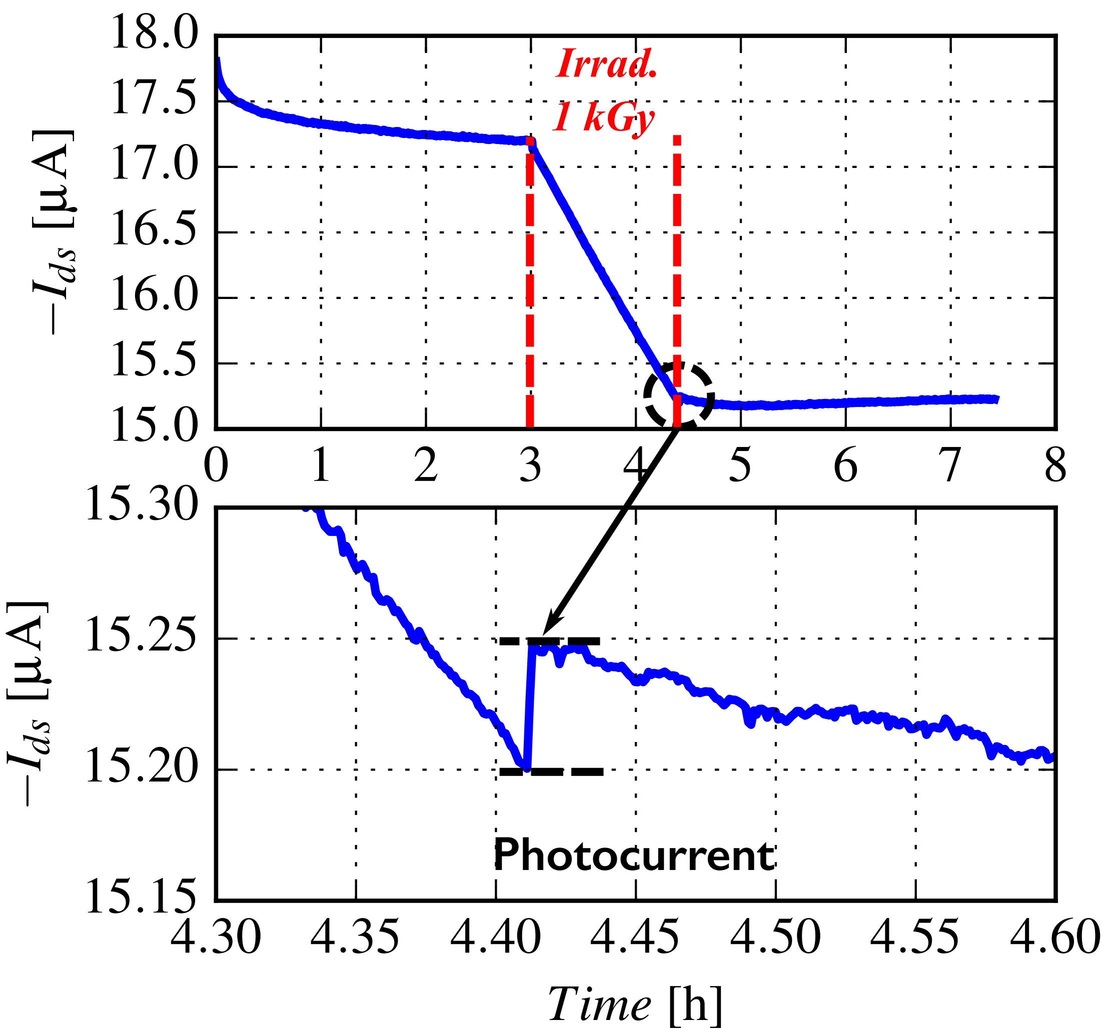


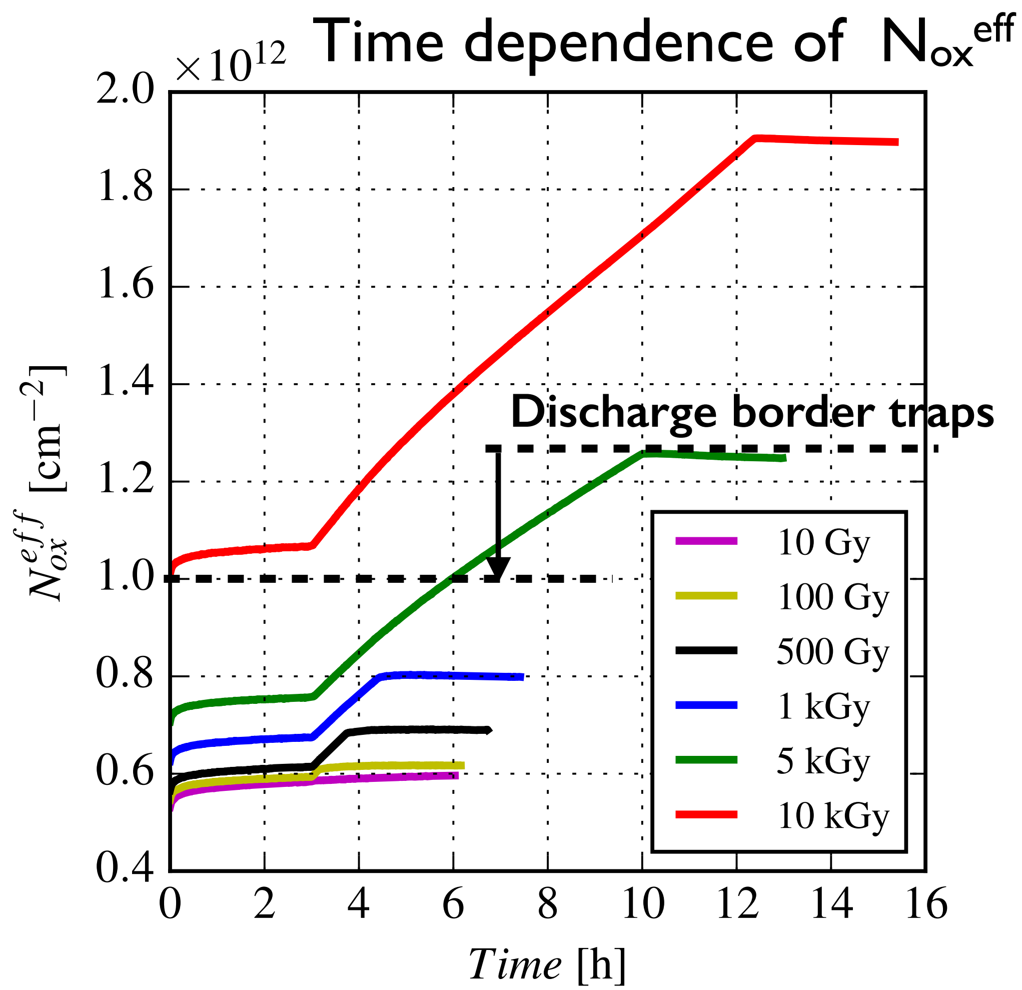
Field-enhanced N_{ox}^{eff} before/during/after irradiation

X-ray irradiations of p-MOSFET:

- ΔDose = 10, 100, 500 Gy, 1, 5, 10 kGy
- MOSFET Canberra 250 nm SiO_2, <111>, n-type $6 \cdot 10^{11} cm^{-3}$

Example: ΔDose=1 kGy irradiation for E ≈ 500 kV/cm





- E-field does not cause anomalous short-term effects during or after irradiation
- observe charging and „de-charging“ of border traps
- attempts to determine D_{it} using sub-threshold current (complementary to MOS-C)

➡ data available to put into simulation and study effects on sensors

Summary

1. Proper simulations of surface effects are complex and require a systematic approach
   - Parameter extraction on test structures
   - Simulate sensors + optimize
   - Verify simulations by measurements
2. Some methods for determination of parameters relevant for simulation of surface effects have been established, some data are available and further data are acquired
3. Methods of implementing proper boundary conditions and implementation of surface effects in TCAD have been established
4. Successful optimization + understanding of surprises have been demonstrated
5. Upper and lower limits on surface-damage parameters are available, to study the combined effects of bulk and surface damage of sensors.
6. Consistency of parameter extraction by TCAD simulation of test structures has not (yet?) been achieved → essential check!

Proposed next steps for bulk-surface damage studies

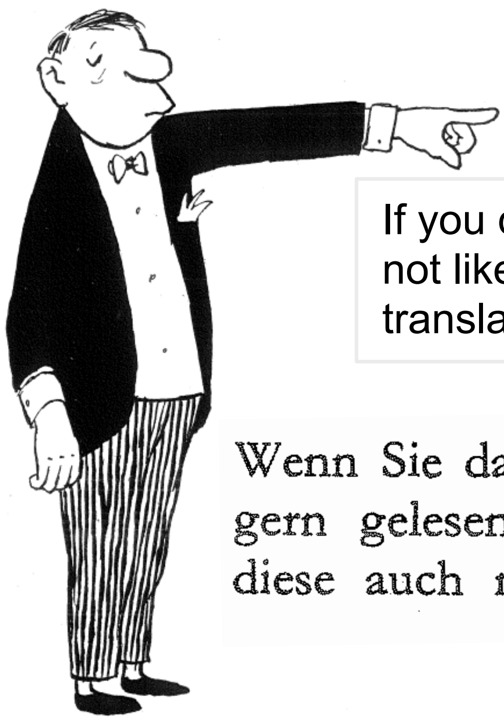
1. Impact of surface-damage effects on sensors with/without bulk damage using TCAD simulations
   a. Agree on which model(s) to use for bulk damage.
   b. Agree on data (from sensors + test structures) and methods for comparison with simulation results.
   c. Agree on „surface boundary conditions“ (> 1 needed!)
   d. Agree on parameter and their extreme values as function of dose for TCAD simulations.

   N.B. An iterative approach for the E-field dependence will be required
   (e. g. assume $N_{ox}(E=0)$ → calculate E(x) at Si-SiO_2 interface → $N_{ox}(E(x)$ etc.)
2. Check methods of surface damage parameters extraction using TCAD simulations (extraction of N_{ox}, D_{it}, border traps + their interaction with Si bulk)
3. New irradiation and measurements of test structures and sensors. In my view should wait until agreement on 1) is reached and it is know, who does what.

My (our) expectations:

i. Surface damage is too complicated to be understood in all details.
ii. Estimation using extreme parameter values should be sufficient.
iii. Surface damage important for guard rings and strip sensors (less for pixels).

References to work from UHH-Group



If you did not like this talk, you will also not like the following publications (free translation from V. von Bülow "Loriot")

Wenn Sie das vorliegende Buch ungern gelesen haben, werden Ihnen diese auch nicht so recht gefallen.

V. von Bülow "Loriot"

Low-dose effects in segmented Si sensors:

C. Henkel, Impact of low dose-rate electron irradiation on the charge collection of n$^+$p silicon strip sensors, BSC thesis, University of Hamburg, March 2014, unpublished

J. Erfle, Irradiation study of different silicon materials for the CMS tracker upgrade, PhD thesis, University of Hamburg, DESY-THESIS-2014-010

R. Klanner et al., Impact of low-dose electron irradiation on n+p silicon strip sensors, POS (TIPP 2014) , detailed paper in preparation

Surface resistivity and border traps:

J. Schwandt et al., Investigation of the insulator layers for segmented silicon sensors before and after X-ray irradiation, Talk presented at the IEEE Nuclear Science Symposium, Seattle 8-15. Nov, 2014

D. Brueske, Investigation of the field dependence of the injection of positive charges into the SiO2 at the Si-SiO2 interface, BSC thesis, University of Hamburg, 2014, unpublished

Charge trapping at the Si-SiO_2 interface - humidity:

T. Poehlsen et al., Study of the accumulation layer and charge losses at the Si–SiO2 interface in p+n-silicon strip sensors, NIM-A 721 (2013) 26; doi: 10.1016/j.nima.2013.04.026

T. Poehlsen et al., Time dependence of charge losses at the Si-SiO2 interface in p+n-silicon strip sensors, NIM-A 731 (2013) 172; doi: 10.1016/j.nima.2013.03.035

T. Poehlsen, Charge Losses in Silicon Sensors and Electric-Field Studies at the Si-SiO2 Interface, PhD thesis, University of Hamburg, DESY-THESIS-2013-025

X-ray radiation damage:

J. Zhang et al., Study of radiation damage induced by 12 keV X-rays in MOS structures built on high-resistivity n-type silicon, J. Synchrotron Rad. 19 (2012) 340; doi: 10.1107/S0909049512002384

R. Klanner et al., Study of high-dose X-ray radiation damage of silicon sensors, NIM-A; 732 (2013) 117, doi: 10.1016/j.numa.2013.05.131

J. Zhang et al., X-ray induced radiation damage in segmented p+n silicon sensors, PoS (Vertex 2012) 019

J. Zhang, X-ray Radiation Damage Studies and Design of a Silicon Pixel Sensor for Science at the XFEL, PhD thesis, University of Hamburg, DESY-THESIS-2013-018 (2013)

Sensor optimization for high X-ray doses:

J. Schwandt et al., Optimization of the radiation hardness of silicon pixel sensors for high x-ray doses using TCAD simulations, 2012 JINST 7 C01006; doi: 10.1088/1748-0221/7/01/C01006

J. Schwandt et al., Design of the AGIPD sensor for the European XFEL, 2013 JINST 8 C01015; doi: 10.1088/1748-0221/8/01/C01015

J. Schwandt et al., Design and First Tests of a Radiation-Hard Pixel Sensor for the European X-Ray Free-Electron Laser, IEEE TNS, doi: 10.1109/RADECS.2013.6937446 and arXiv-140213

J. Schwandt , Design of a radiation hard pixels sensor for X-ray science, PhD thesis, University of Hamburg, DESY-THESIS-2014-029

Backup

Oxide charges N_{ox} + interface traps D_{it}

C/G-V+TDRC for MOS-C (from 4 vendors, <100> and <111>, surface damage by X-rays (0 - 1 GGy), no E-field during irradiation, annealing)

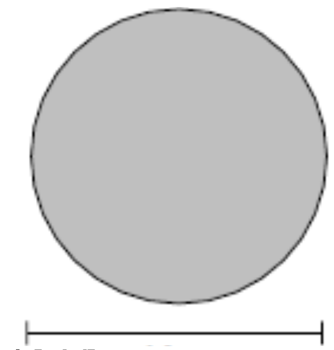
How to obtain reproducible results ?
(1) Annealing at 80°C for 10 min
(2) Stop voltage scan before strong in-version → no injection of border traps

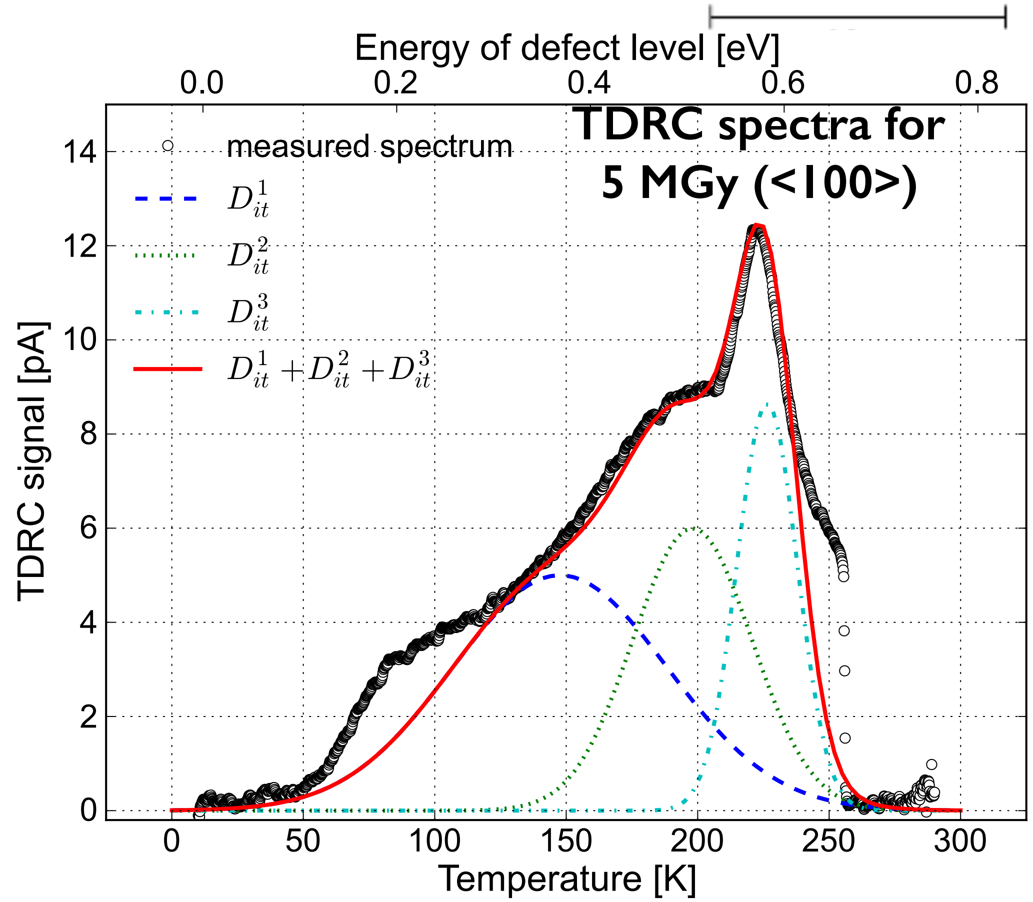
- TDRC: Properties of interface traps
  (Thermal Dielectric Relaxation Current)
  - Bias MOS-C in e-accumulation
    ➡fill interface traps with electrons
  - Cool to ~10 K
    ➡freeze e in traps
  - Bias to inversion and heat up to 290 K

➡I_{TDRC} due to release of trapped e's
$I_{TDRC}(T)$ → $D_{it}(E)$ *)
➡(Energy levels + widths + densities)$_{it}$

*) Temperature T → $E_c - E_{it}$ (T dependence of Fermi level)

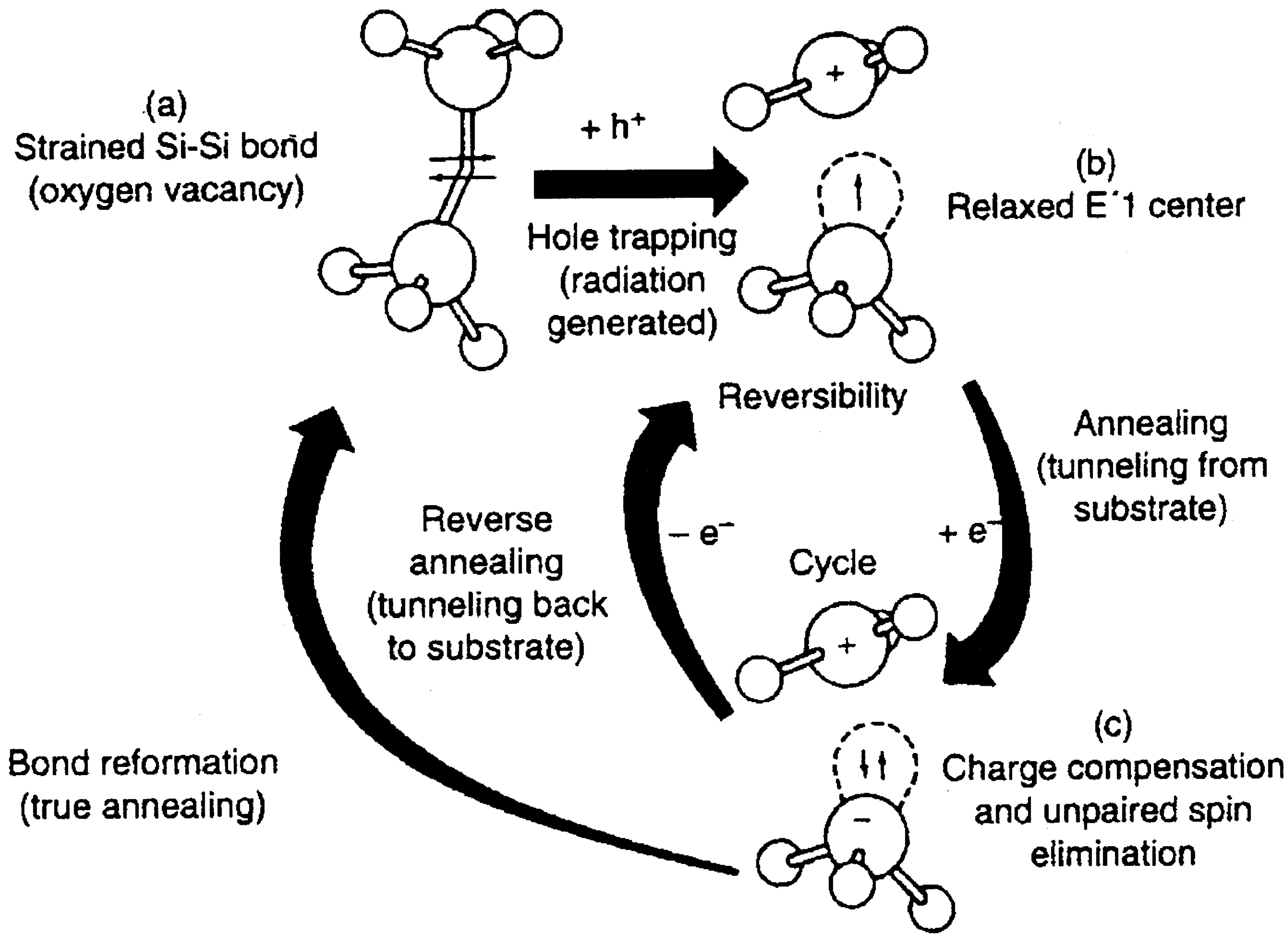
MOS Capacitor MOS-C





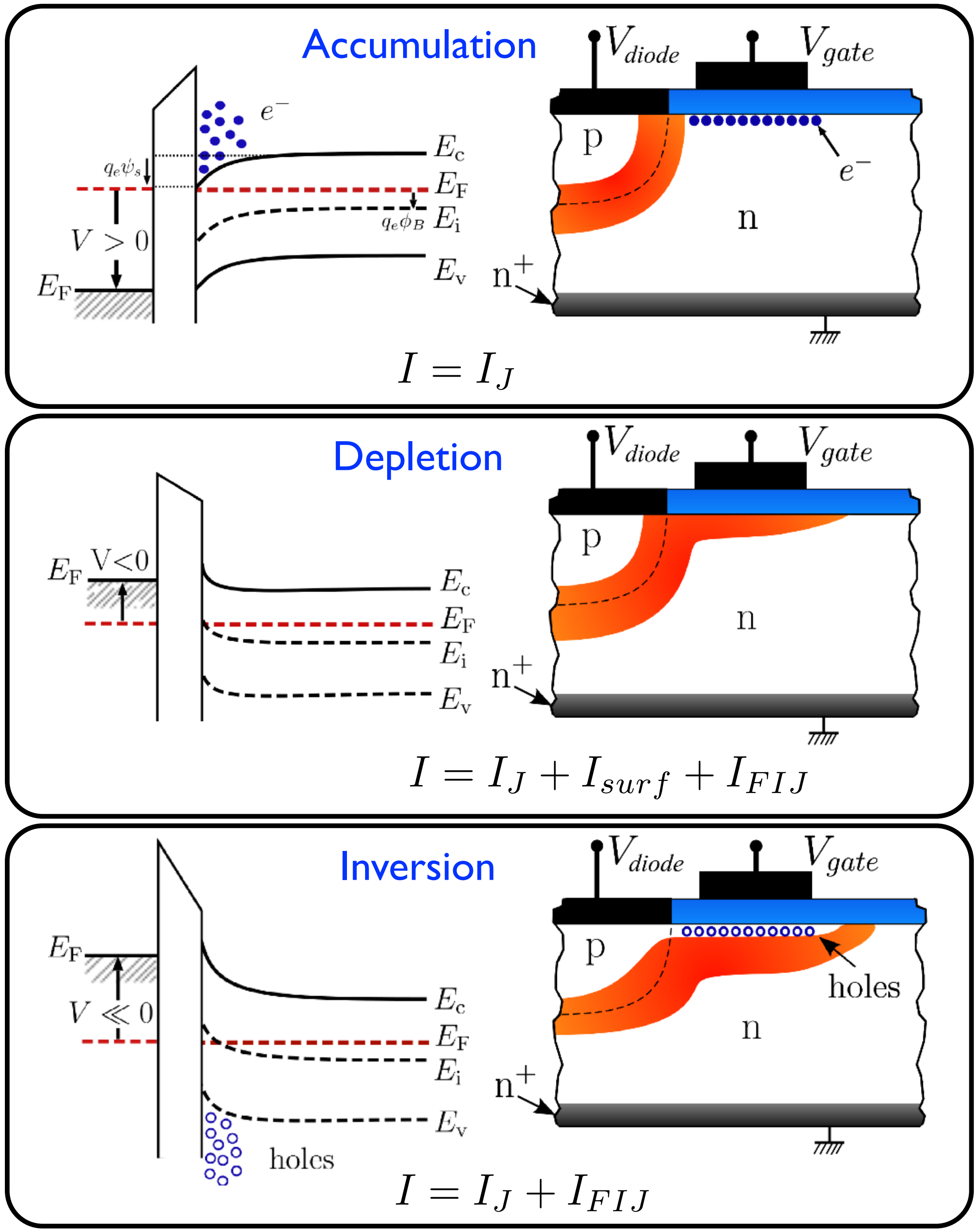
Parameterized by 3 states
interpretation not unambiguous !

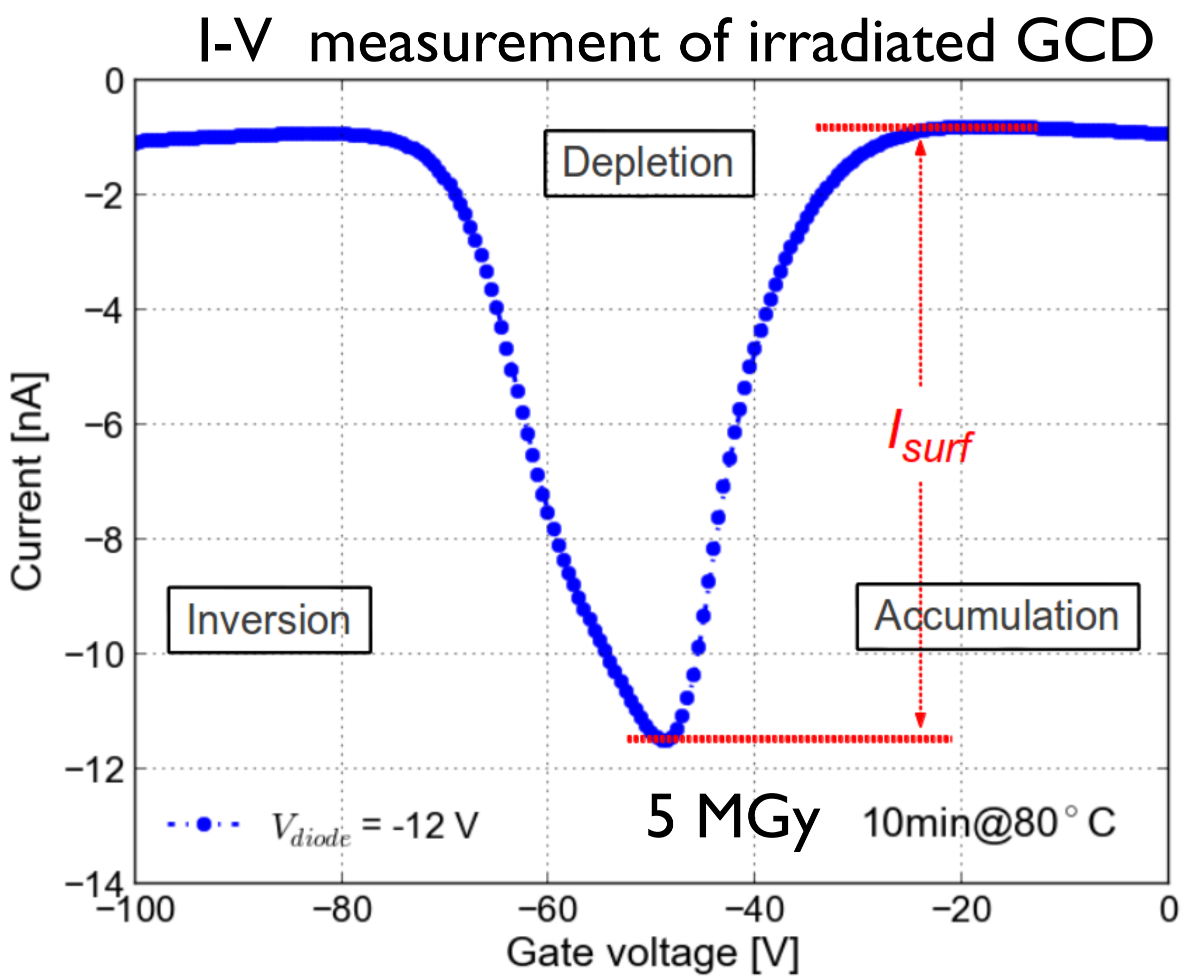
Border traps



X-Ray damage: J_{surf}

- Surface current density J_{surf} from GCD:
  - Measure I-V curve
  - J_{surf} dominated by mid-gap traps





- Comments on J_{surf} measurements:
  - For high J_{surf} voltage drop along surface
    - ➡ $Si\text{-}SiO_2$ interface only partially depleted
  - $Si\text{-}SiO_2$ interface states decrease of mobility
  - We do not take into account these effects
    - ➡ Measured I_{surf} = lower limit of surface current